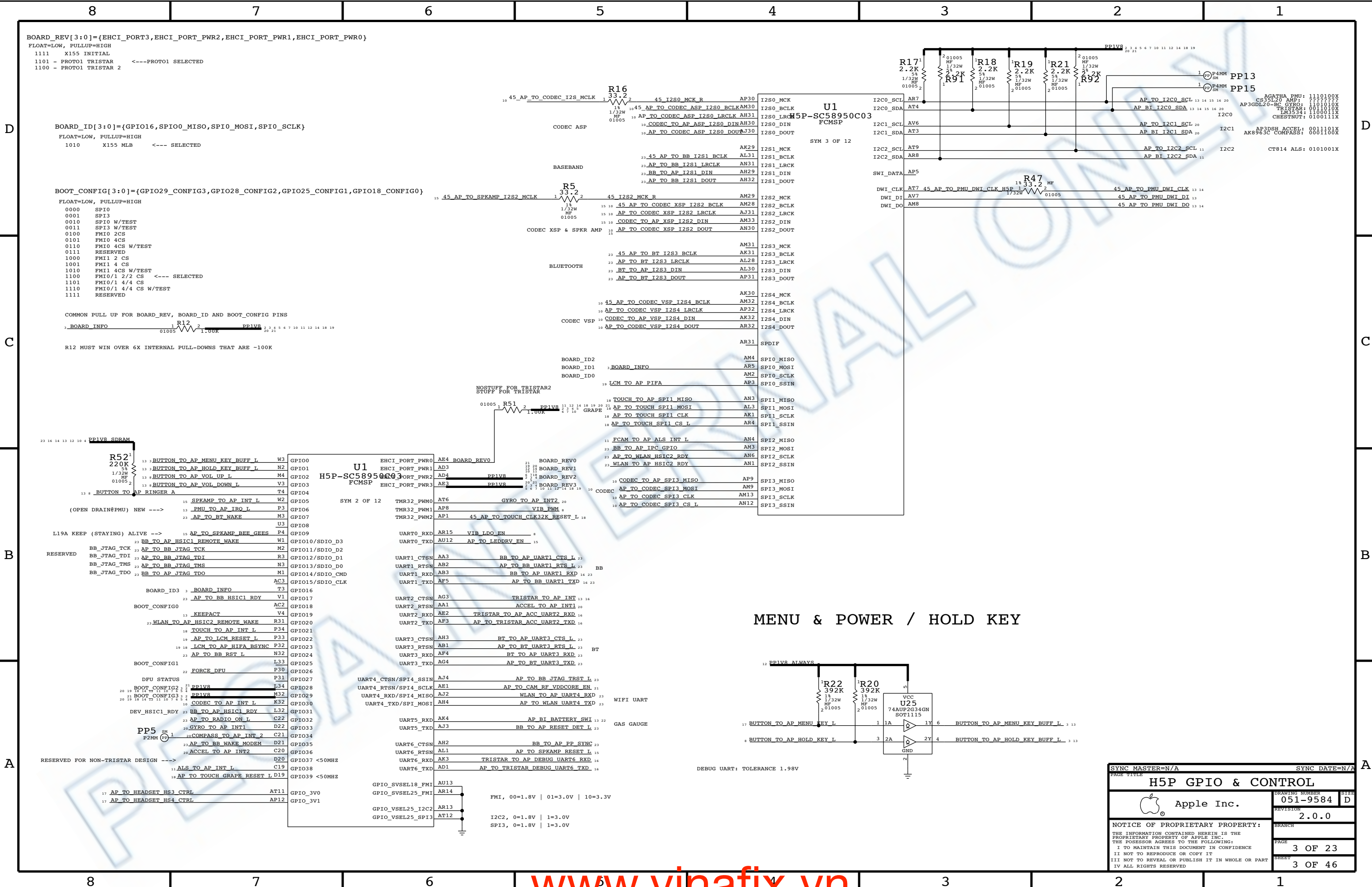


87654321

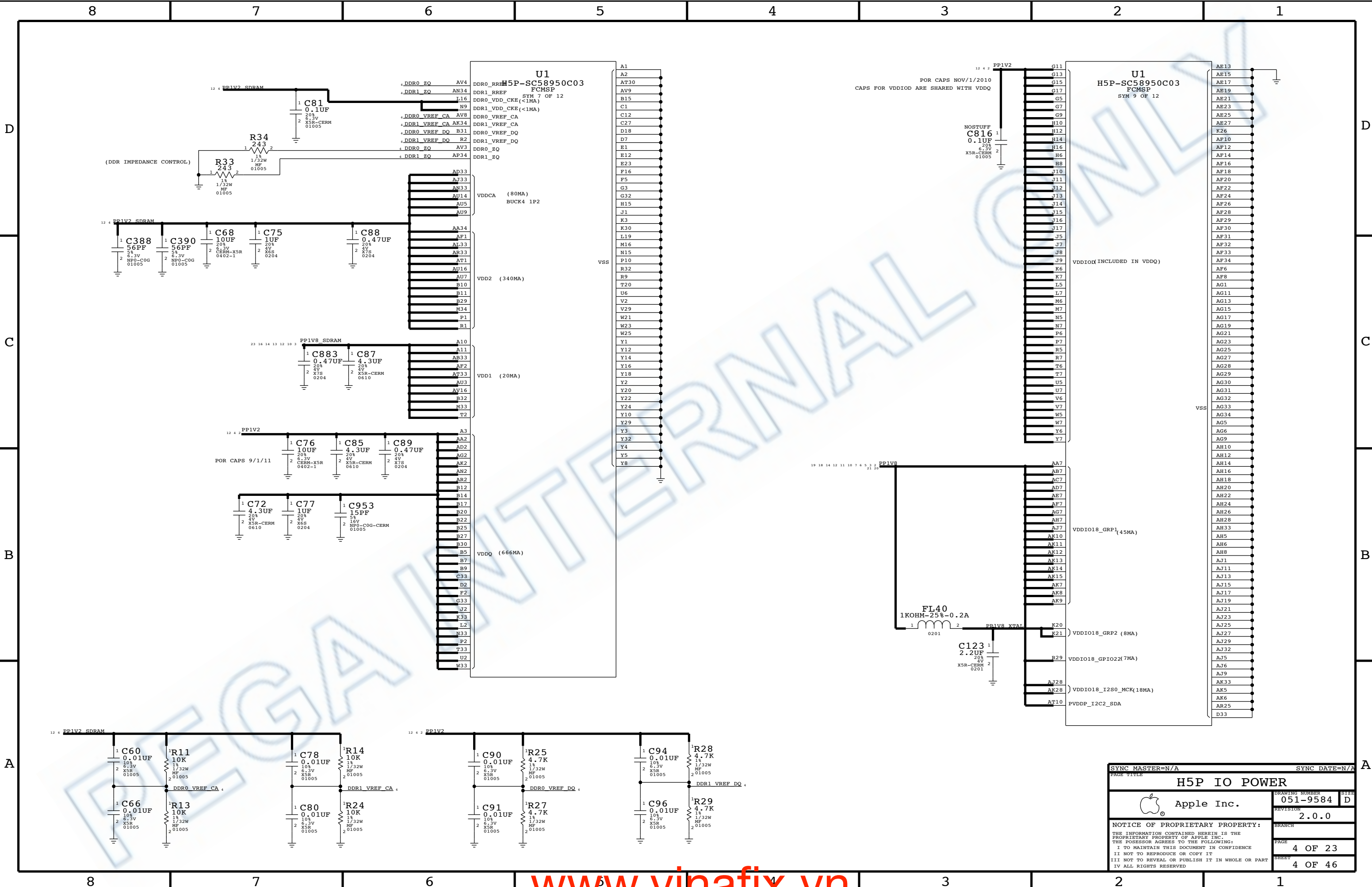
www.vinafix.vn

87654321

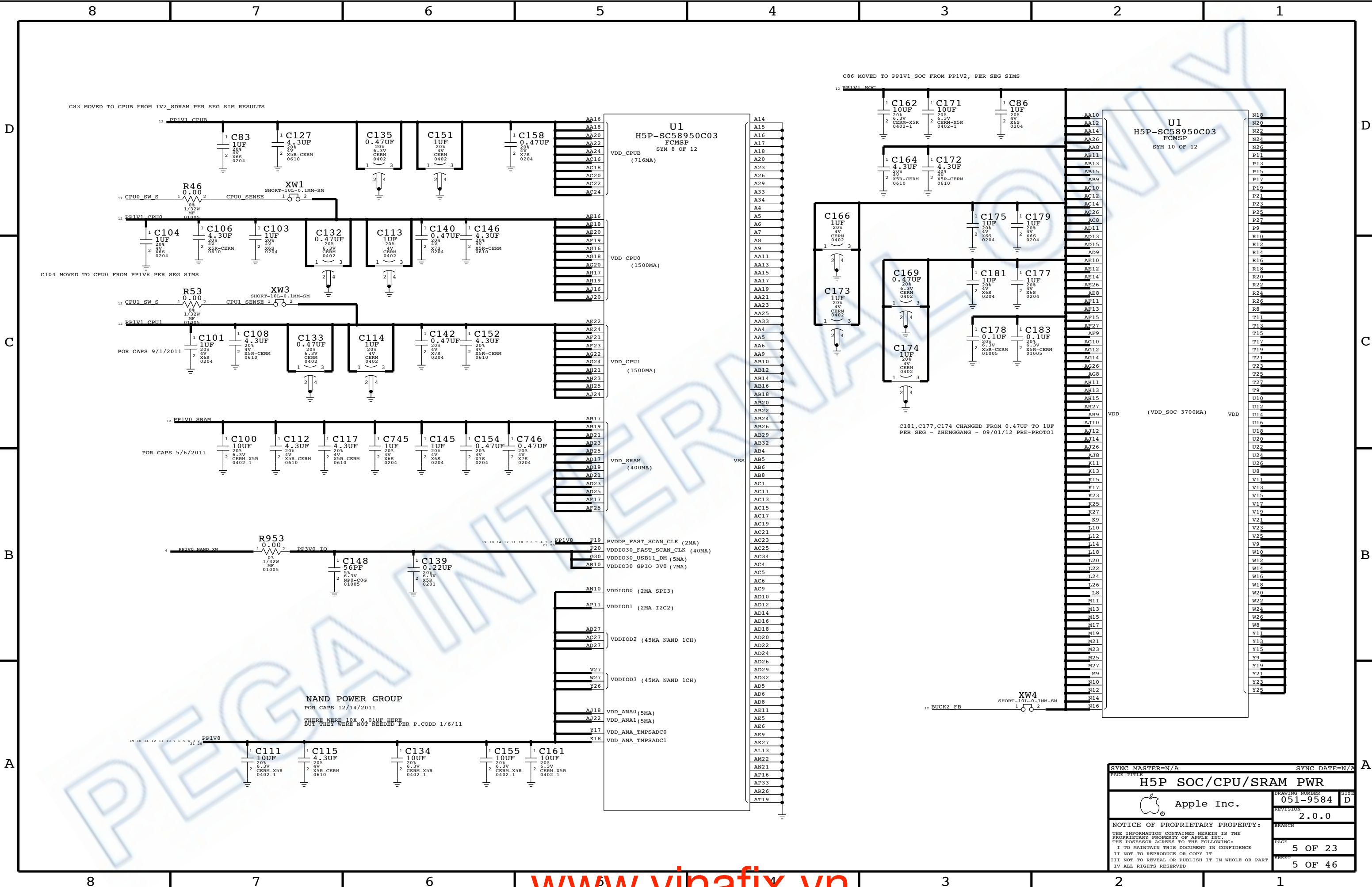




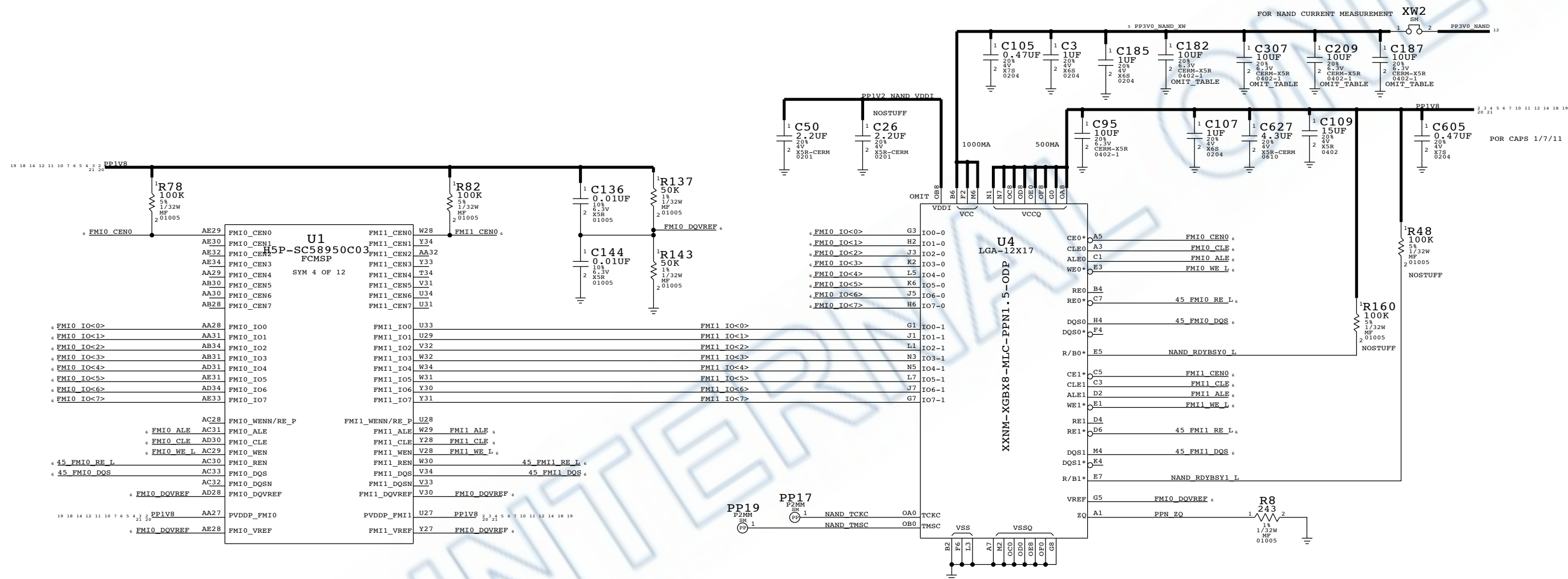
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PAGE TITLE		H5P GPIO & CONTROL	
Apple Inc.		DRAWING NUMBER	051-9584
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		4 OF 46	




NAND



PP2 P4MM 1 FMIO\_IO<0>  
PP3 P4MM 1 45\_FMIO\_RE\_L  
PP14 P4MM 1 45\_FMIO\_DQS

NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

SYNC MASTER=N/A		SYNC DATE=N/A	
PAGE TITLE			
H5P W/ NAND			
 Apple Inc.	DRAWING NUMBER	051-9584	SIZE D
	REVISION	2.0.0	
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8 7 6 5 4 3 2 1

# BUTTON FLEX (VIBE DRIVER, BUTTONS, ANC REF MIC, STROBE, STROBE\_NTC)

WIFI FLEX PAC:  
VDD (3.0V)

BUTTONS:  
RINGER, HOLD,  
VOL\_UP/DOWN

VIBE DRIVE

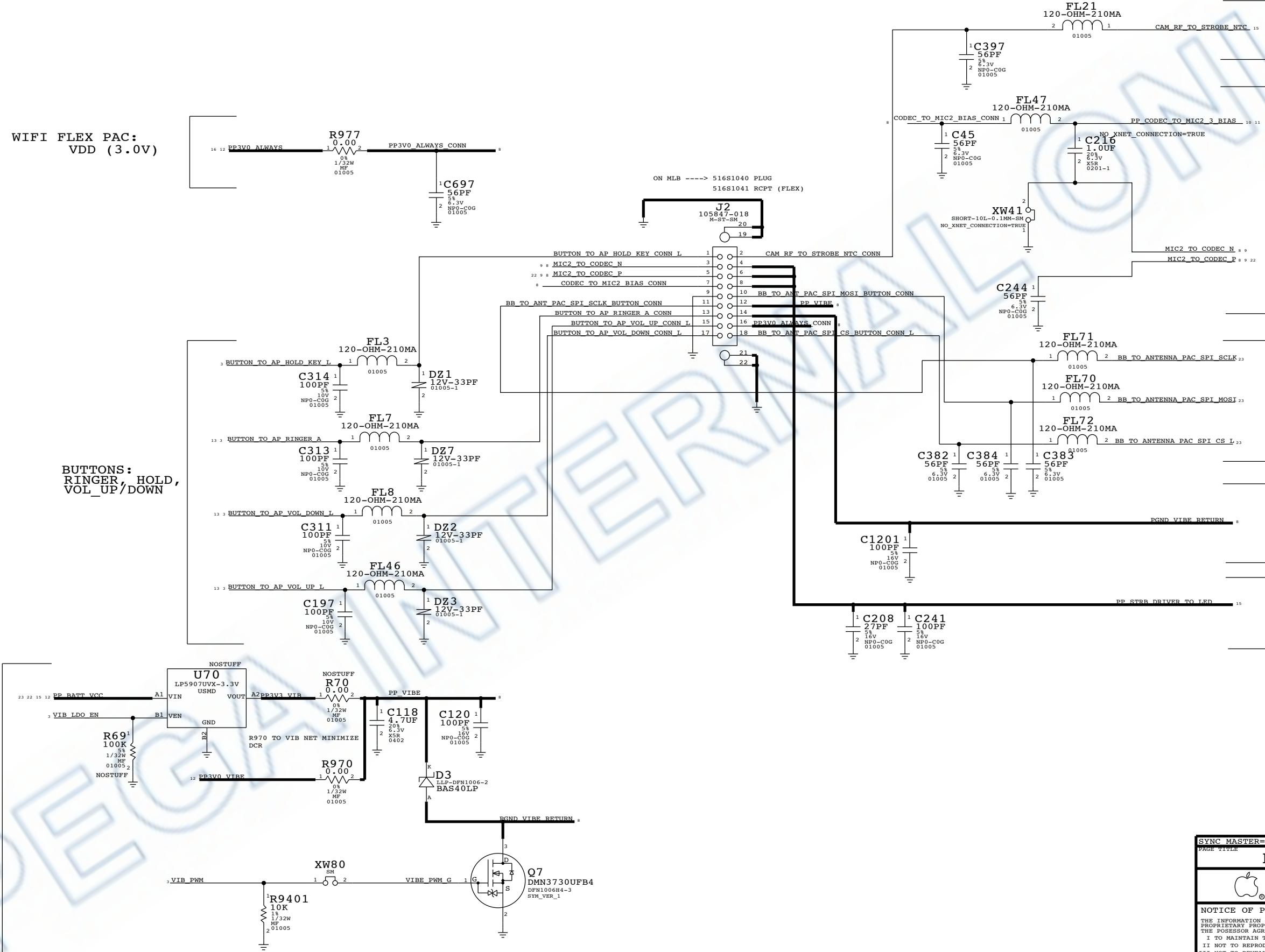
STROBE:  
STROBE NTC

MIC2 (ANC REF MIC):  
MIC2/3 BIAS,  
MIC2\_P,\_N

WIFI FLEX PAC:  
PAC SPI BUS

VIBE RETURN

STROBE:  
LED COOL



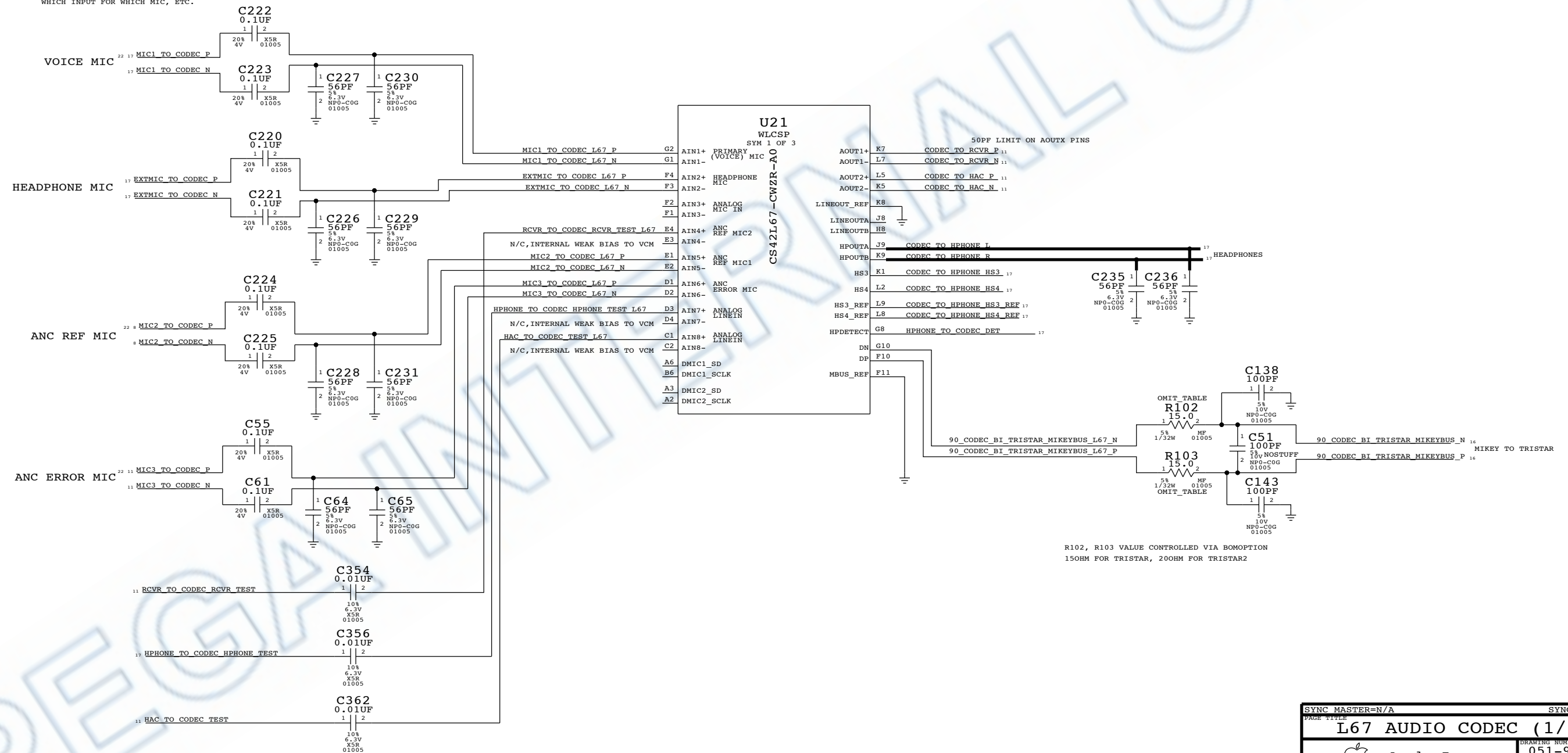
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REVISION		2.0.0		D	
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				8 OF 23	
				8 OF 46	

# L67 AUDIO CODEC

## AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)

TO DO: REVIEW AUDIO MIC PATHS,  
WHICH BIAS OUTPUT USED FOR WHICH MIC,  
WHICH INPUT FOR WHICH MIC, ETC.



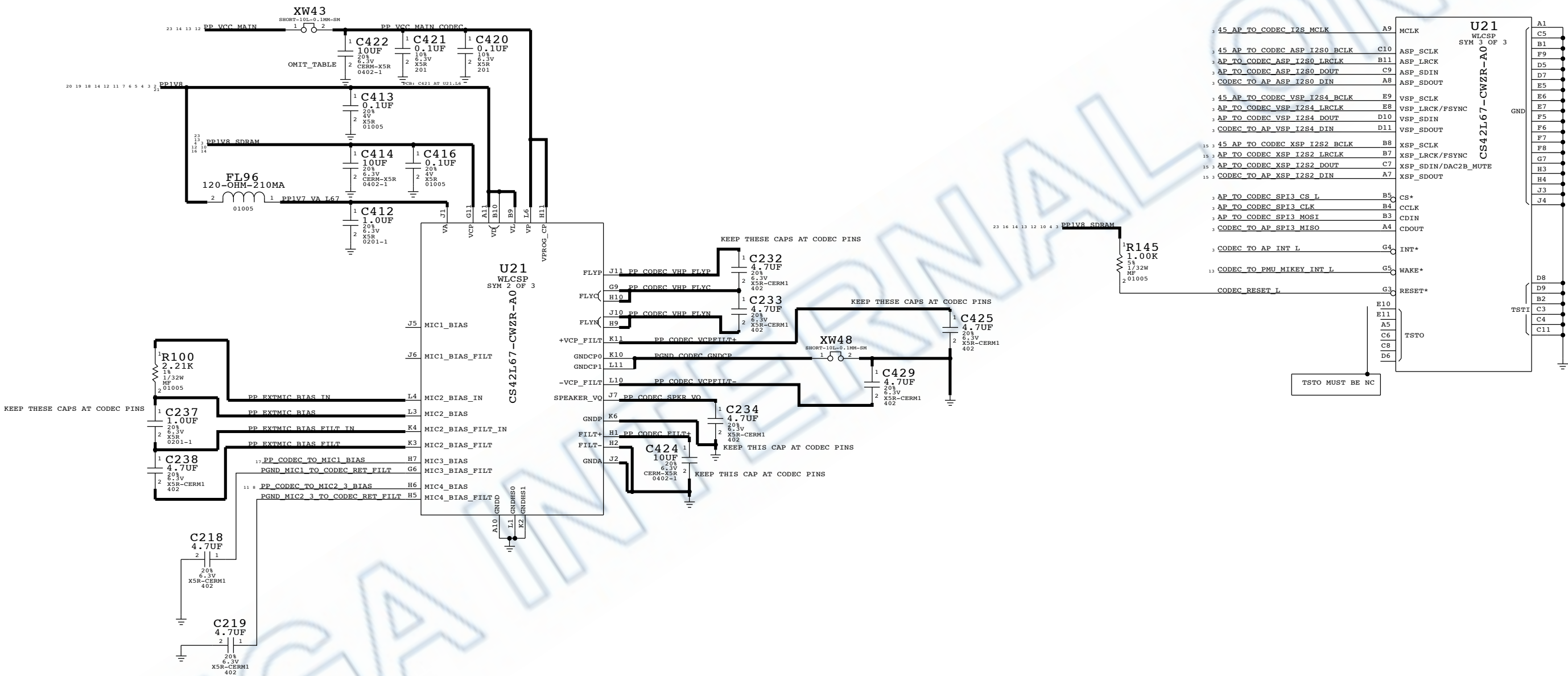
R102, R103 VALUE CONTROLLED VIA BOMOPTION  
150HM FOR TRISTAR, 200HM FOR TRISTAR2

PAGE TITLE		SYNC MASTER=N/A		SYNC DATE=N/A	
L67 AUDIO CODEC (1/2)		DRAWING NUMBER		SIZE	
Apple Inc.		051-9584		D	
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PROPRIETARY PROPERTY OF APPLE INC.		PAGE		9 OF 23	
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# L67 AUDIO CODEC

## POWER, MICBIAS

## DIGITAL SYSTEM I/O



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L67 AUDIO CODEC (2/2)		DRAWING NUMBER	
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		10 OF 46	

## CG FLEX B2B

(FF CAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

D

FRONT CAM:  
CLK, I2C, SHDN

C

PROX: POWER,  
RX, RX\_EN

B

ALS: POWER,  
I2C, INT

A

RECEIVER

D

C

B

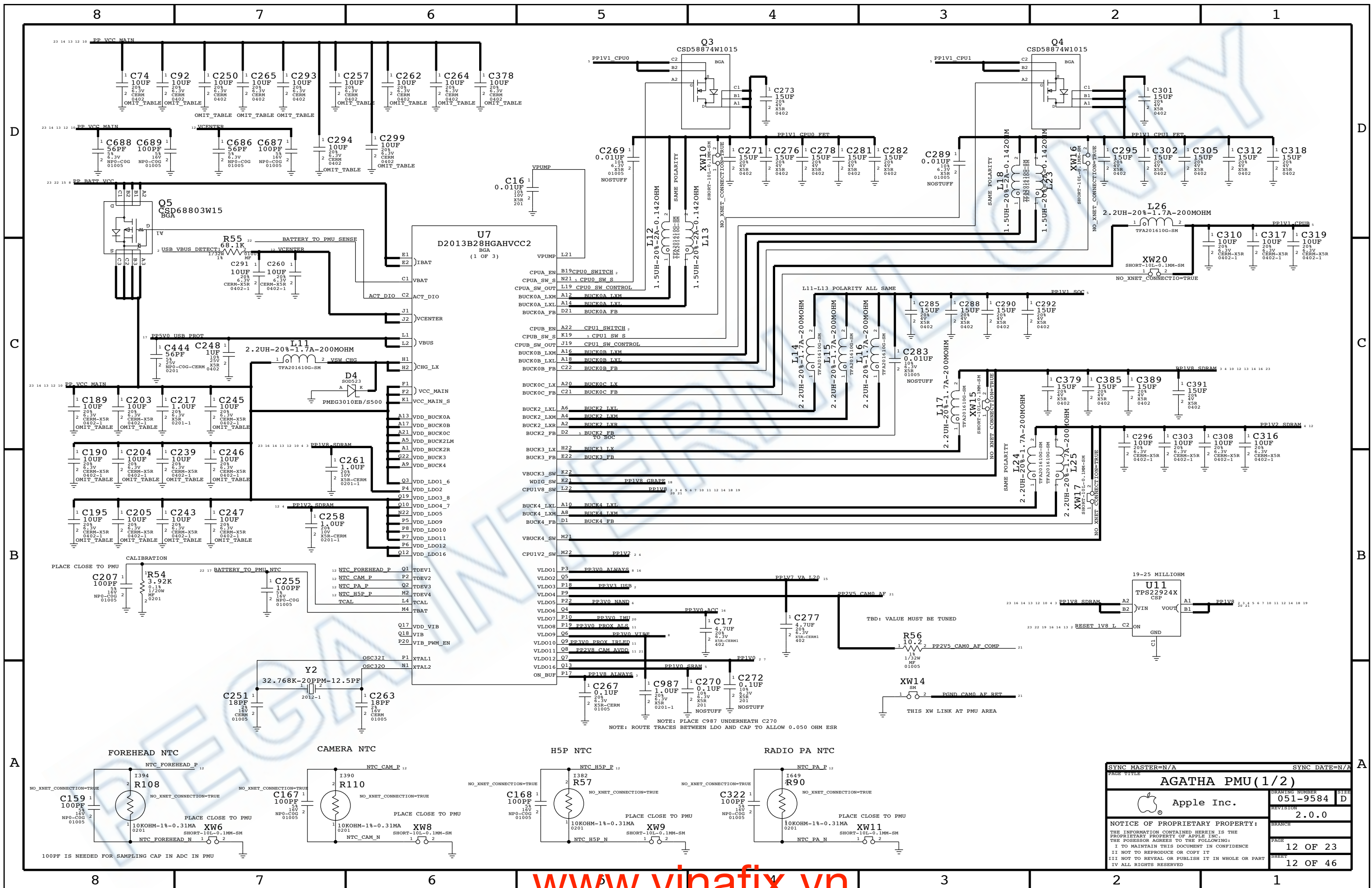
A

FRONT CAM:  
POWER AND MIPIMIC3  
(ANC ERROR MIC)

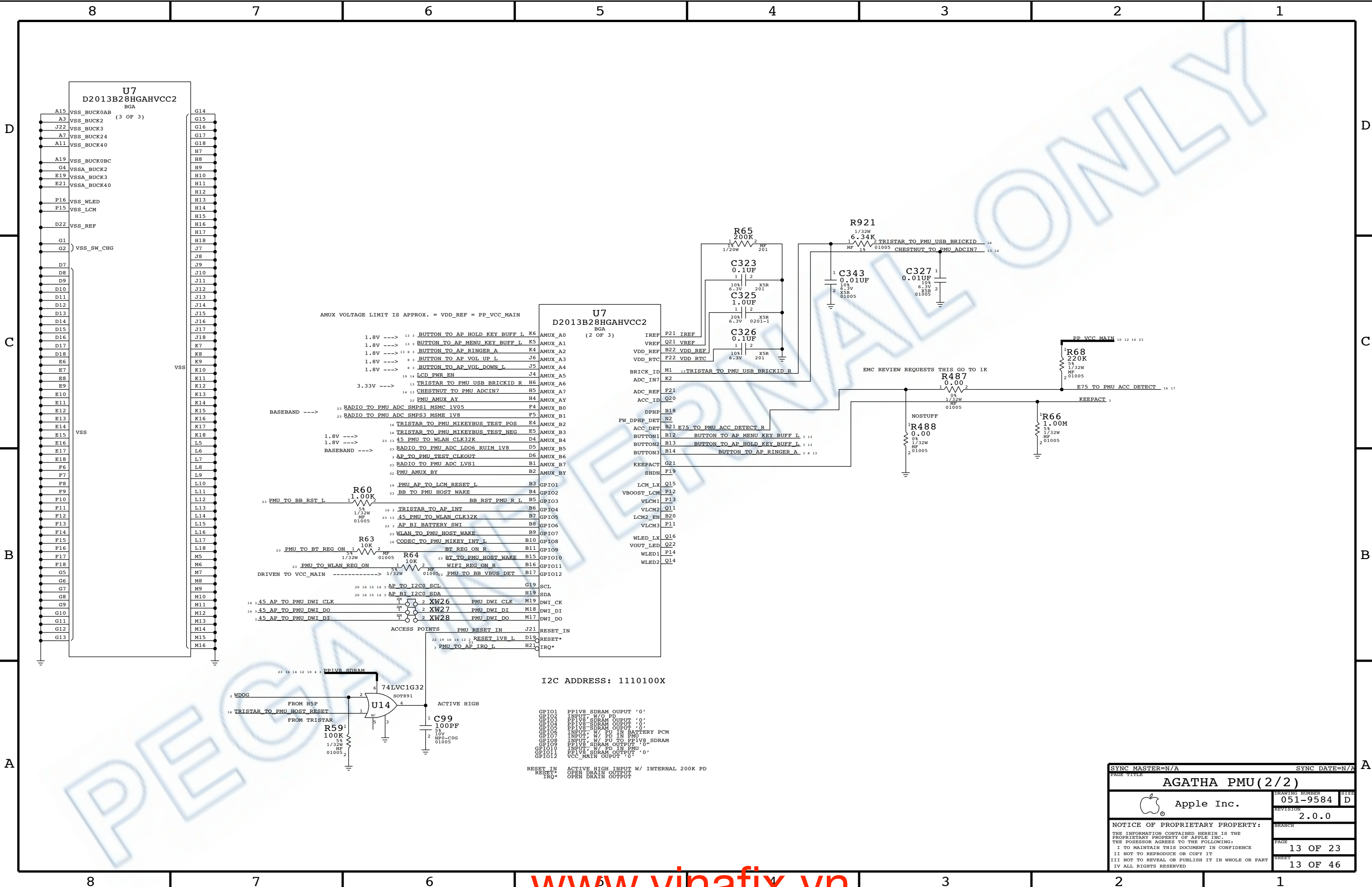
CAM1 ALS INT

PROX: PWR, TX EN

PAGE TITLE		PAGE NUMBER		PAGE	
CG FLEX B2B		051-9584		D	
Apple Inc.		REVISION		2.0.0	
NOTICE OF PROPRIETARY PROPERTY:		BRANCH		11 OF 23	
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REVISION		2.0.0		BRANCH	
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## D



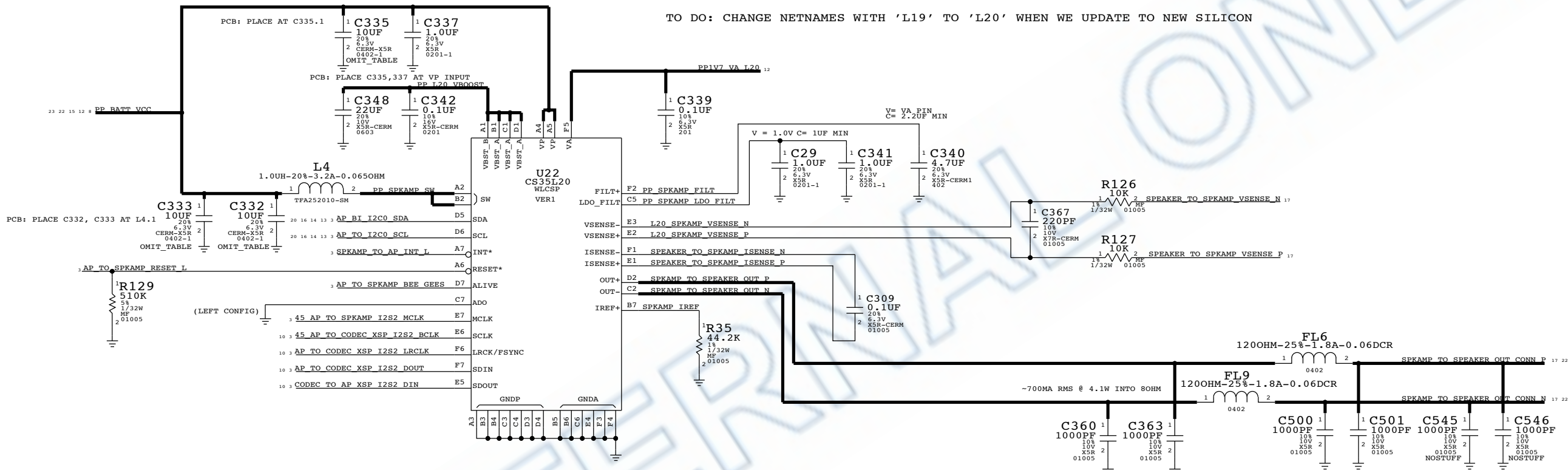
## B

D

# SPEAKER AMP, LED DRIVER

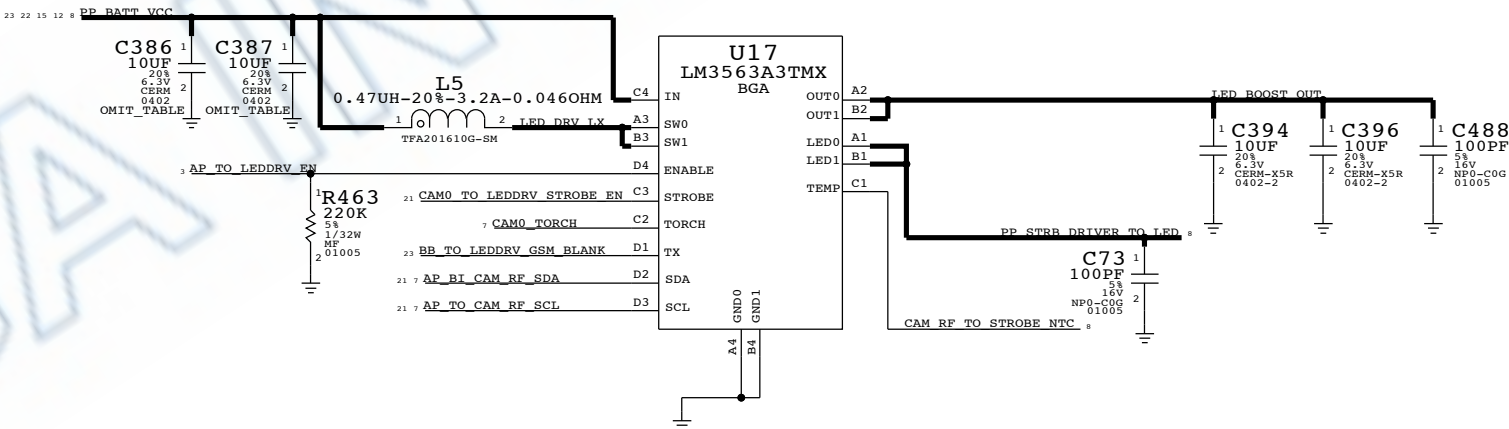
## SPEAKER AMP (REPLACED WITH L20)

I2C ADDRESS: 1000000X



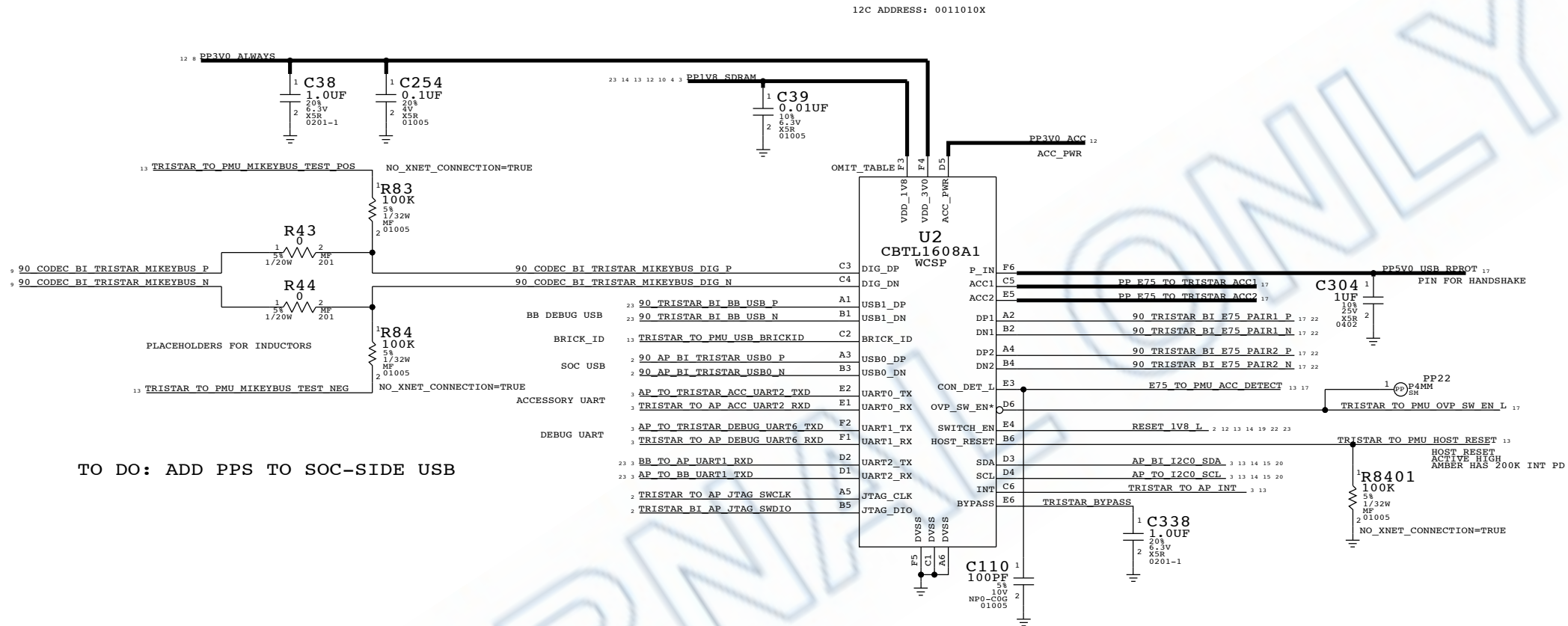
## LED DRIVER


I2C ADDRESS: 1100011X



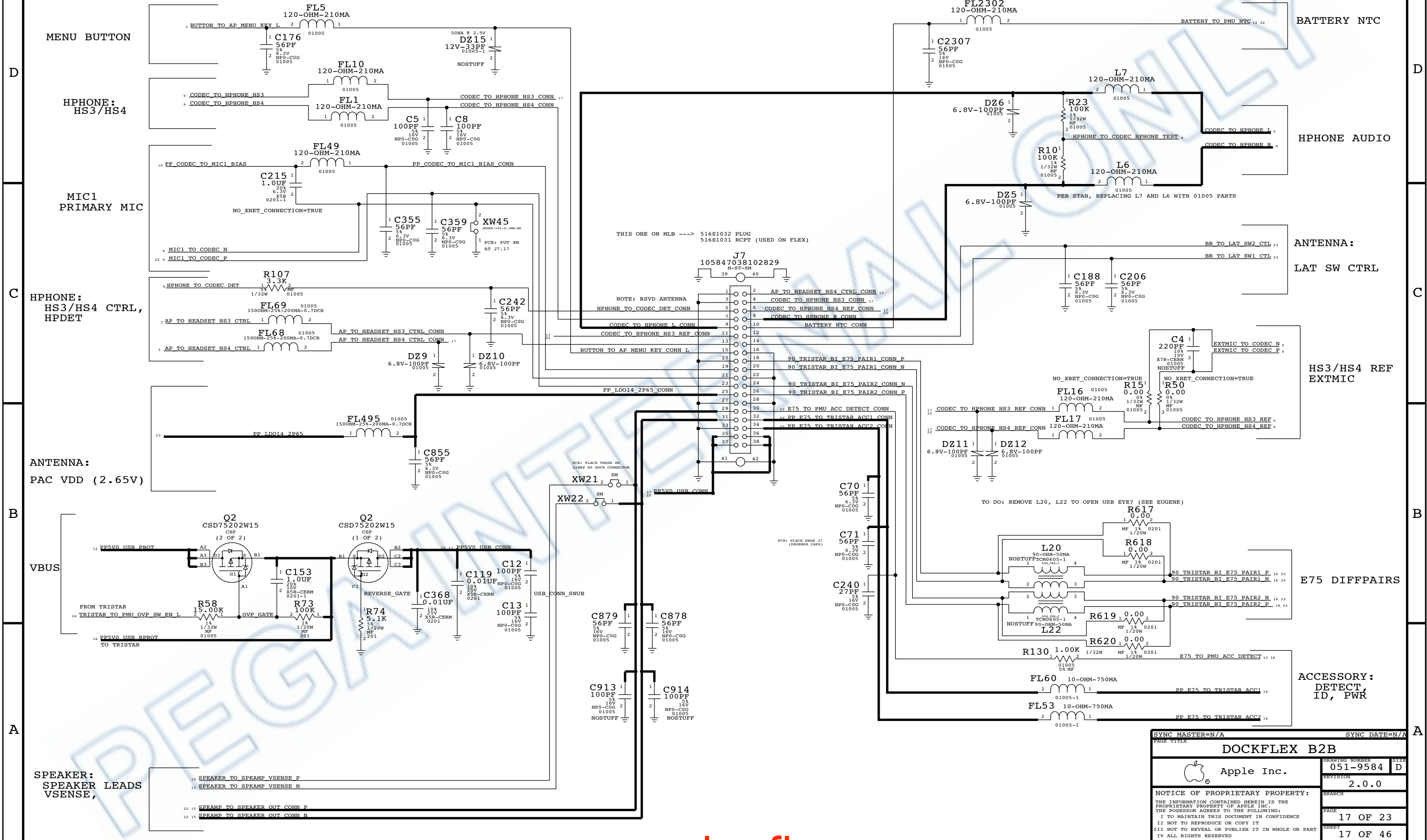
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SPKR AMP + LED DRIVER		DRAWING NUMBER		051-9584	
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TRISTAR



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# DOCKFLEX B2B (USB VBUS, MENU BTN, SPEAKER, HP, HP EXTMIC, NAVAJO, ANTENNA PAC/LAT SW CTRL, MIC1 (VOICE MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)



SYNC MASTER=N/A		SYNC DATE=N/A	
PAGE TITLE			
DOCKFLEX B2B			
 Apple Inc.		DRAWING NUMBER	051-9584
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		REVISION	2.0.0
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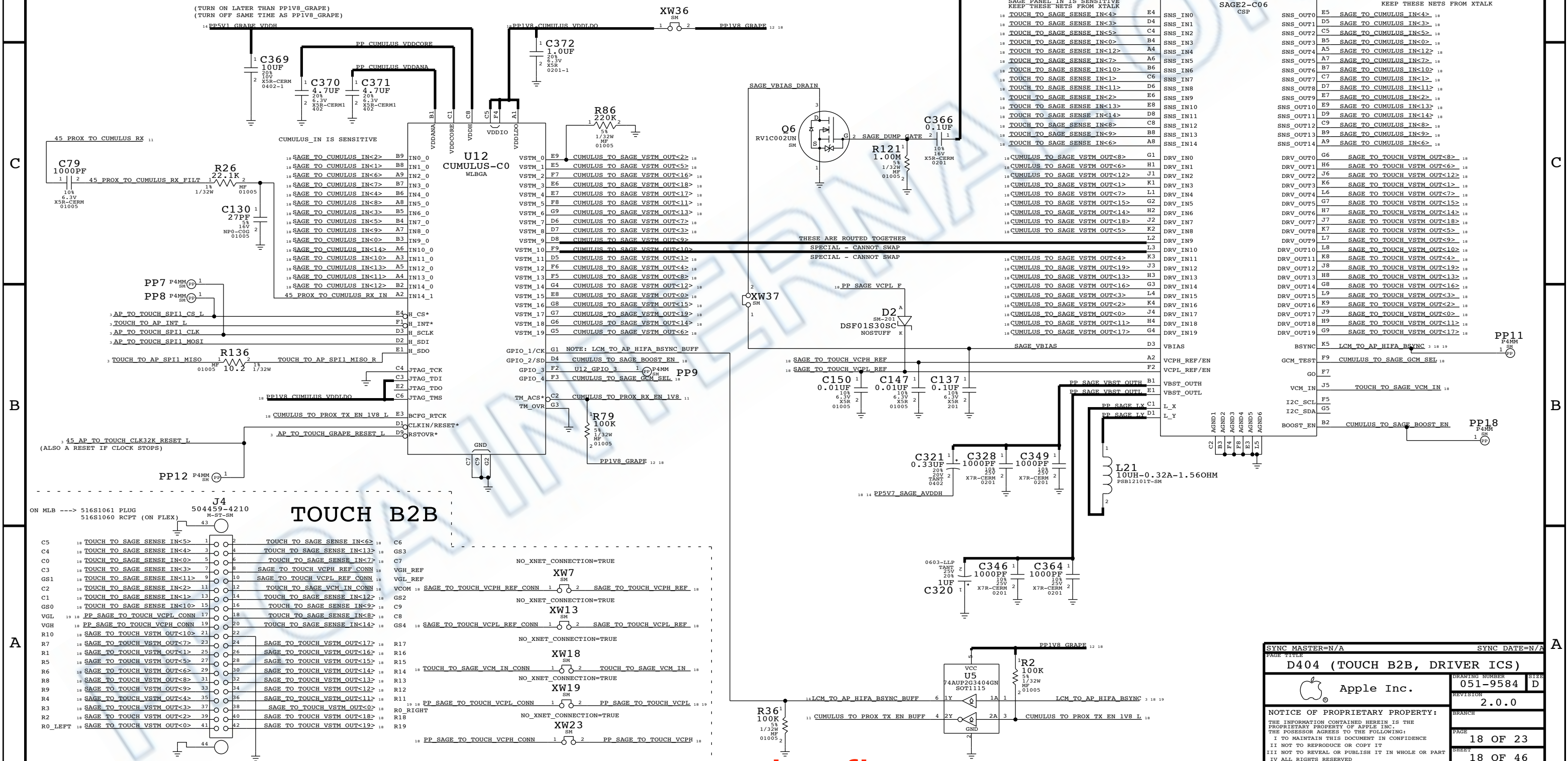
# D404 (B2B, DRIVER ICS)

SAGE2 C0

343S0628: B0 APN FOR PROTO1  
343S0645: C0 APN FOR EVT1

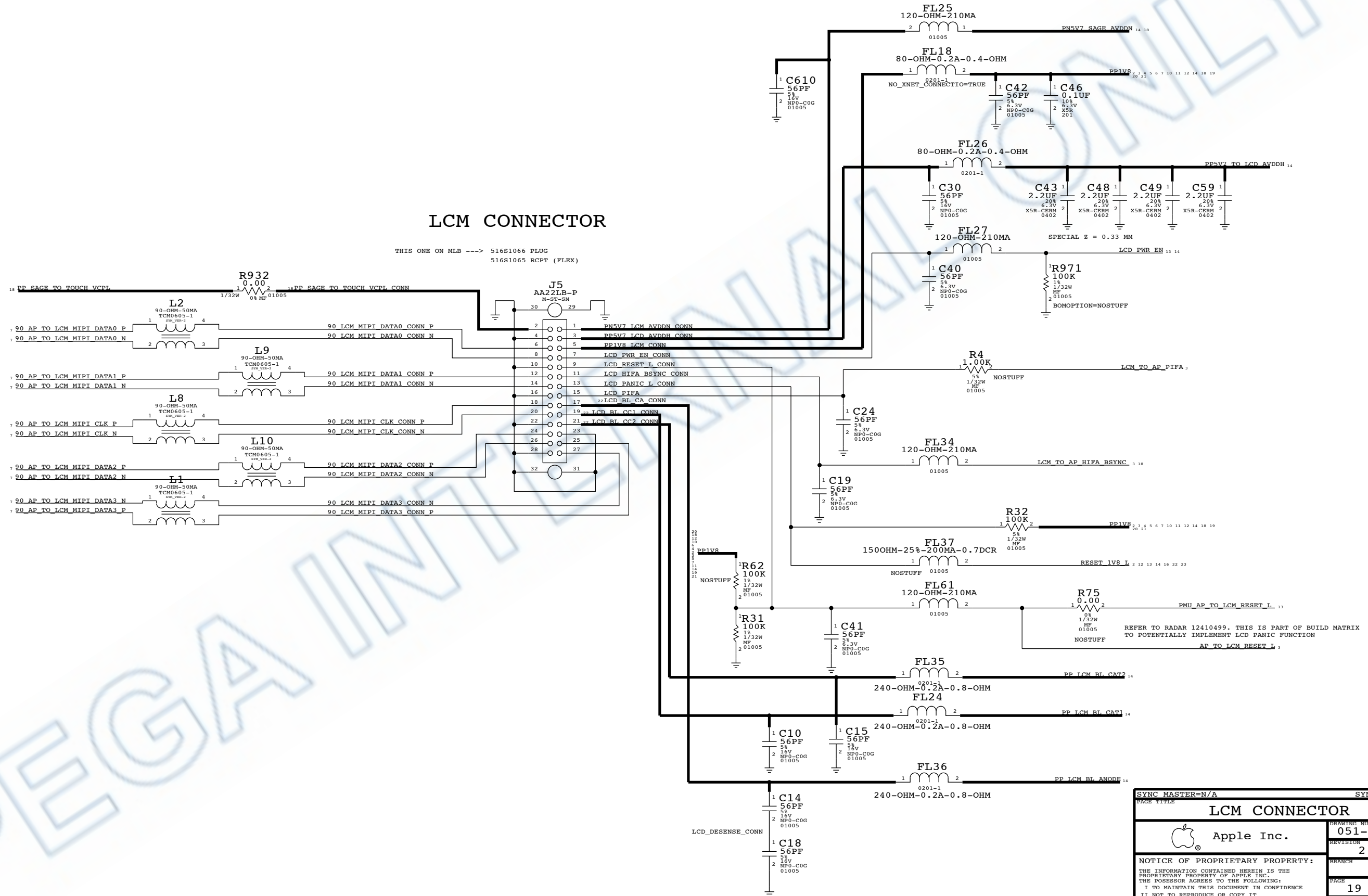
CUMULUS C0


343S0574



# LCM B2B

## LCM CONNECTOR



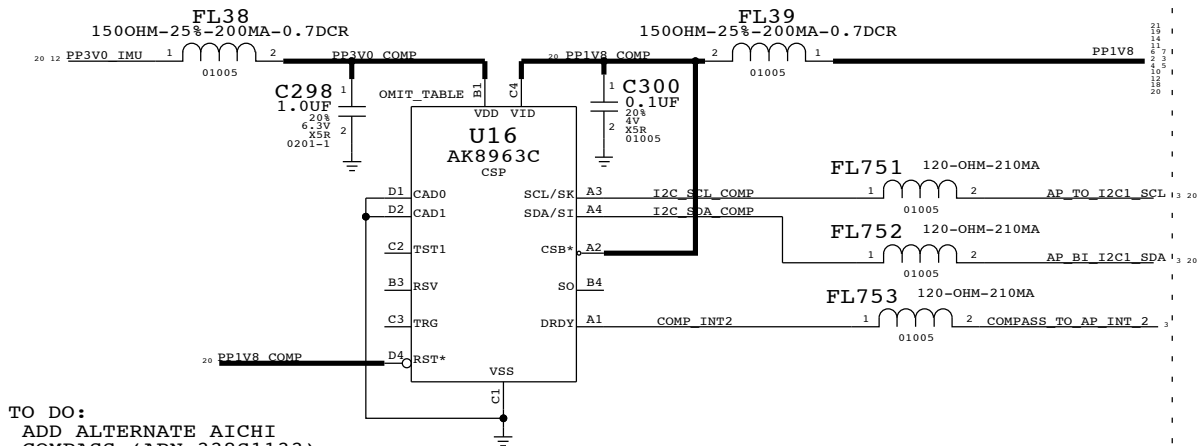
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LCM CONNECTOR			
 Apple Inc.	DRAWING NUMBER	051-9584	SIZE D
	REVISION	2.0.0	
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SENSORS

THIS PART OUTSIDE OF SHIELD

COMPASS

COMPASS DEVICE: 338S1014  
COMPASS INTERPOSER: 998-5120

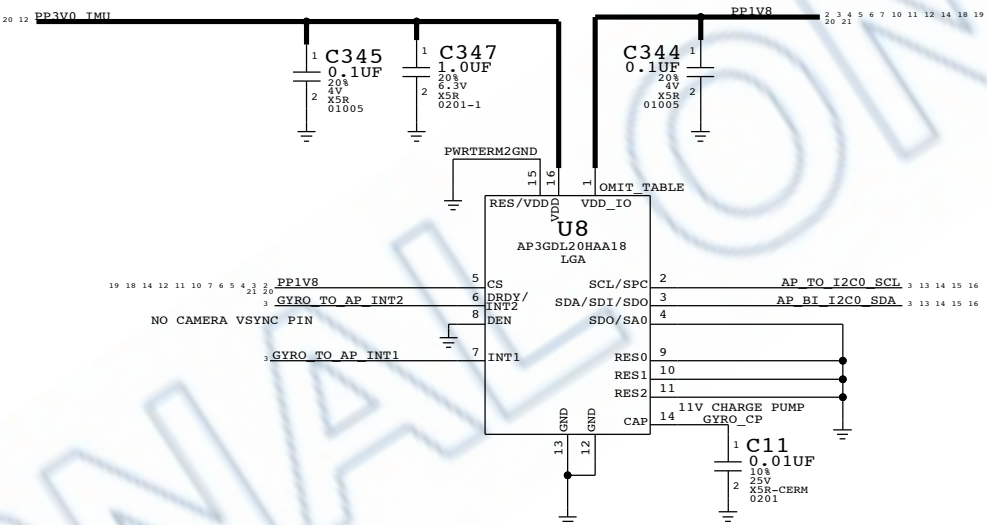


TO DO:  
ADD ALTERNATE AICHI  
COMPASS (APN 338S1133)

THESE PARTS INSIDE OF SHIELD

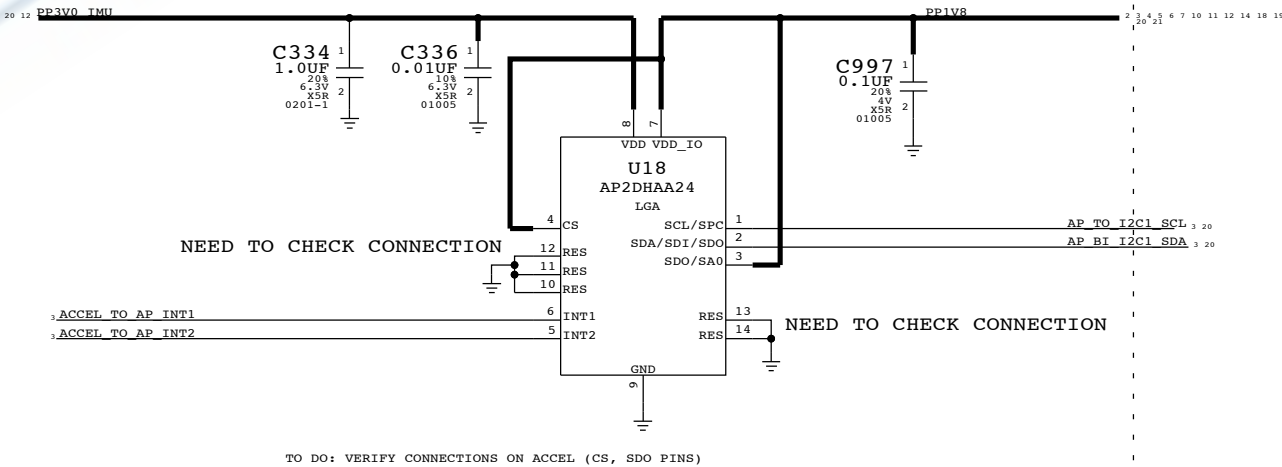
GYRO

AP3GDL20H, APN 338S1158



ACCELEROMETER


AP2DHAA, APN 338S1114



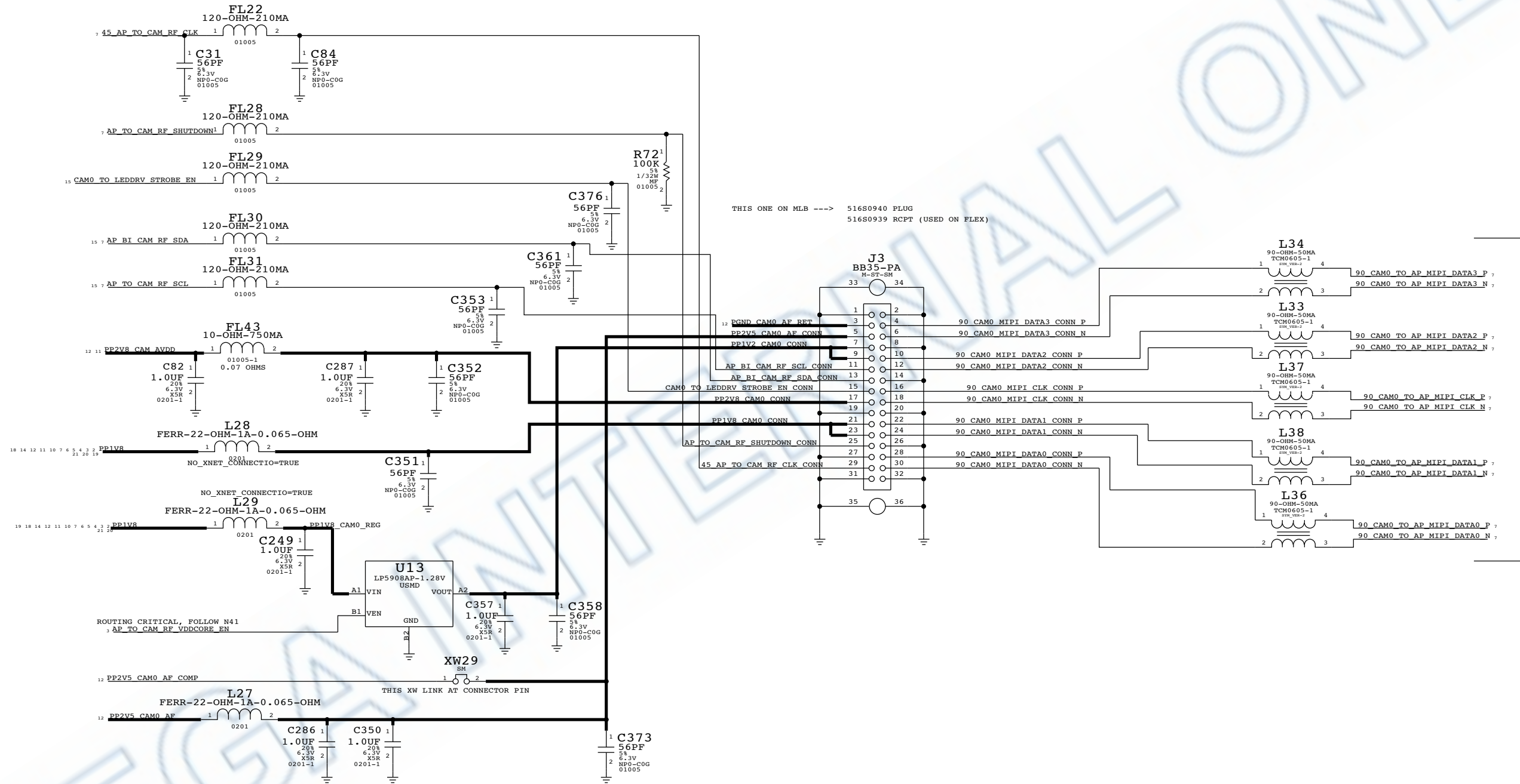
NEED TO CHECK CONNECTION

NEED TO CHECK CONNECTION


TO DO: VERIFY CONNECTIONS ON ACCEL (CS, SDO PINS)

SYNC MASTER=N/A		SYNC DATE=N/A	
PAGE TITLE			
OSCAR + SENSORS			
 Apple Inc.		DRAWING NUMBER	051-9584
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CAM0: MAIN CAMERA CONNECTOR

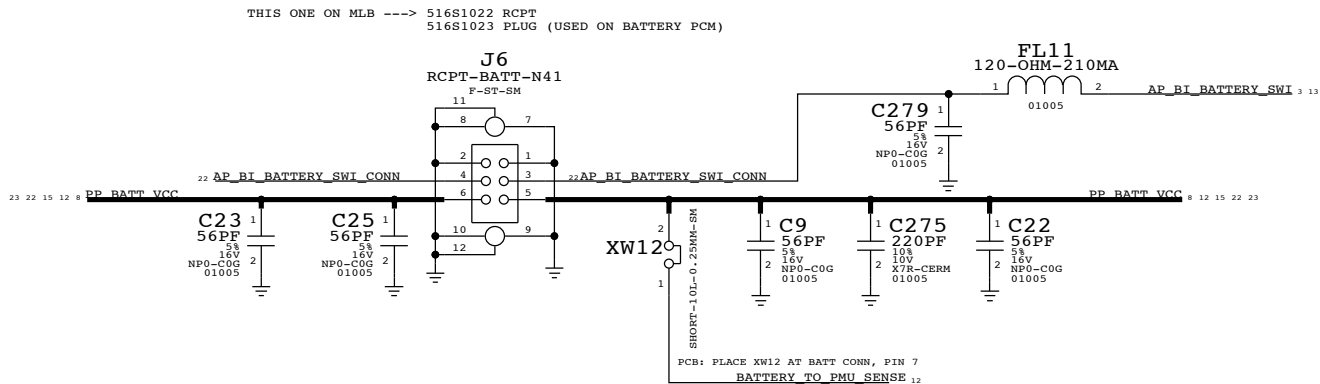


CAM0:  
4-LANE MIPI

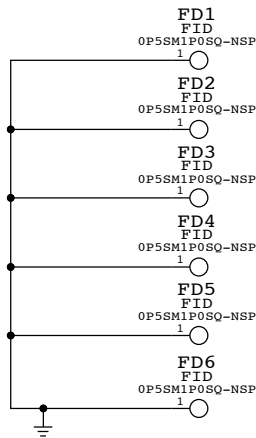
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PAGE TITLE			
CAM0 CONNECTOR			
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		21 OF 23	
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BATT CONN, TPS, STANDOFFS/SHIELDS/FIDUCIALS

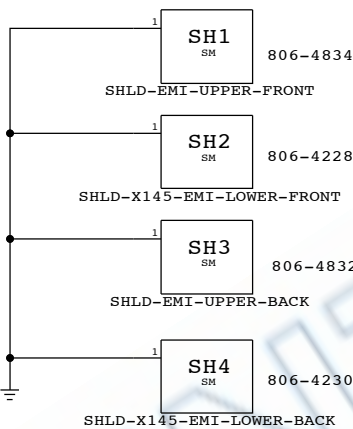
BATTERY CONN



FIDUCIALS

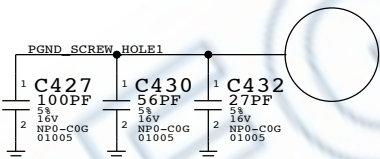


SHIELDS

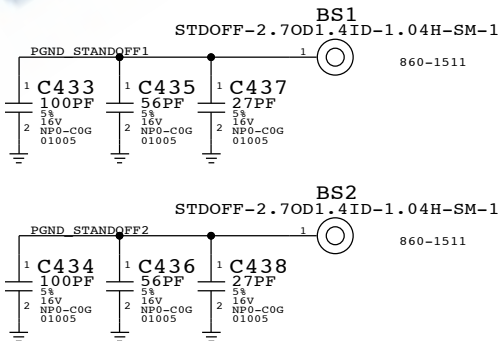


AC COUPLED SCREW HOLES + STANDOFFS  
(ON NORTH END OF SINGLE\_BRD, TO MITIGATE COMPASS RETURN CURRENTS)

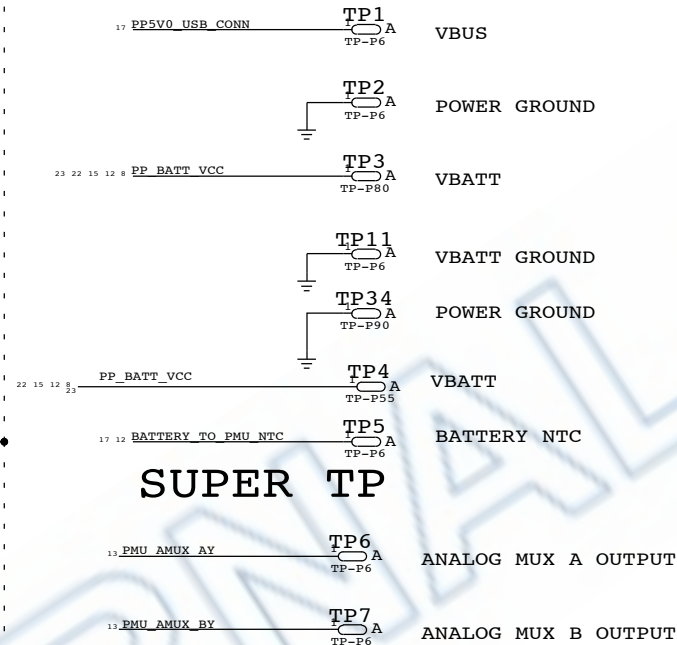
SCREW HOLES



STANDOFFS



POWER TP

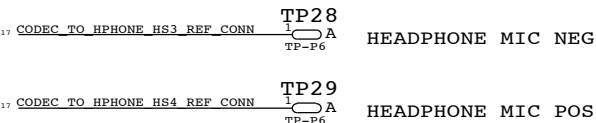


SUPER TP

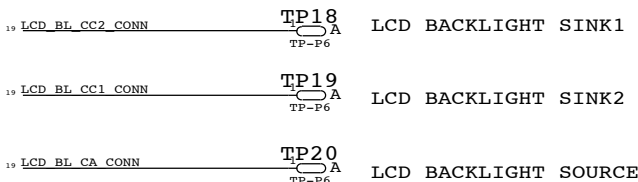
RESET

DFU

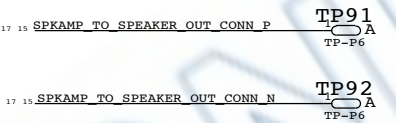
HEADPHONE MIC



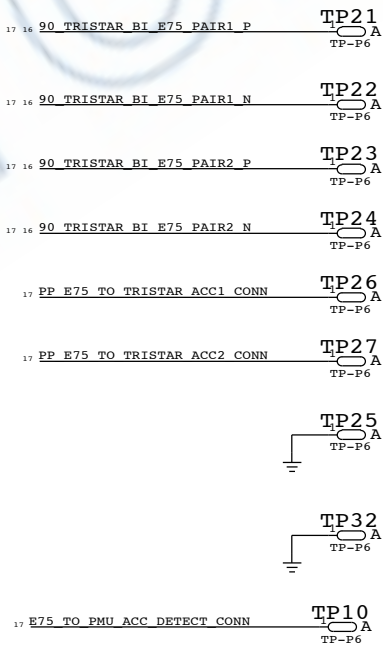
LCM BACKLIGHT



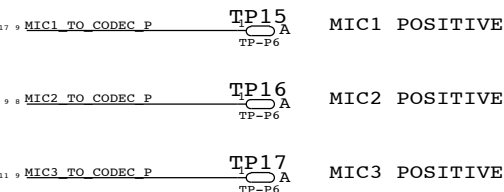
TESTPOINTS  
SPKAMP OUTPUT TP




E75 - USB/UART/ID/POWER



MIC AUDIO



DRIVE MIC WRT NEAREST GROUND TEST POINT

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PAGE TITLE			
BATT B2B, TPS, PD FEATURES			
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		SHEET	22 OF 46

# RADIO\_MLB HIERARCHICAL SYMBOL

## AP/RADIO INTERFACE



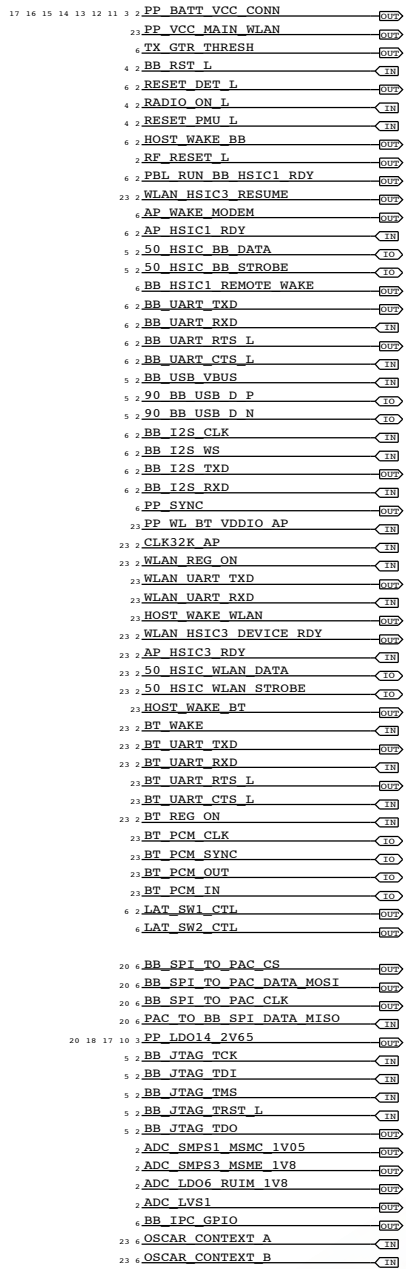
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RADIO_MLB_HIERARCH. SYMBOL			
DRAWING NUMBER		SIZE	
051-9584		D	
REVISION		2.0.0	
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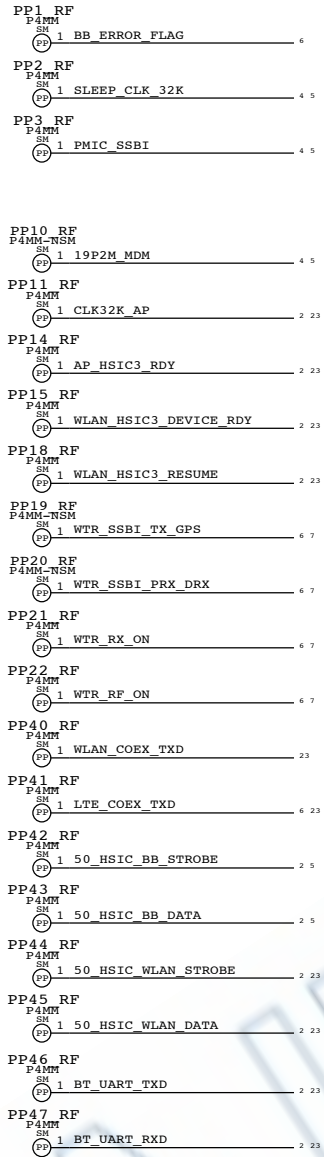
# AP INTERFACE & DEBUG CONNECTORS

## AP CONNECTIONS

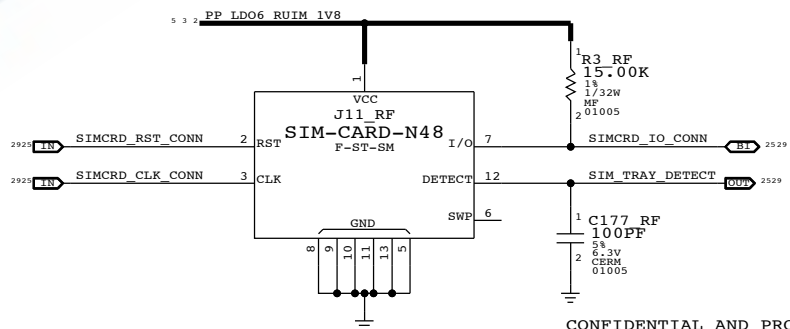
IN = FROM AP  
OUT = TO AP



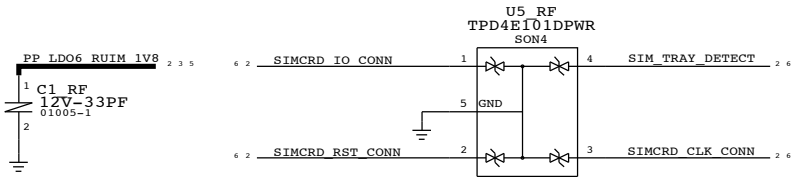
## PROBE POINTS



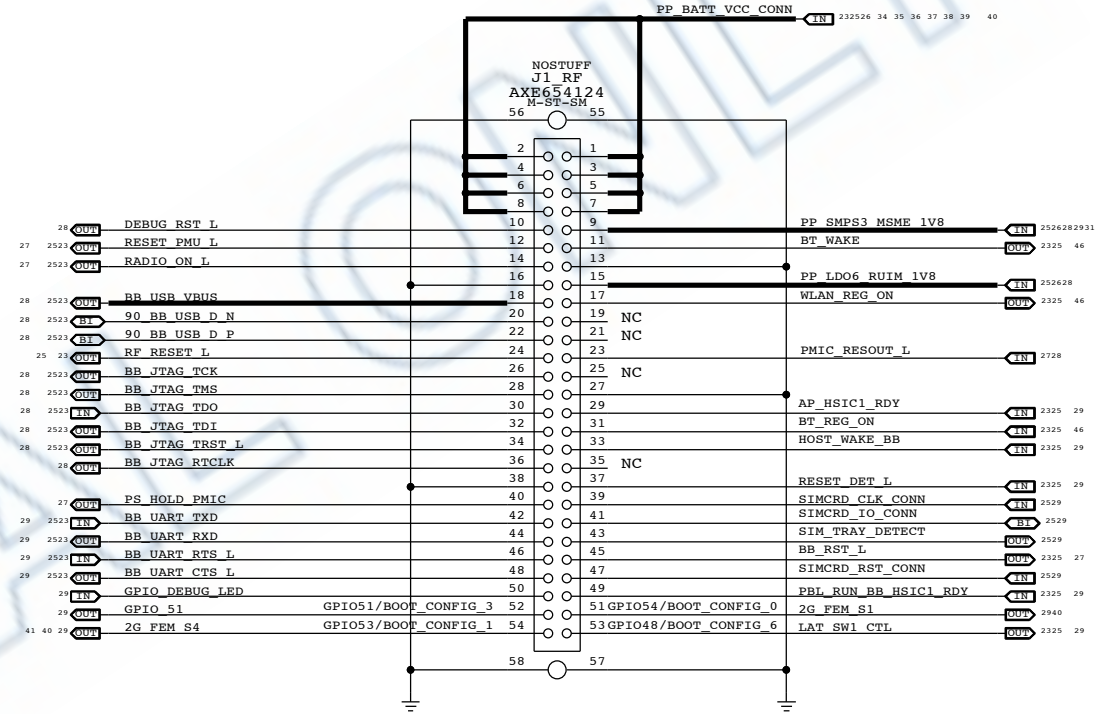
## SIM CARD CONNECTOR



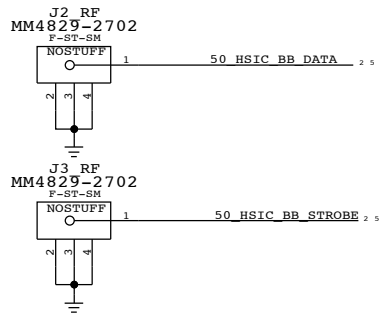
## SIM CARD ESD PROTECTION



## DEBUG CONNECTOR

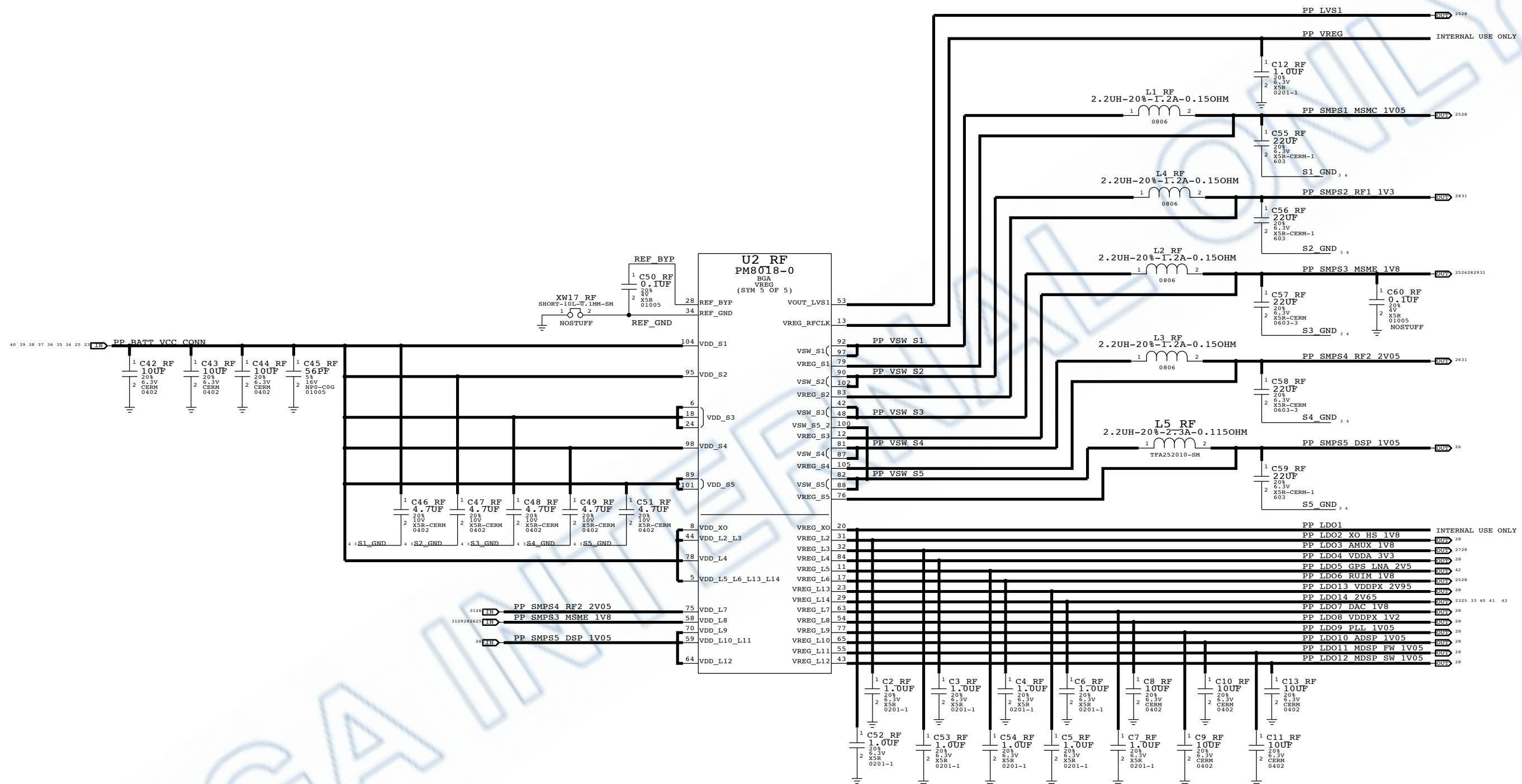


BOOT OPTIONS	BOOT_CONFIG SW REGISTER VALUE	GPIO/BOOT_CONFIG CONFIGURATION									
		6	5	4	3	2	1	0			
BOOT_DEFAULT_OPTION	0X00	X	0	0	0	0	0	0	0	X	
BOOT_NAND_OPTION	0X01	X	1	0	0	0	0	0	1	X	
BOOT_HSIC_OPTION	0X02	X	1	0	0	0	0	1	0	X	
BOOT_USB_OPTION	0X03	X	1	0	0	0	0	1	1	X	
ENABLE_SAHARA_PROTOCOL	0X08	X	1	0	0	1	0	X	X	X	



AP INTERFACE & DEBUG CONNECTORS		DRAWING NUMBER		051-9584		SIZE		D	
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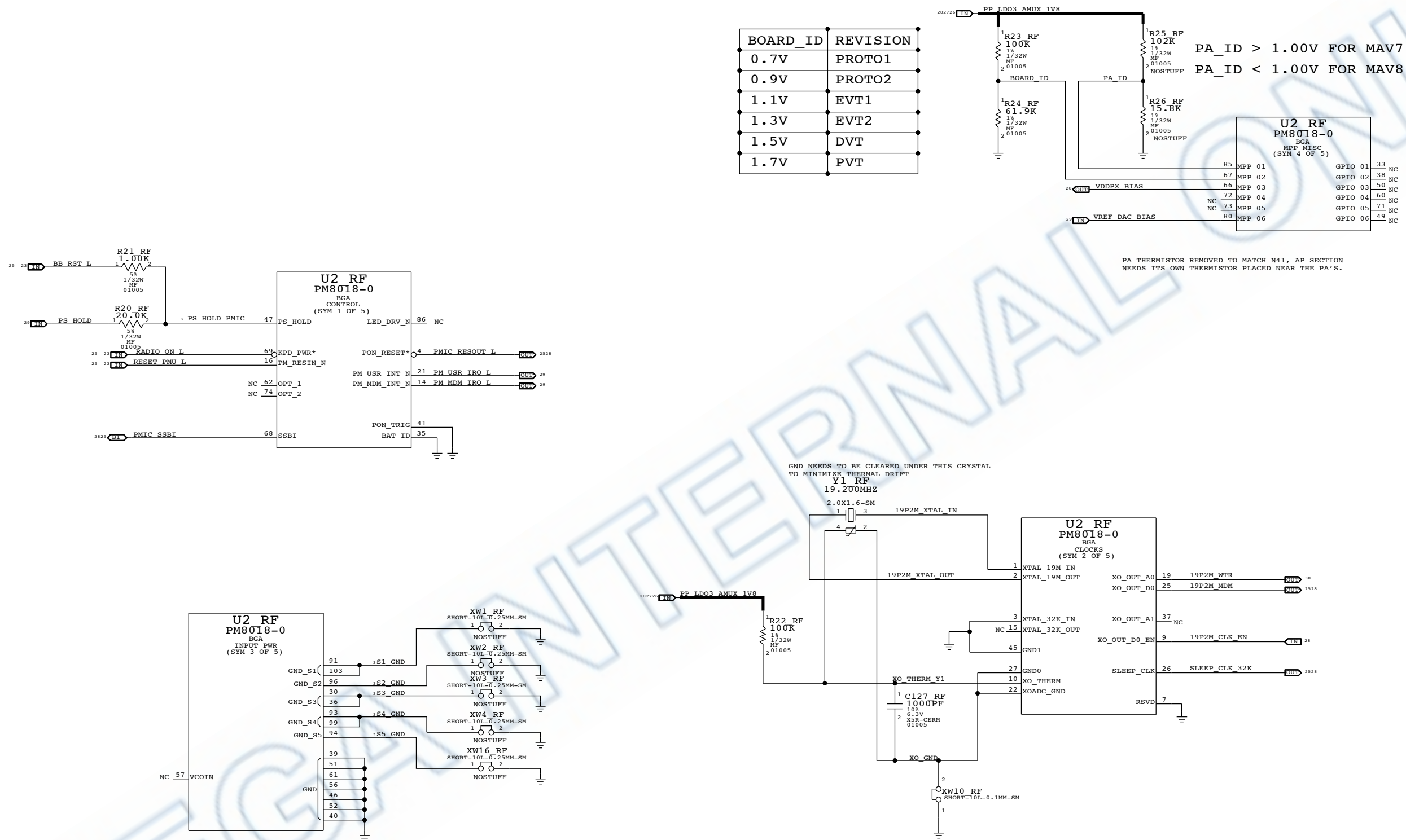
# PMU (1 OF 2)



CELLULAR PMU: (1 OF 2)		
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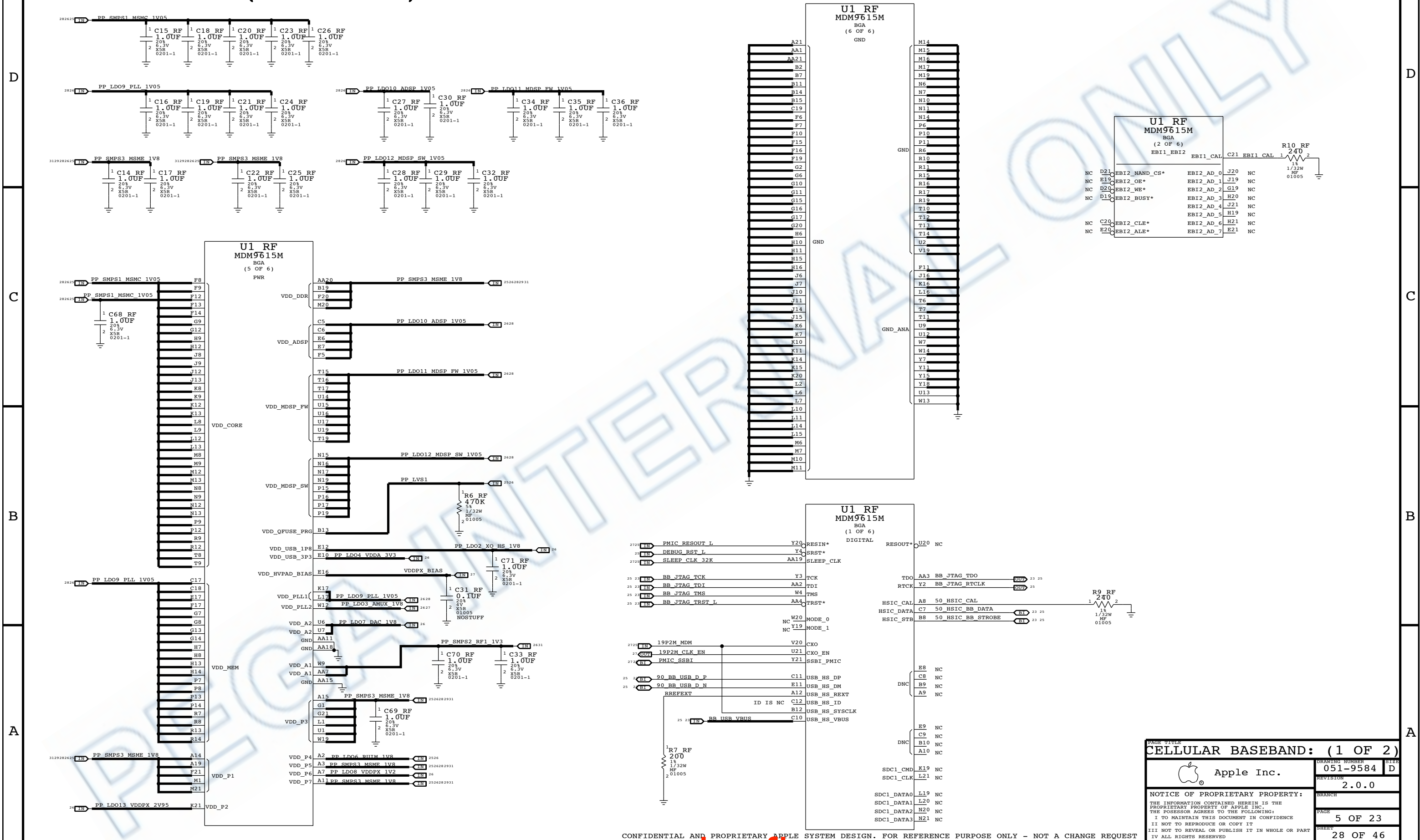
PMU ( 2 OF 2 )

BOARD_ID	REVISION
0.7V	PROTO1
0.9V	PROTO2
1.1V	EVT1
1.3V	EVT2
1.5V	DVT
1.7V	PVT



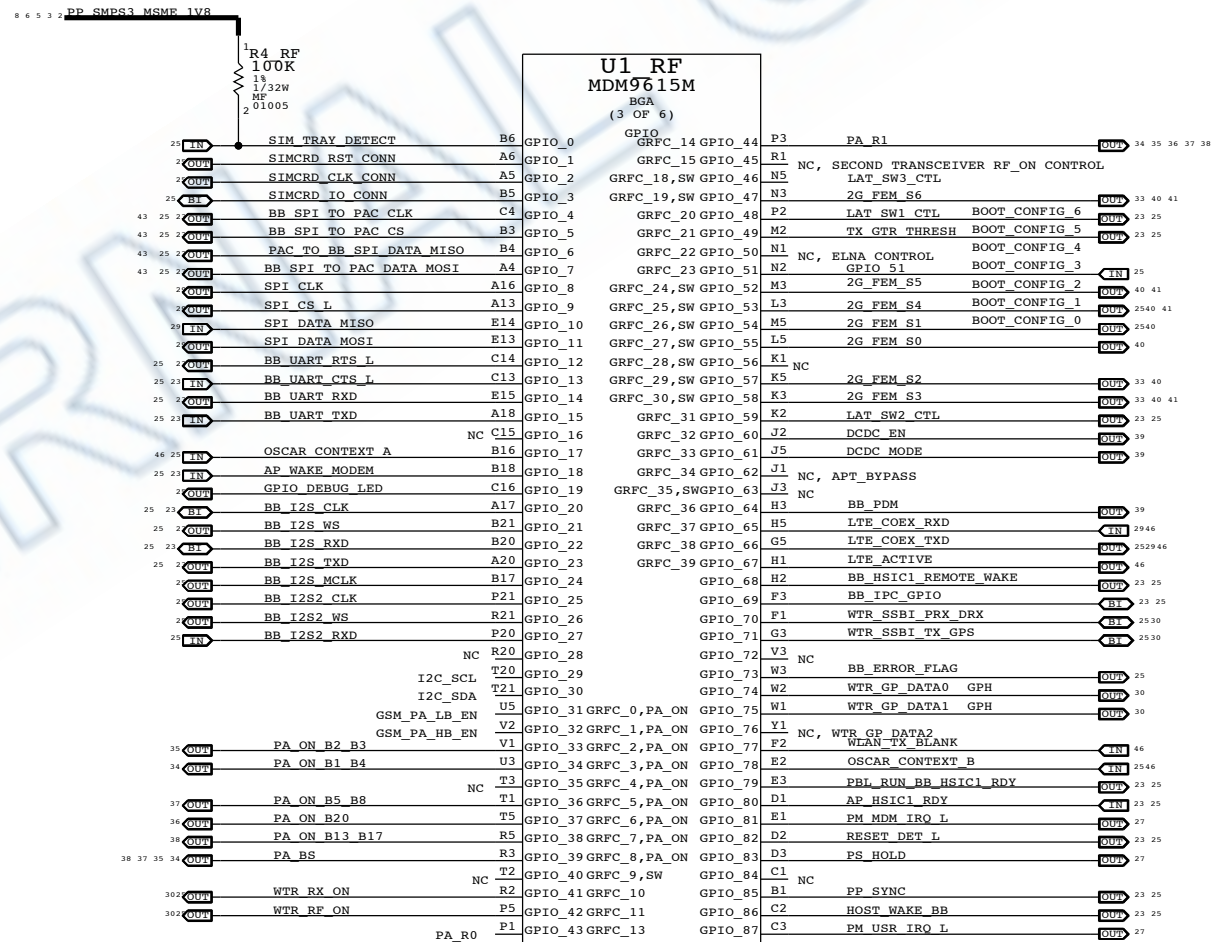
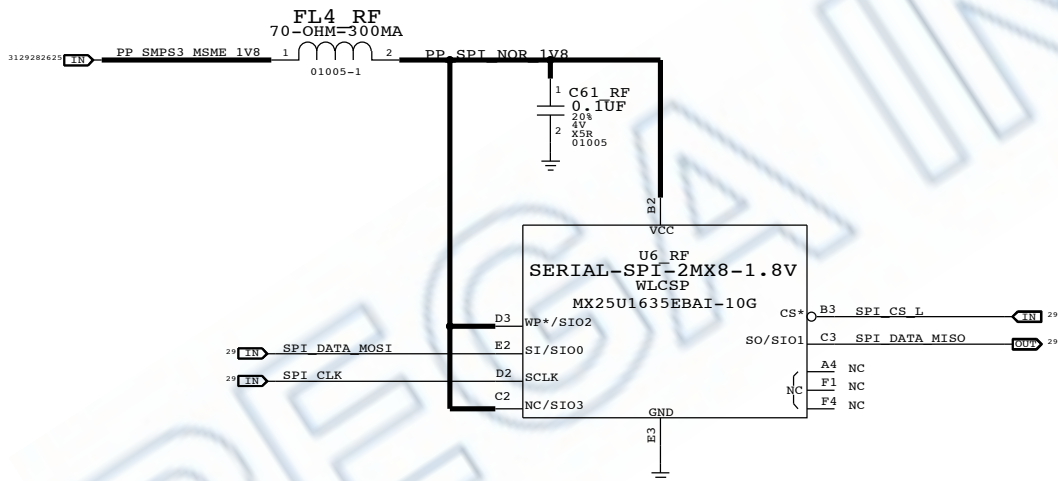
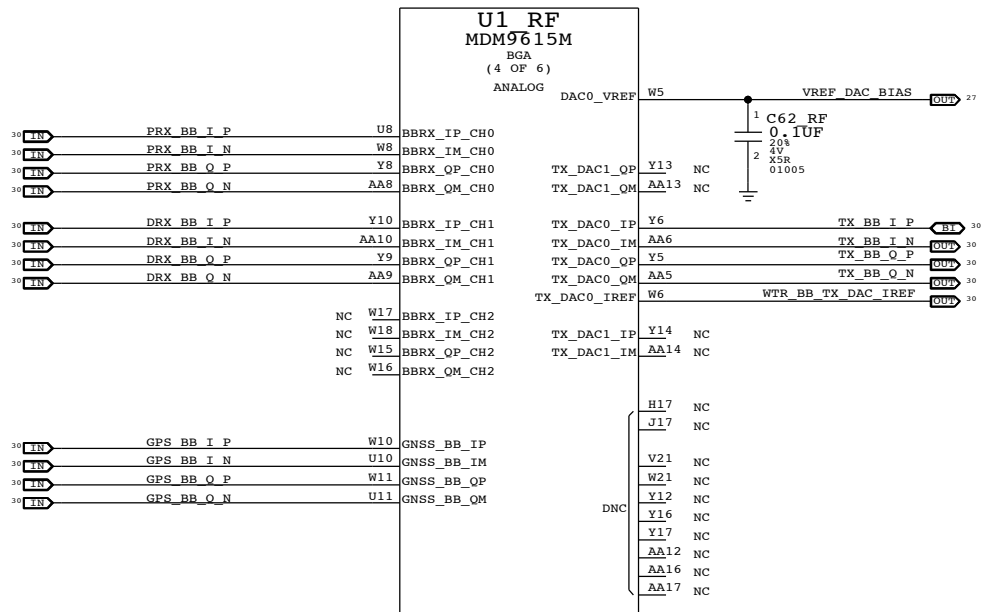
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		SHEET 27 OF 46

# BASEBAND (1 OF 2)



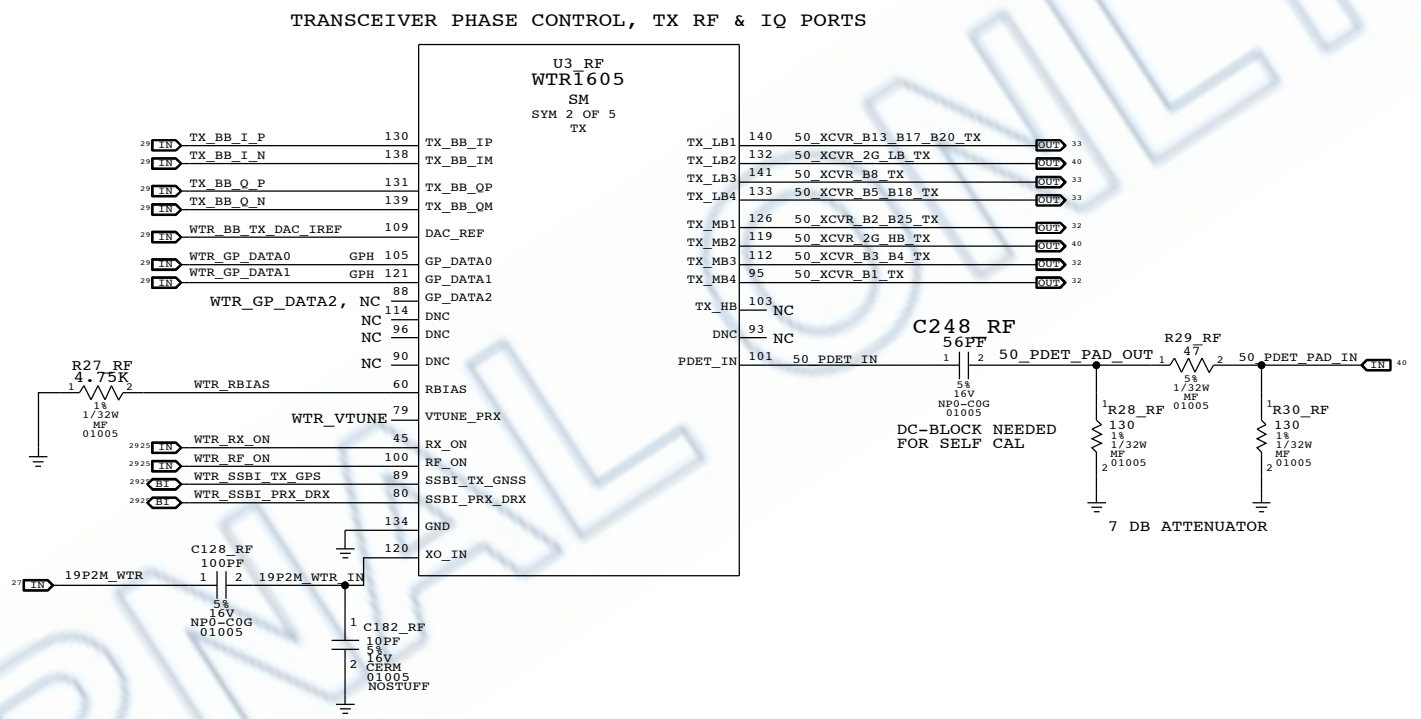
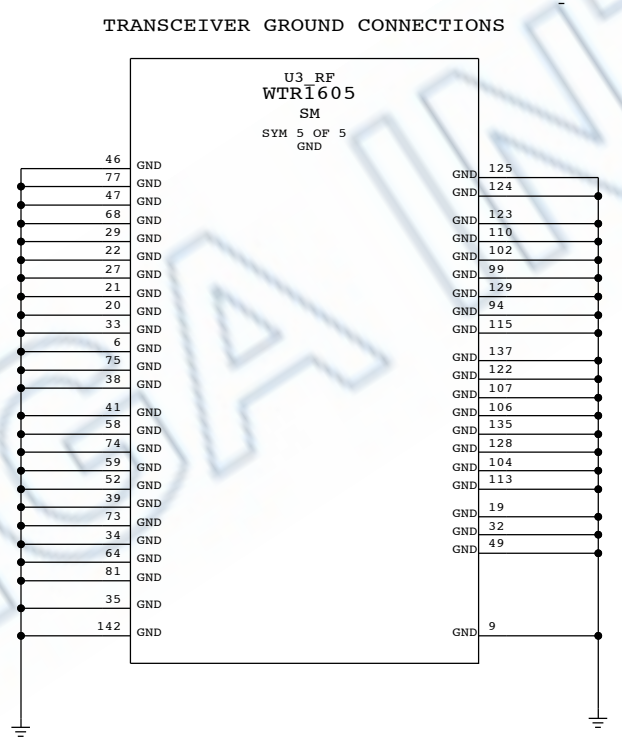
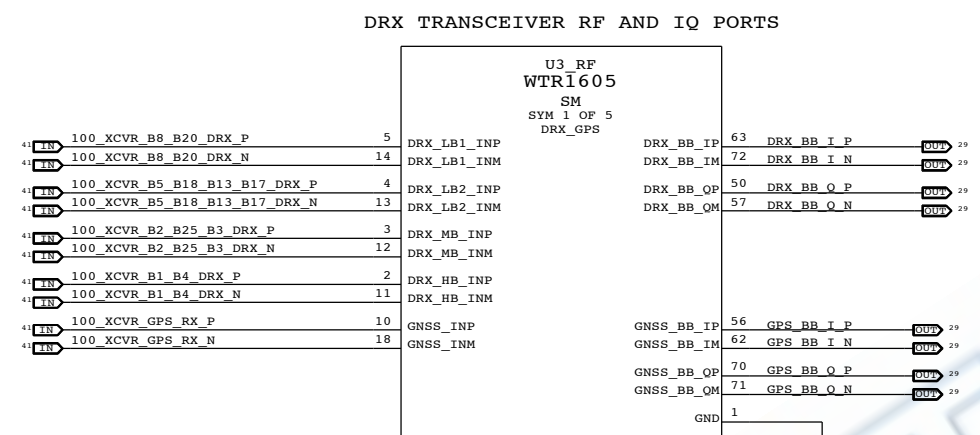
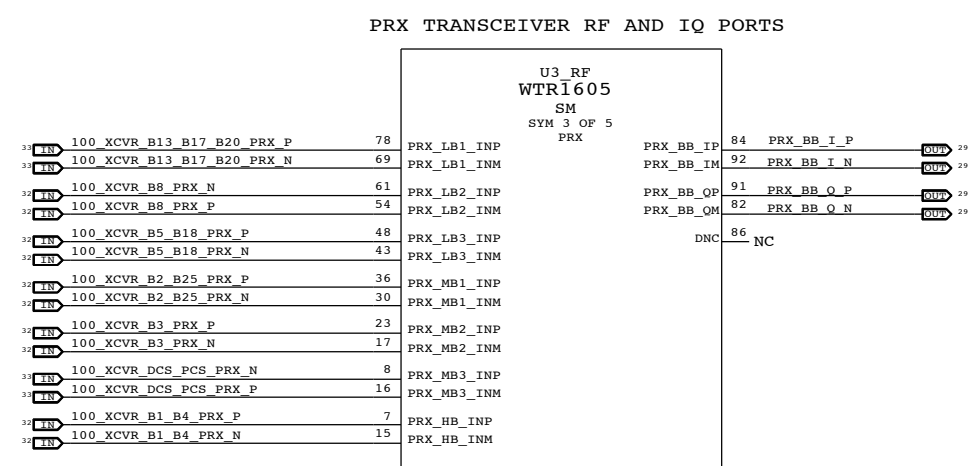
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# BASEBAND ( 2 OF 2 )



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RF TRANSCEIVER (1 OF 4)



CELLULAR RF TRANSCEIVER: (1 OF 2)

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RF TRANSCEIVER ( 2 OF 2 )

RF1\_1V3

RF1\_1V3

RF2\_2V05


RF1\_1V8

TRANSCEIVER POWER CONNECTIONS

U3\_RF  
WTR1605  
SM  
SRM 4 OF 5  
PWR

PP RF1 1V3 PRX FELO1	53	VDD_RF1_P_FELO	VDD_RF2_T_DA	111	PP RF2 2V05 TX DA
PP RF1 1V3 PRX FELO2	42	VDD_RF1_P_FELO	VDD_RF1_T_DA	118	PP RF1 1V3 TX DA
PP RF1 1V3 DRX LBLO	28	VDD_RF1_D_LBLO	VDD_RF1_T_UPC	117	PP RF1 1V3 TX UPCONVERTER
PP RF1 1V3 DRX FE	26	VDD_RF1_D_FE	VDD_RF1_T_LO	116	PP RF1 1V3 TX LO
PP RF1 1V3 DRX MBLO	25	VDD_RF1_D_MBLO	VDD_RF2_T_BB	108	PP RF2 2V05 TX BB
PP RF1 1V3 JAM DET	85	VDD_RF1_JDET	VDD_RF2_T_VCO	136	PP RF2 2V05 TX VCO
PP RF2 2V05 PRX BB	83	VDD_RF2_P_BB	VDD_RF2_XO	127	PP RF2 2V05 XO FILT
PP RF2 2V05 DRX BB	44	VDD_RF2_D_BB	VDD_RF1_T_SYN	98	PP RF1 1V3 TX SYNTH
PP RF2 2V05 PRX VCO	67	VDD_RF2_P_VCO	VDD_RF2_T_PLL	97	PP RF2 2V05 TX PLL
PP RF1 1V3 PRX VCO	76	VDD_RF1_P_VCO	VDD_RF1_G_LNA	24	PP RF1 1V3 GPS LNA
PP RF1 1V3 PRX PLL	66	VDD_RF1_P_PLL	VDD_RF1_G_VCO	37	PP RF1 1V3 GPS VCO
PP RF2 2V05 SHDR VCO	51	VDD_RF2_S_VCO	VDD_RF1_G_PLL	55	PP RF1 1V3 GPS PLL
PP RF1 1V3 SHDR VCO	40	VDD_RF1_S_VCO	VDD_RF1_G_BB	31	PP RF1 1V3 GPS DIG
PP RF1 1V3 SHDR PLL	55	VDD_RF1_S_PLL	VDD_DIO	87	PP RF1 1V8 DIG

CELLULAR RF TRANSCEIVER: ( 2 OF 2 )

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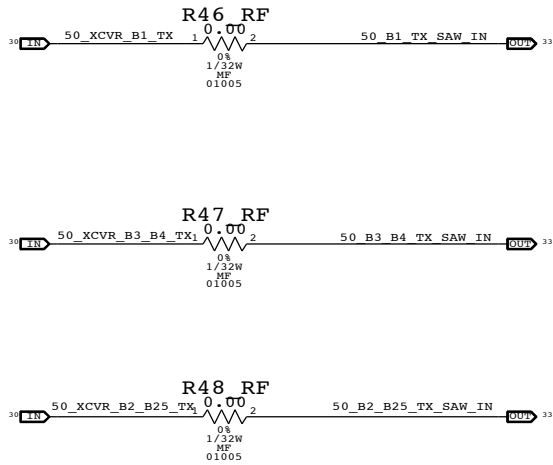
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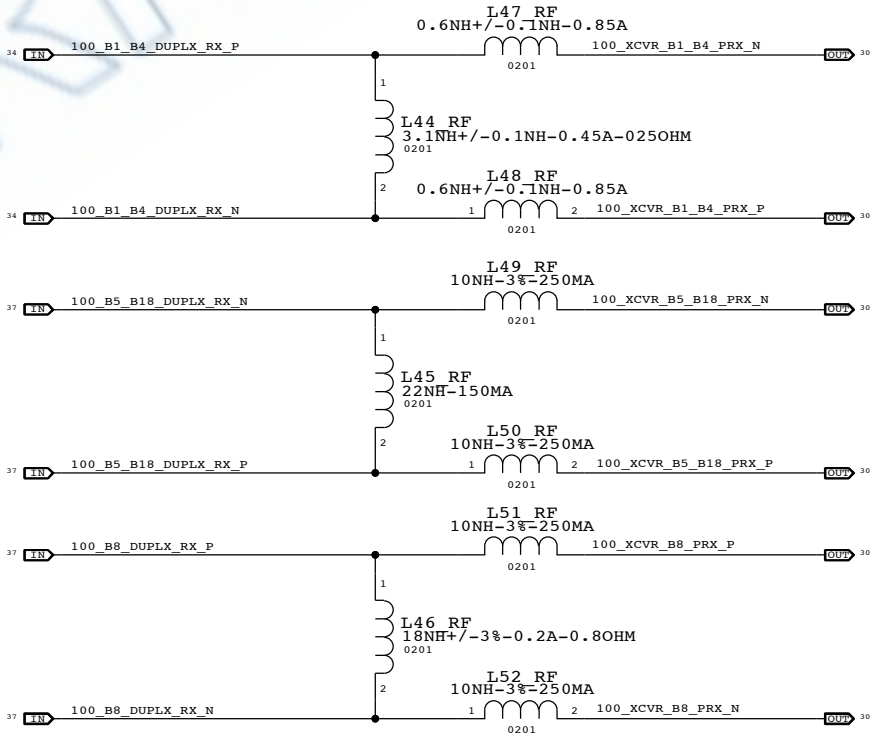
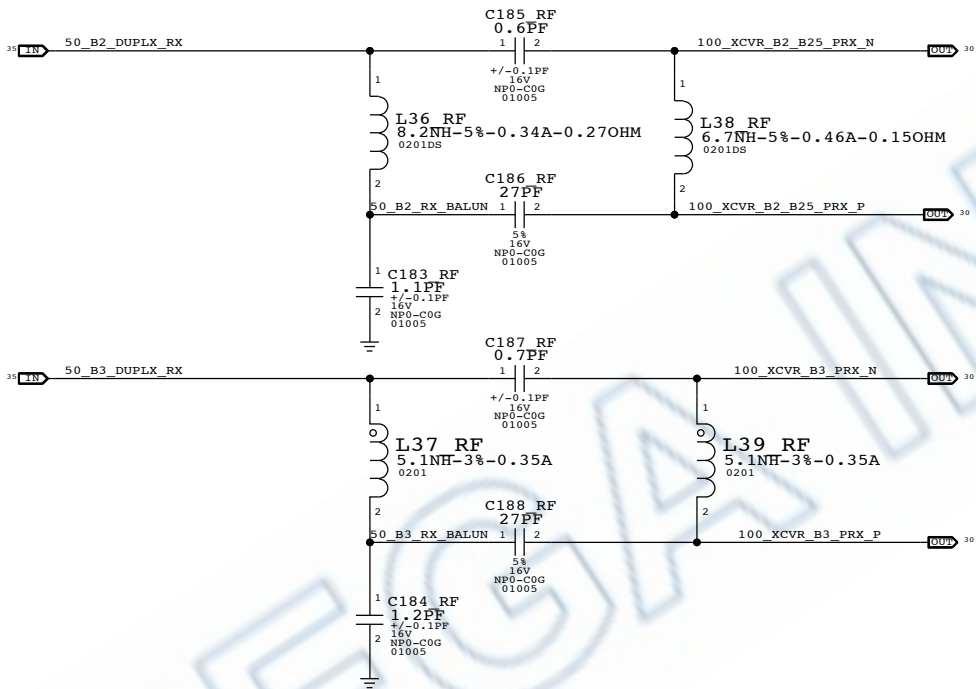
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TRANSCEIVER TX AND RX MATCHING


TX MATCHING NETWORKS



RX MATCHING NETWORKS



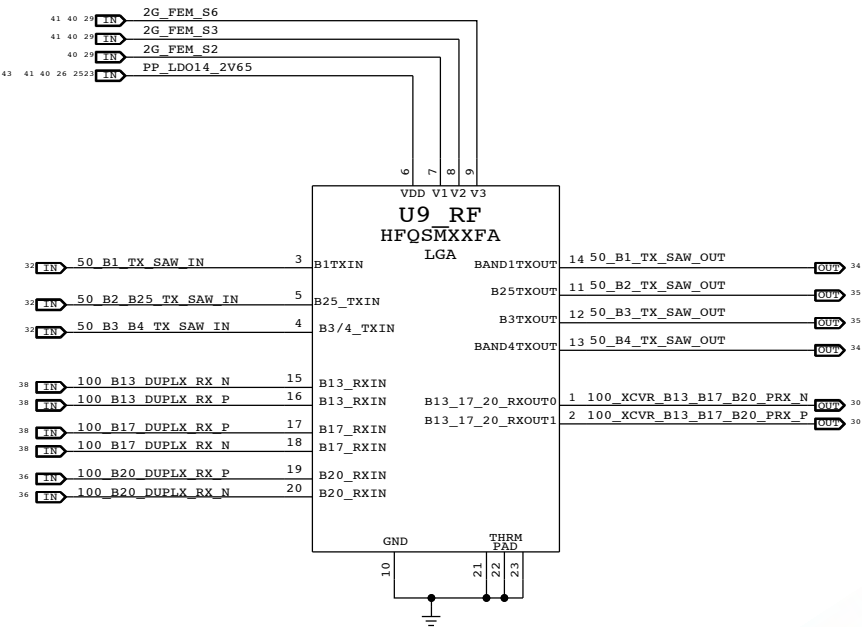
CELLULAR FRONT END: TX AND RX MATCHING

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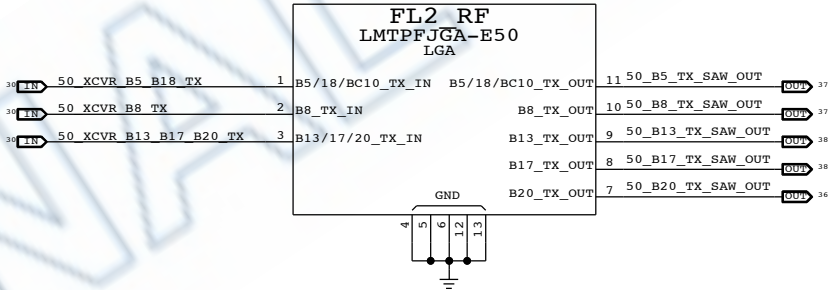
SAW BANKS

HB TX SAW BANK +  
B13/B17/B20 DP6T SWITCH AND MATCHING

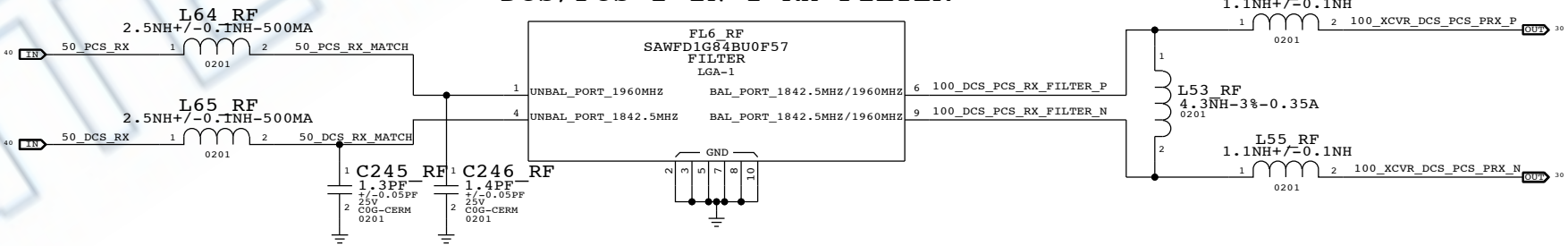


BAND	S6	S3	S2
B3 TX	HIGH	X	X
B4 TX	LOW	X	X
B13 RX	X	HIGH	HIGH
B17 RX	X	HIGH	LOW
B20 RX	X	LOW	HIGH


LB TX SAW BANK



DCS/PCS 2-IN-1 RX FILTER



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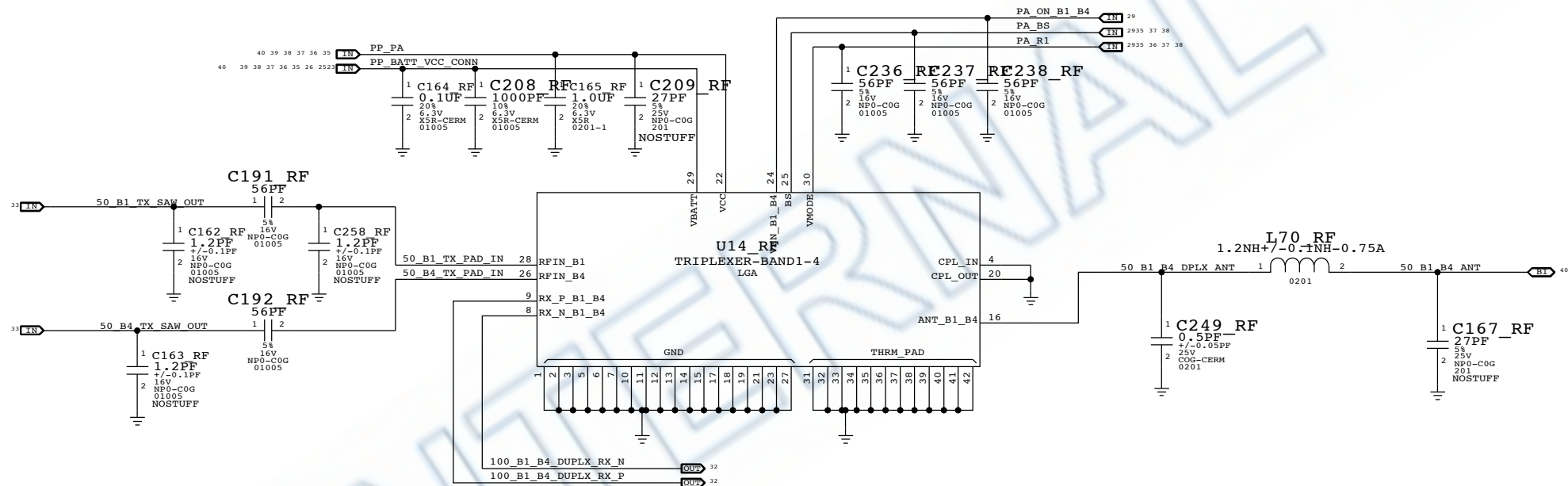
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BAND 1/4 PAT



BAND	PA	POWER	MODE	PA	BS	PA	ON	B1	B4	PA	R1
=====	=====	=====	=====	=====	=====	=====	=====	=====	=====	=====	=====
POWER DOWN		X		0		0		0		0	
STANDBY		X		X		0		X		X	
B4		HPM		0		1		0		0	
B4		LPM		0		1		1		1	
B1		HPM		1		1		0		0	
B1		LPM		1		1		1		1	

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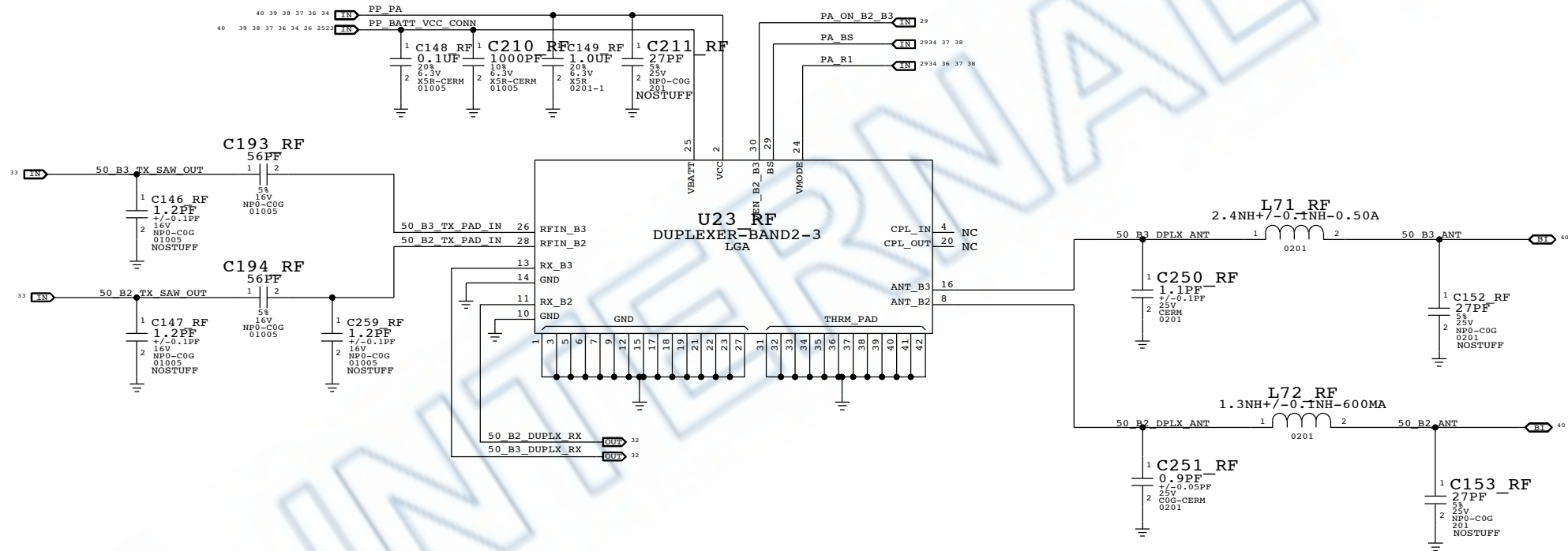
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BAND 2/3 PAD



BAND	PA POWER MODE	PA BS	PA_ON	B2	B3	PA_R1
=====	=====	=====	=====	=====	=====	=====
POWER DOWN	X	0	0			0
STANDBY	X	X	0			X
B3	HPM	0	1			0
B3	LPM	0	1			1
B2	HPM	1	1			0
B2	LPM	1	1			1

CELLULAR FRONT END: BAND 2/3 PAD

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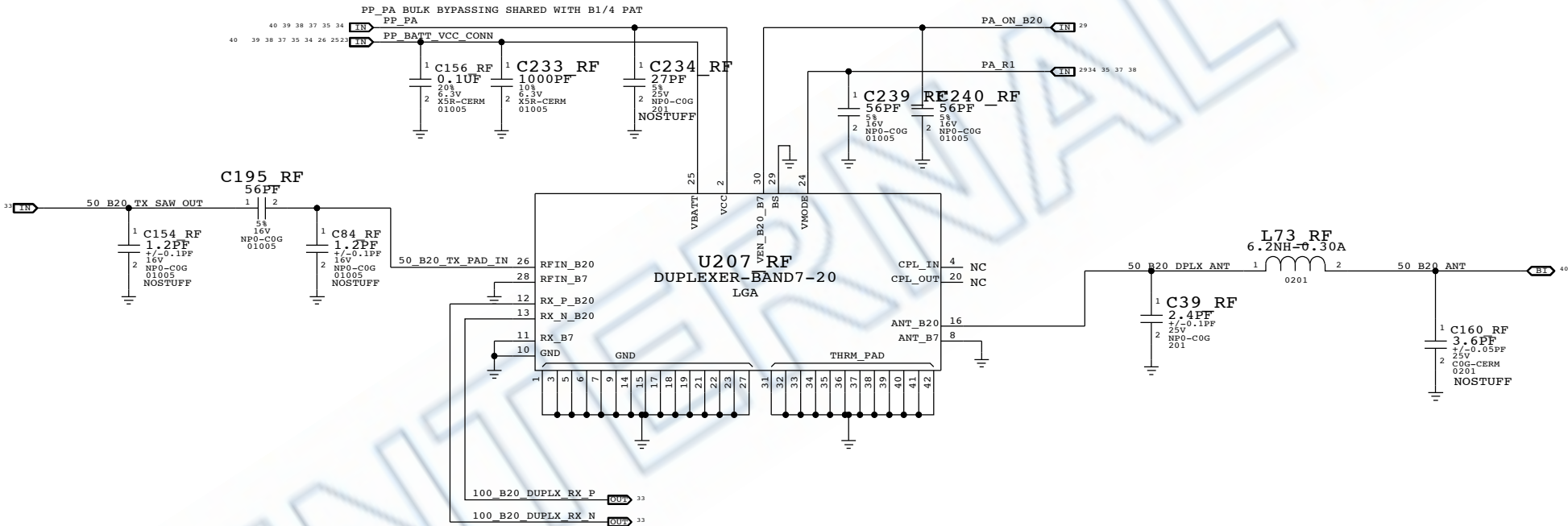
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BAND 20 PAD



BAND	PA	POWER	MODE	PA	ON	B20	PA	R1
POWER DOWN		LPM		0			0	
STANDBY		X		0			X	
B20		HPM		1			0	
B20		LPM		1			1	

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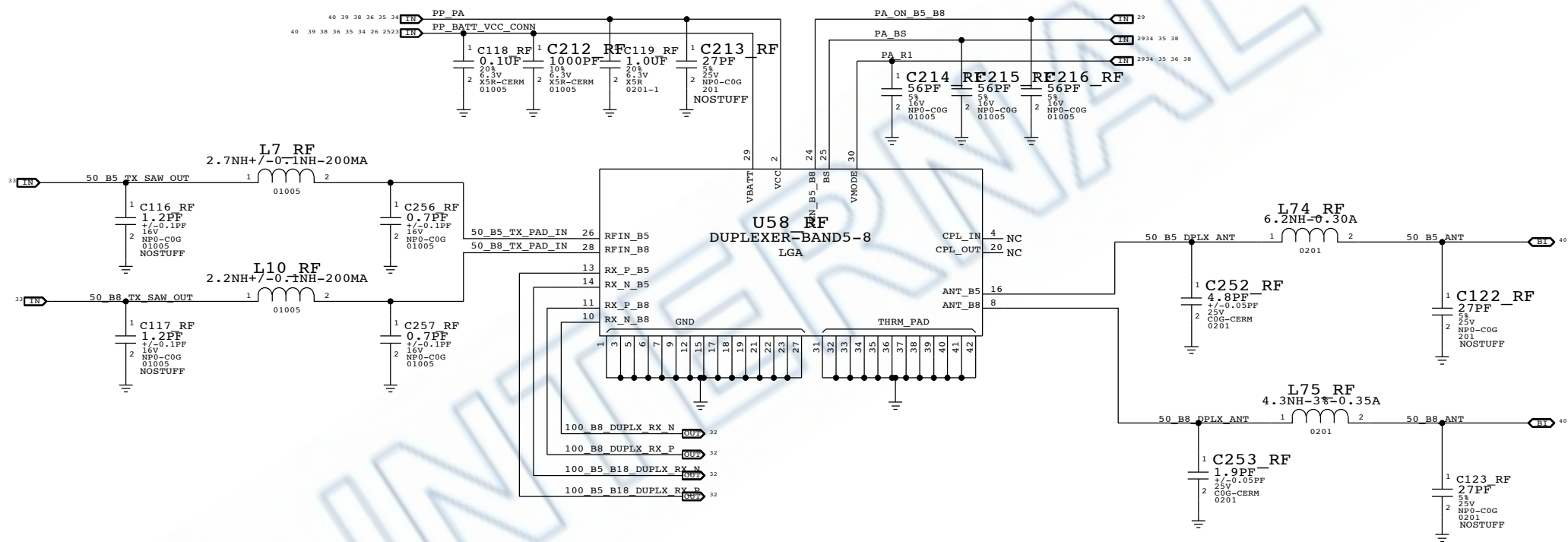
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BAND 5/8 PAD



BAND	PA POWER MODE	PA_BS	PA_ON_B5_B8	PA_R1
POWER DOWN	X	0	0	0
STANDBY	X	X	0	X
B5	HPM	0	1	0
B5	LPM	0	1	1
B8	HPM	1	1	0
B8	LPM	1	1	1

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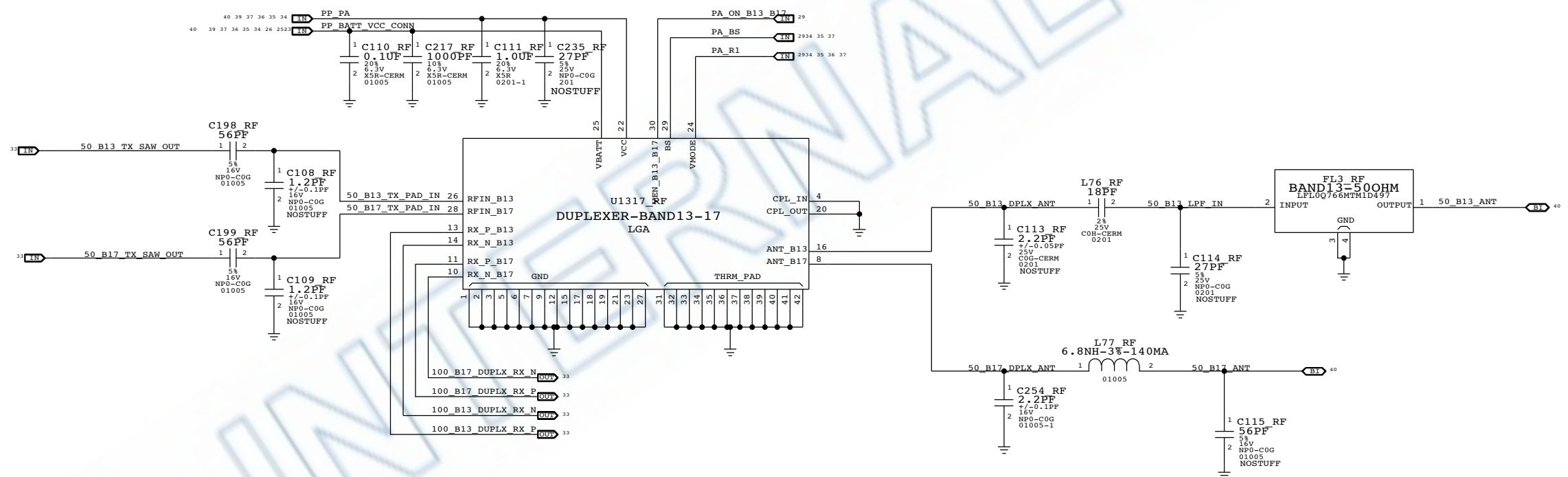
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# BAND 13/17 PAD



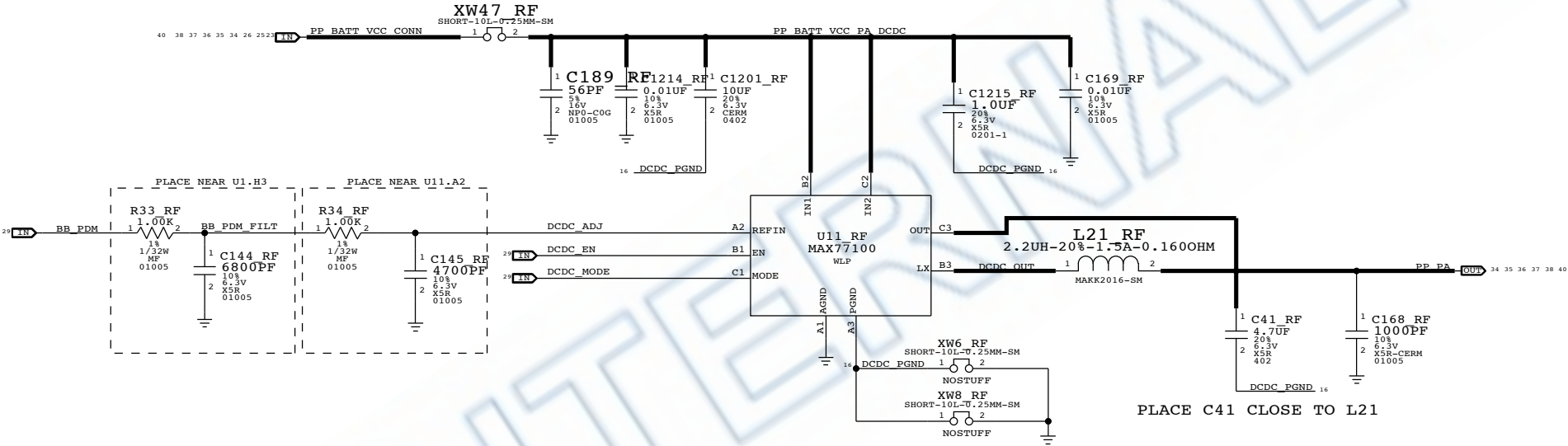
BAND	PA	POWER	MODE	PA_BS	PA_ON	B13	B17	PA_R1
=====	=====	=====	=====	=====	=====	=====	=====	=====
POWER DOWN		X		0	0			0
STANDBY		X		X	0			X
B17		HPM		0	1			0
B17		LPM		0	1			1
B13		HPM		1	1			0
B13		LPM		1	1			1

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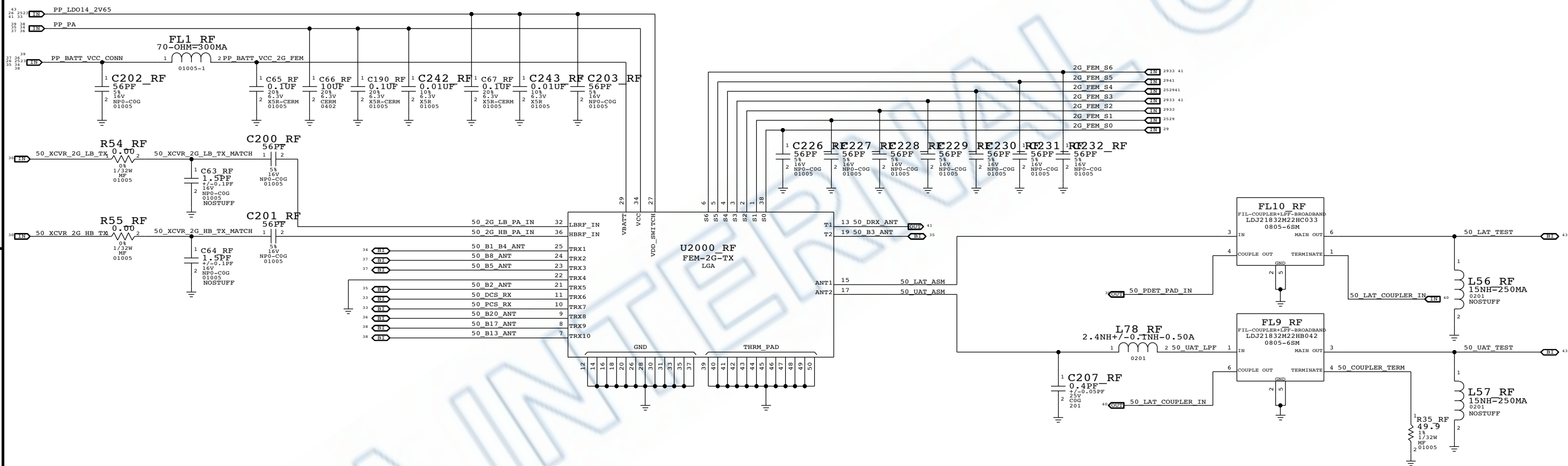
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PA DC/DC CONVERTER




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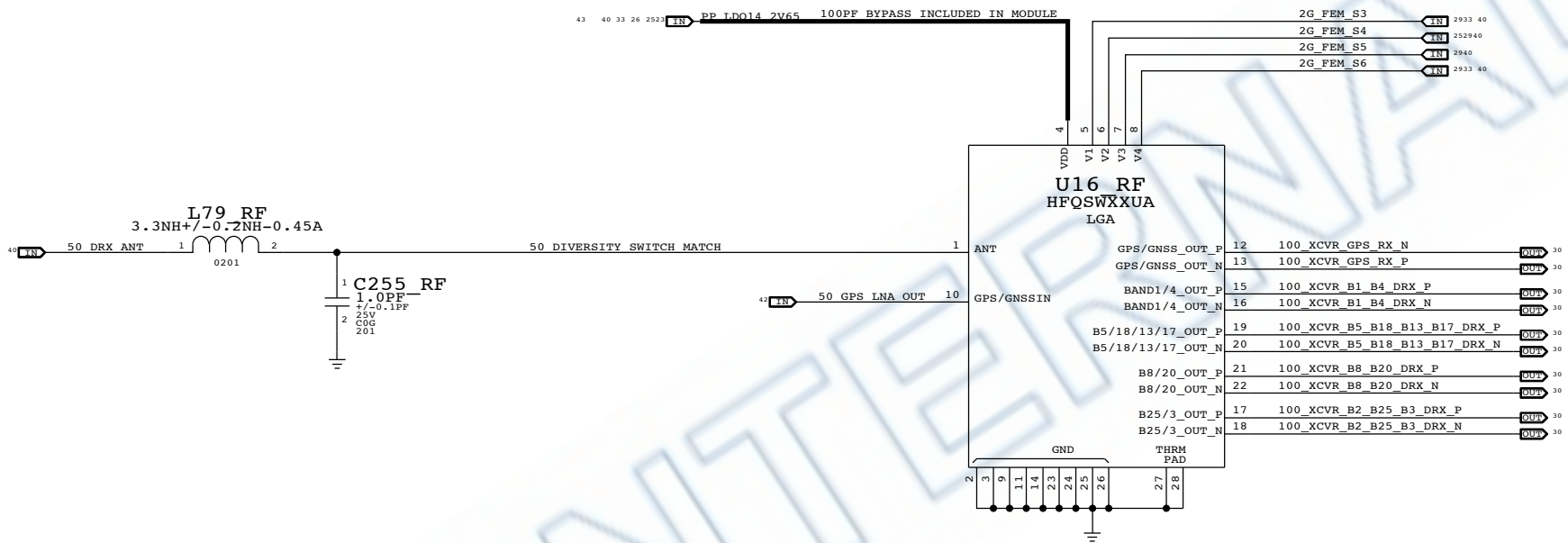
# 2G FEM



SEE PGS. 21-22 FOR 2G FEM LOGIC TABLE


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RX DIVERSITY



BAND	S6	S5	S4	S3
B1/B4	LOW	LOW	LOW	LOW
B2/25	LOW	HIGH	LOW	LOW
B3	HIGH	LOW	LOW	LOW
B5/6/18	LOW	LOW	HIGH	LOW
B8	LOW	LOW	LOW	HIGH
B13/17	LOW	HIGH	HIGH	HIGH
B20	LOW	HIGH	HIGH	LOW
OFF	LOW	LOW	HIGH	HIGH
SWITCH IS TERMINATED IN ALL OTHER POSSIBLE STATES				

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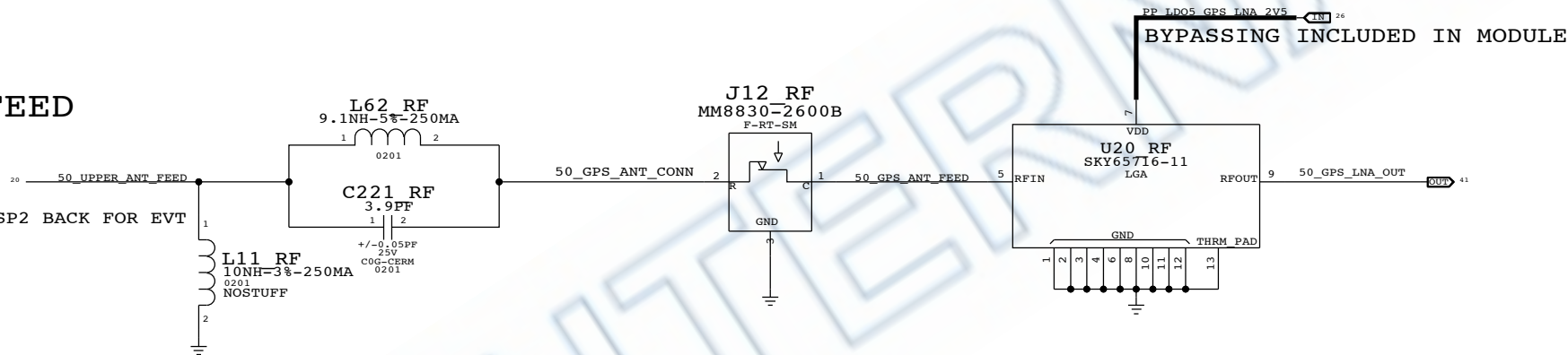
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GPS

GPS FEED

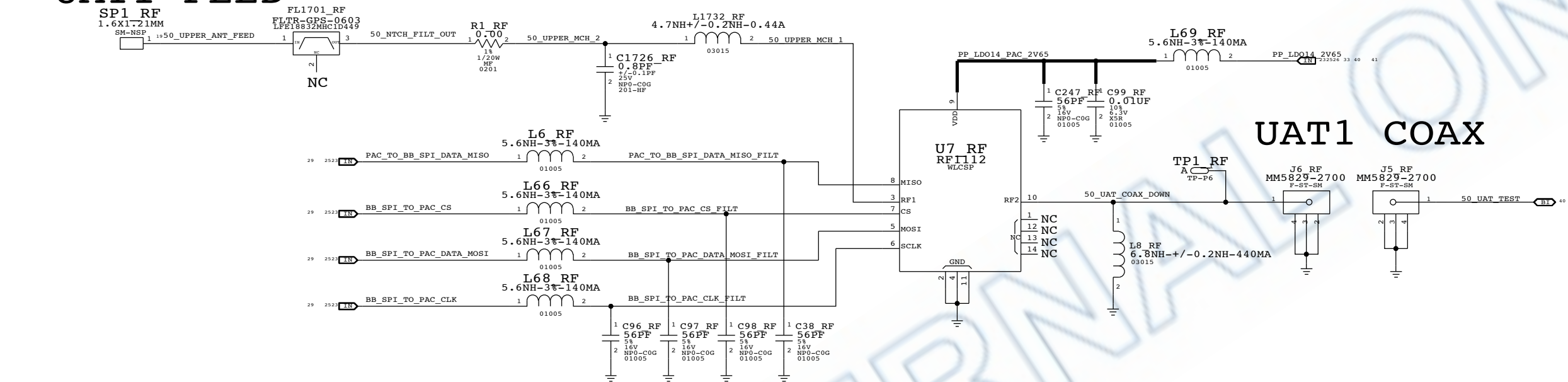
NOTE: ADD SP2 BACK FOR EVT



PAGE TITLE CELLULAR FRONT END: GPS LNA		
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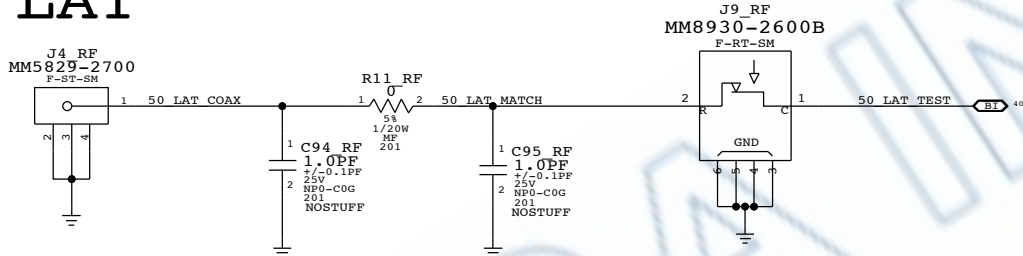
ANTENNA FEEDS

UAT1 FEED



UAT1 COAX

LAT



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FRONT END LOGIC TABLE ( 1 OF 2 )															
D	BAND		S6	S5	S4	S3	S2	S1	S0	TX/PRX	PATH	DRX		PATH	
	LB TX, IDLE, LAT		HIGH	HIGH	HIGH	LOW	LOW	HIGH	HIGH	LAT	UAT				
	LB TX, IDLE, UAT		HIGH	HIGH	HIGH	LOW	LOW	LOW	HIGH	UAT	LAT				
	LB TX, LAT, HPM		HIGH	HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LAT	UAT				
	LB TX, UAT, HPM		HIGH	HIGH	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT				
	LB TX, LAT, LPM		HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LOW	LAT	UAT				
	LB TX, UAT, LPM		HIGH	HIGH	HIGH	LOW	HIGH	LOW	LOW	UAT	LAT				
C	LB TX, HIGH Z, LAT, HPM		HIGH	HIGH	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT				
	LB TX, HIGH Z, UAT, HPM		HIGH	HIGH	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT				
	LB TX, HIGH Z, LAT, LPM		HIGH	HIGH	LOW	LOW	HIGH	HIGH	LOW	LAT	UAT				
	LB TX, HIGH Z, UAT, LPM		HIGH	HIGH	LOW	LOW	HIGH	LOW	LOW	UAT	LAT				
	HB TX, IDLE, LAT		HIGH	HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LAT	UAT				
	HB TX, IDLE, UAT		HIGH	HIGH	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT				
	HB TX, LAT, HPM		HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LAT	UAT				
	HB TX, UAT, HPM		HIGH	HIGH	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT				
	HB TX, LAT, LPM		HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LOW	LAT	UAT				
	HB TX, UAT, LPM		HIGH	HIGH	HIGH	HIGH	HIGH	LOW	LOW	UAT	LAT				
	HB TX, HIGH Z, LAT, HPM		HIGH	HIGH	LOW	HIGH	HIGH	HIGH	HIGH	LAT	UAT				
	HB TX, HIGH Z, UAT, HPM		HIGH	HIGH	LOW	HIGH	HIGH	LOW	HIGH	UAT	LAT				
	HB TX, HIGH Z, LAT, LPM		HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LOW	LAT	UAT				
	HB TX, HIGH Z, UAT, LPM		HIGH	HIGH	LOW	HIGH	HIGH	LOW	LOW	UAT	LAT				
B	GSM850 RX, LAT		HIGH	LOW	HIGH	HIGH	LOW	HIGH	HIGH	LAT	UAT				
	GSM850 RX, UAT		HIGH	LOW	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT				
	GSM900 RX, LAT		HIGH	LOW	HIGH	HIGH	HIGH	HIGH	HIGH	LAT	UAT				
	GSM900 RX, UAT		HIGH	LOW	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT				
	GSM1900 RX, LAT		LOW	HIGH	LOW	HIGH	HIGH	HIGH	HIGH	LAT	UAT				
	GSM1900 RX, UAT		LOW	HIGH	LOW	HIGH	HIGH	LOW	HIGH	UAT	LAT				
	GSM1800 RX, LAT		HIGH	LOW	LOW	HIGH	HIGH	HIGH	HIGH	LAT	UAT				
	GSM1800 RX, UAT		HIGH	LOW	LOW	HIGH	HIGH	LOW	HIGH	UAT	LAT				
	TERMINATED, UAT		HIGH	LOW	HIGH	LOW	HIGH	HIGH	HIGH	UAT	LAT				
	TERMINATED, LAT		HIGH	LOW	HIGH	LOW	HIGH	LOW	HIGH	LAT	UAT				
LAT = LOWER ANTENNA UAT = UPPER ANTENNA															
A															
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www.vinafix.vn															
8		7		6		5		4		3		2		1	

FRONT END LOGIC TABLE ( 1 OF 2 )

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	8	7	6	5	4	3	2	1		
	FRONT END LOGIC TABLE – DEV2 (2 OF 2)									
	BAND	S6	S5	S4	S3	S2	S1	S0	TX/PRX PATH	DRX PATH
D	B1/BC6, LAT	LOW	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B1/BC6, UAT	LOW	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B2/B25/BC1, LAT	LOW	HIGH	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B2/B25/BC1, UAT	LOW	HIGH	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
C	B3, LAT	HIGH	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B3, UAT	HIGH	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B4/BC15, LAT	LOW	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B4/BC15, UAT	LOW	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B5/B6/B18/BC0/BC10, LAT	LOW	LOW	HIGH	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B5/B6/B18/BC0/BC10, UAT	LOW	LOW	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT
	B8, LAT	LOW	LOW	LOW	HIGH	HIGH	HIGH	HIGH	LAT	UAT
	B8, UAT	LOW	LOW	LOW	HIGH	HIGH	LOW	HIGH	UAT	LAT
	B13, LAT	LOW	HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LAT	UAT
	B13, UAT	LOW	HIGH	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT
	B17, LAT	LOW	HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LAT	UAT
	B17, UAT	LOW	HIGH	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT
	B20, LAT	LOW	HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B20, UAT	LOW	HIGH	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT
	OFF	LOW	LOW	HIGH	HIGH	X	X	X	X	X
	STANDBY	LOW	LOW	LOW	LOW	LOW	LOW	LOW	X	X
B	LAT = LOWER ANTENNA									
	UAT = UPPER ANTENNA									
	OFF = LOWEST POWER STATE WITHOUT REMOVING LDO14 2V65 POWER									
	STANDBY = ADDED TO SUPPORT EXISTING SW ARCHITECTURE. NOT TO BE USED AS A LOW POWER STATE.									
A										
	8	7	6	5	4	3	2	1		

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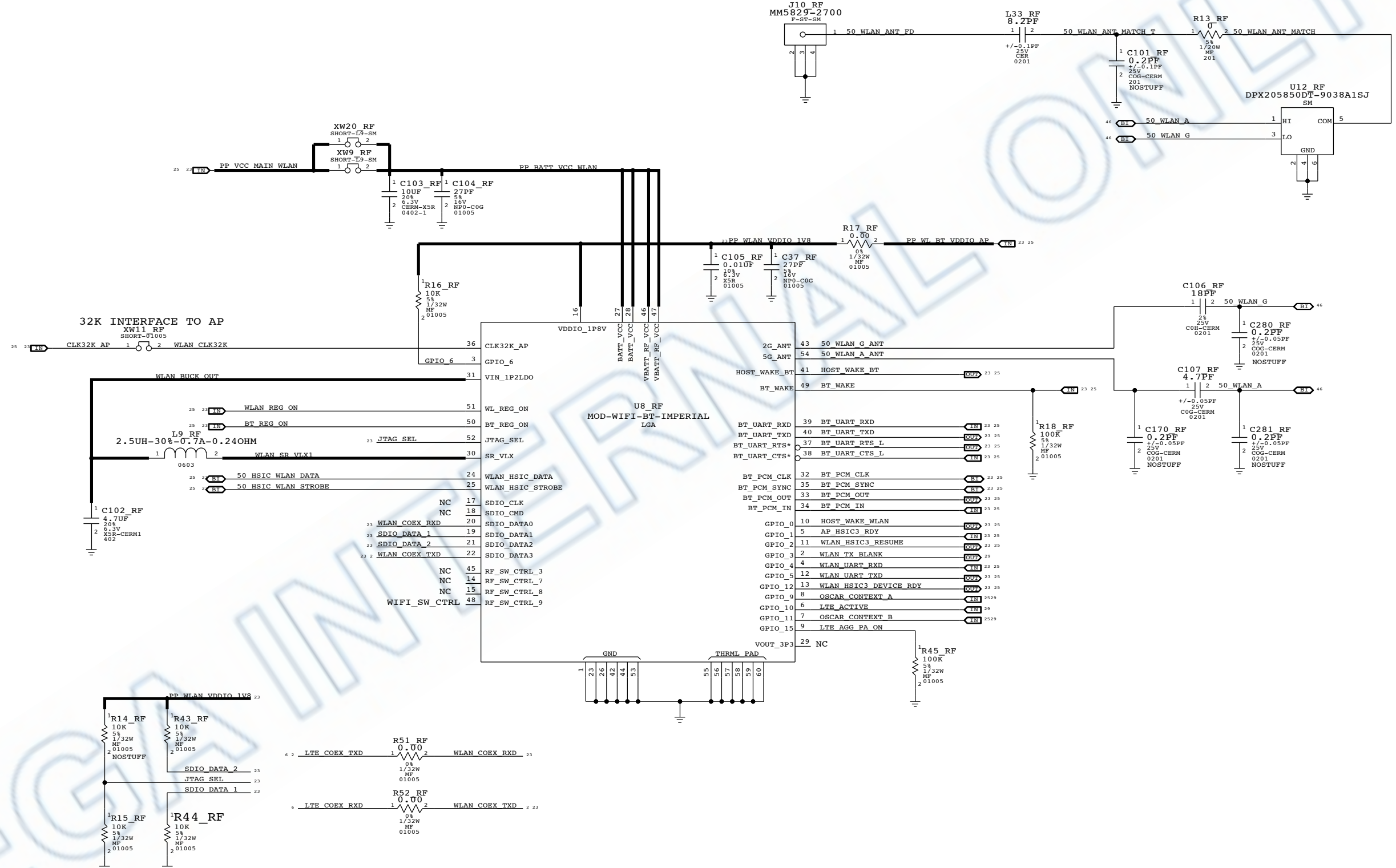
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# WLAN / BT

# UAT2



PULL-UP ON GPIO6, SDIO DATA 2 & PULL-DOWN ON SDIO DATA 1 REQUIRED FOR HSIC BOOTSTRAPPING

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