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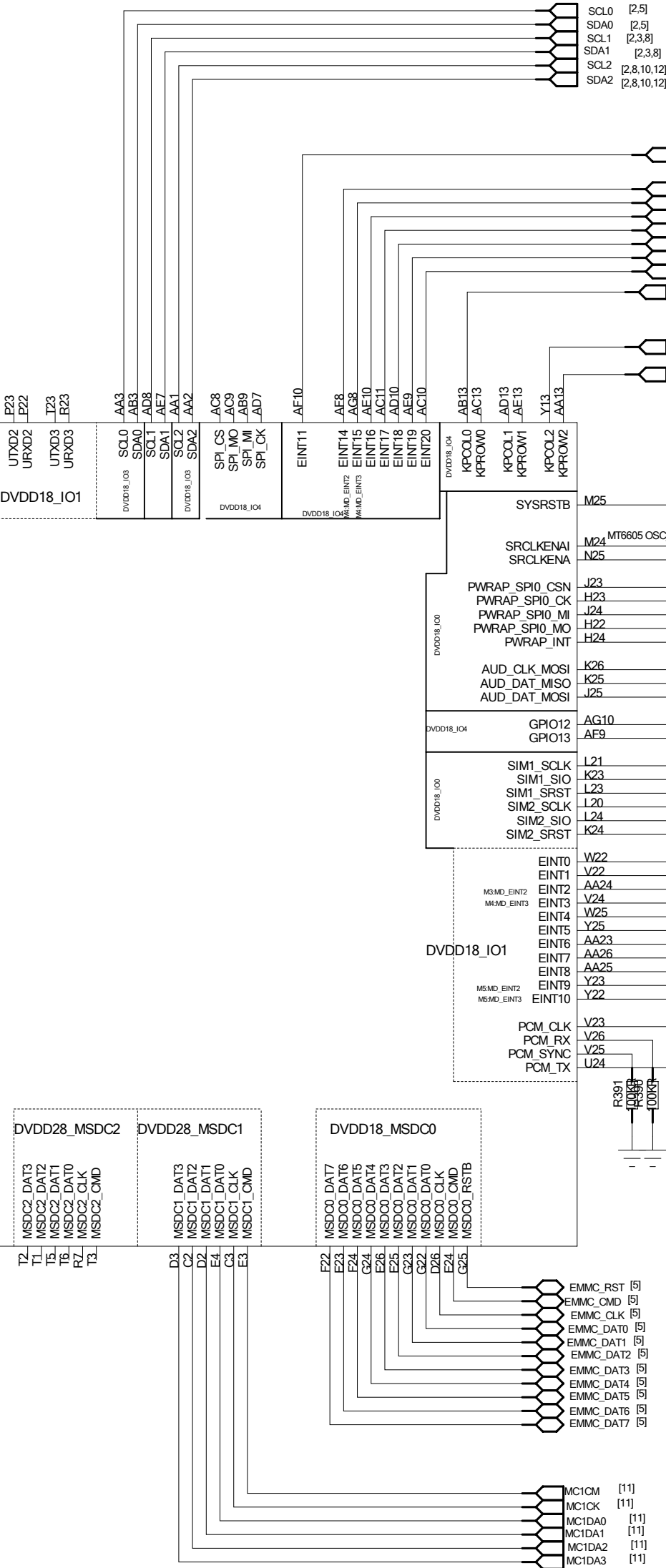
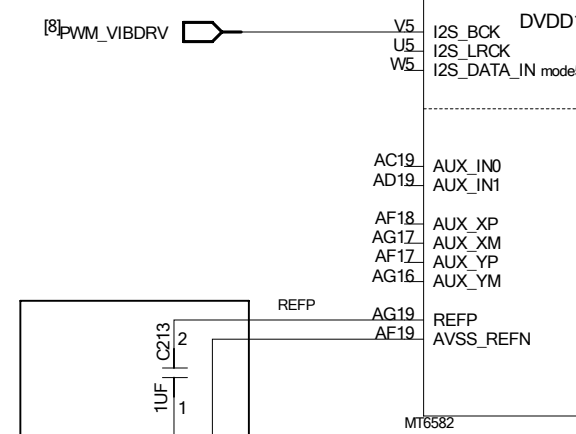
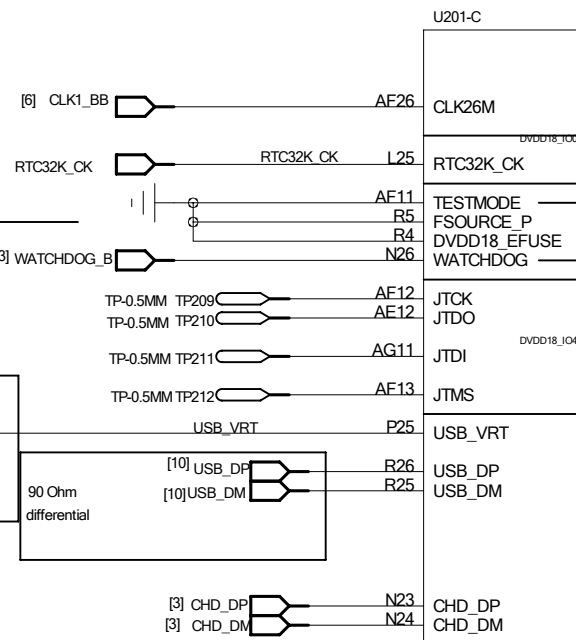
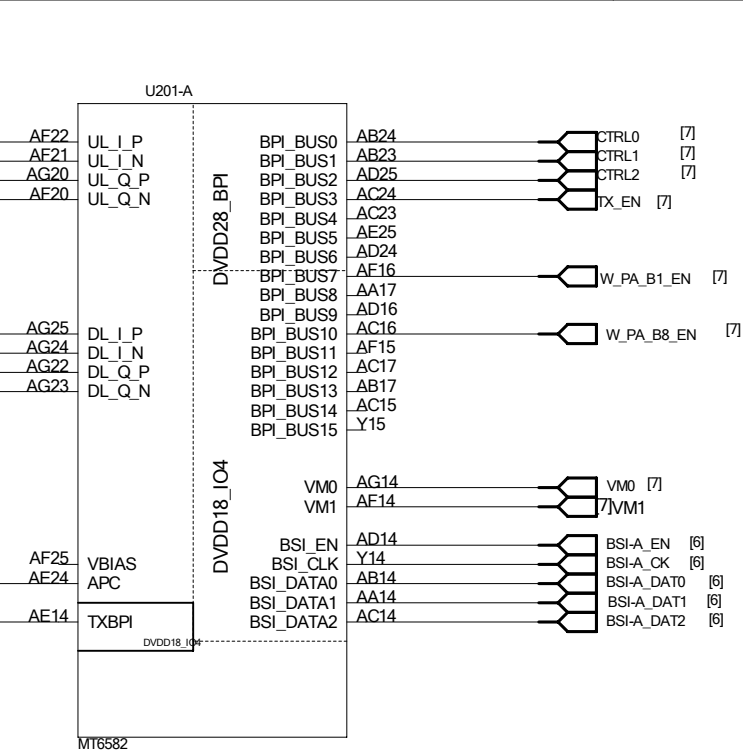
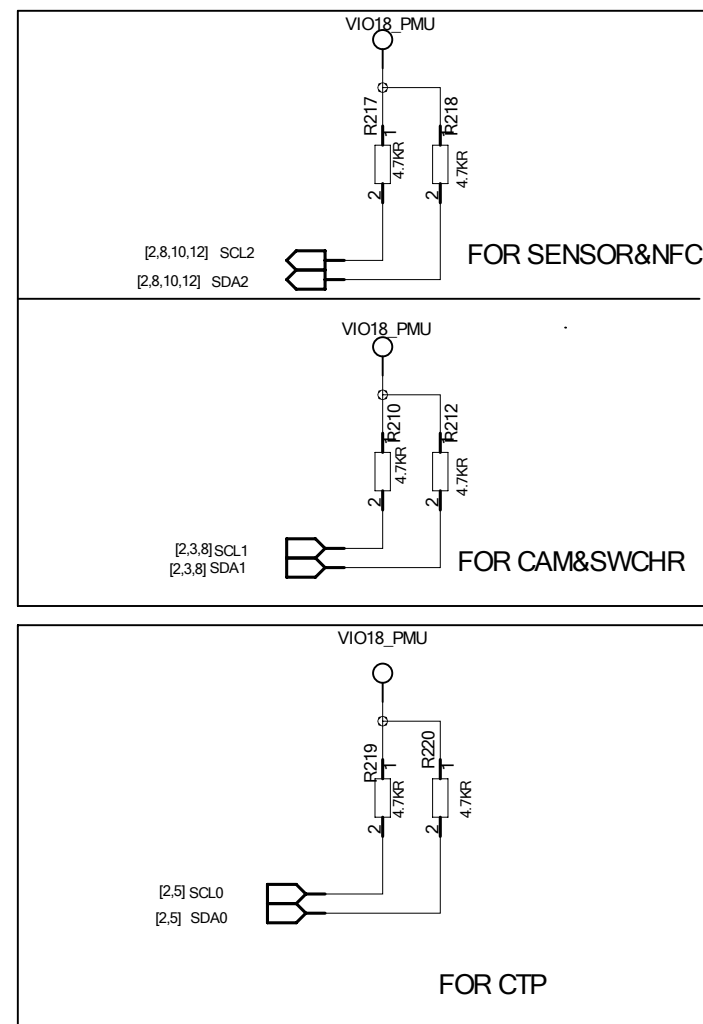
B

A

Pin	DVDD18_EFUSE	FSOURCE_P	Description
	VGPx (2V0)		w/ EFUSE program
Voltage	GND		w/o EFUSE program

EFUSE Notice

Please reserve UART0/1 test point
UART0 for AP log
UART1 for MD log

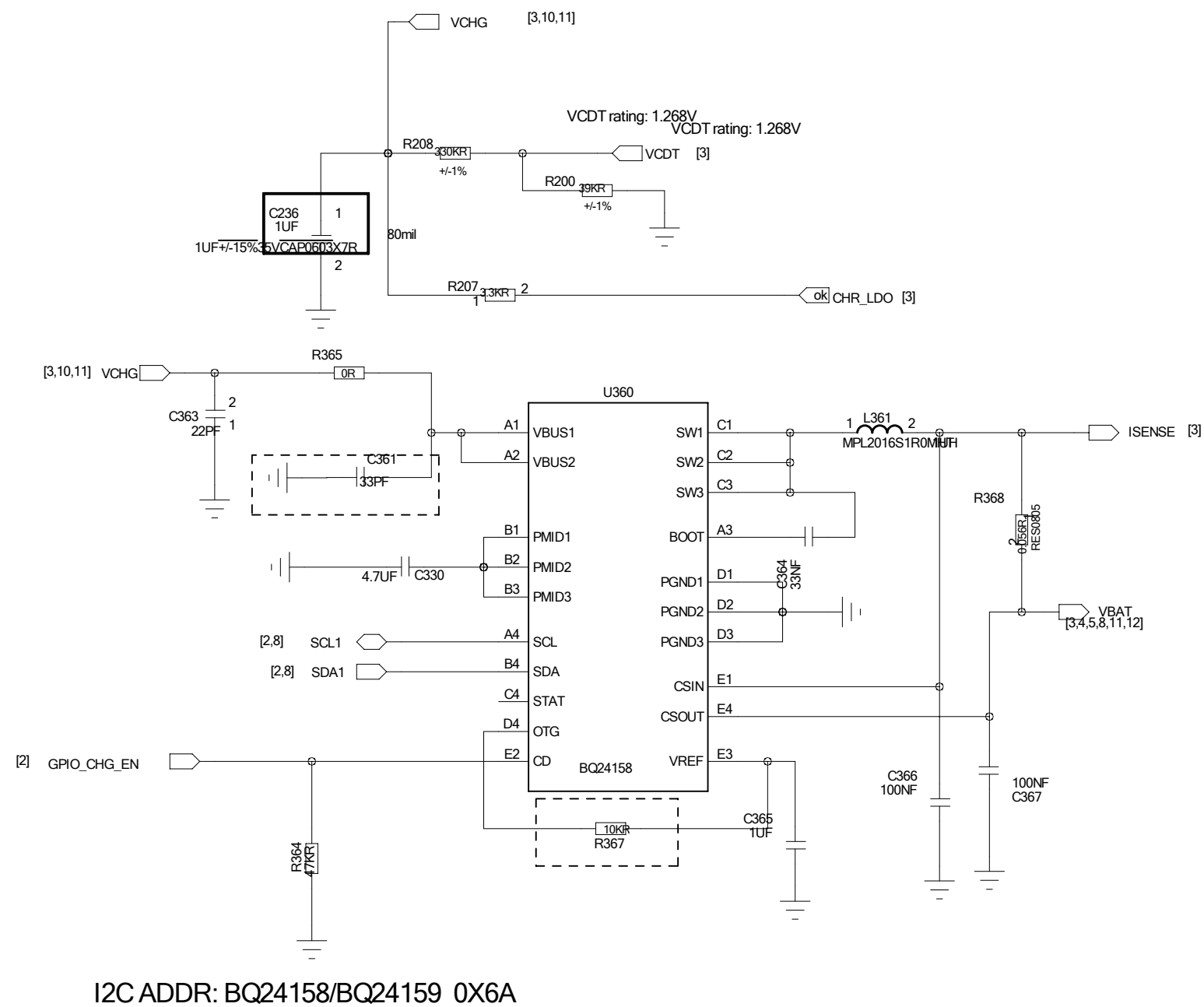


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	SHEET: No of No	REV: <NO>
		<DATE HERE>

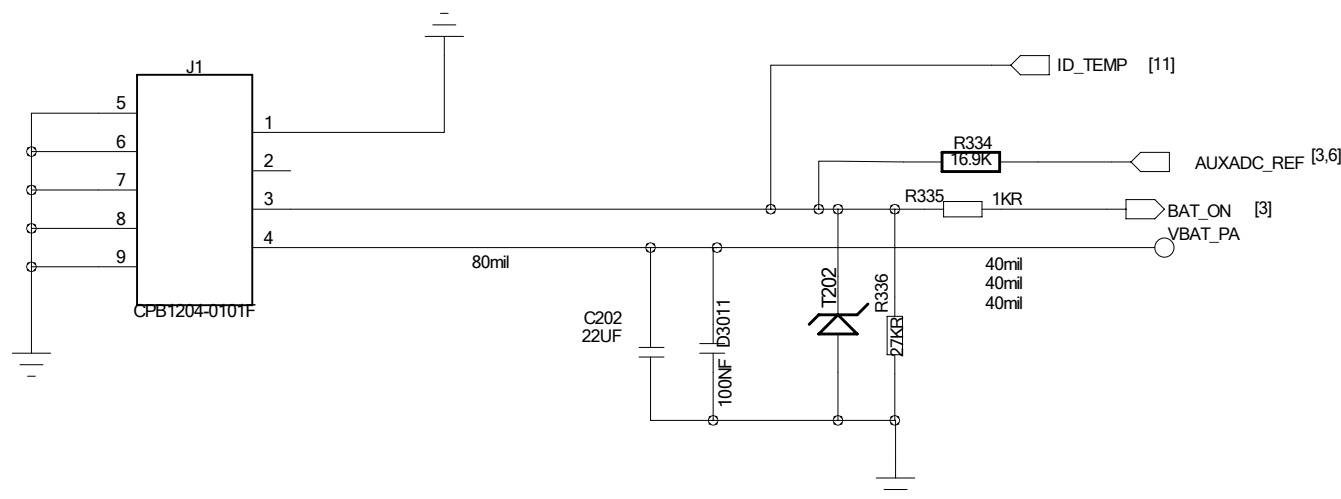
Charger

OVP: 12V

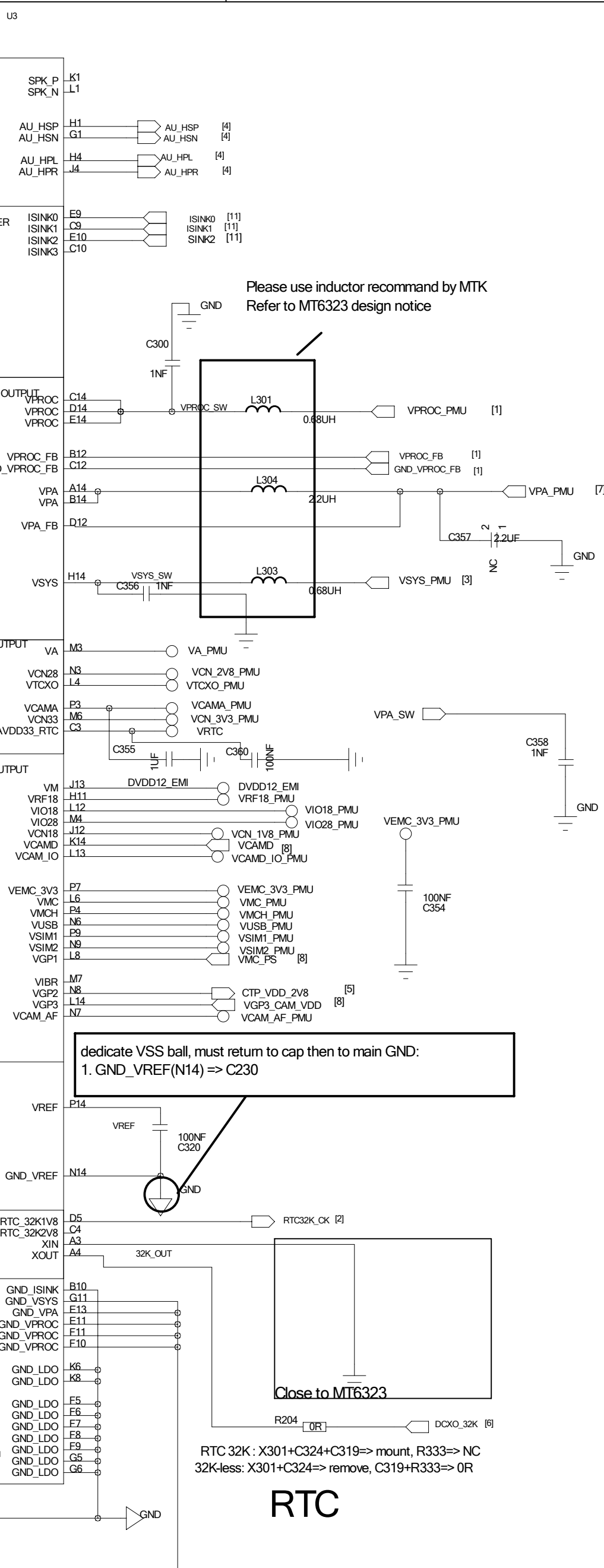
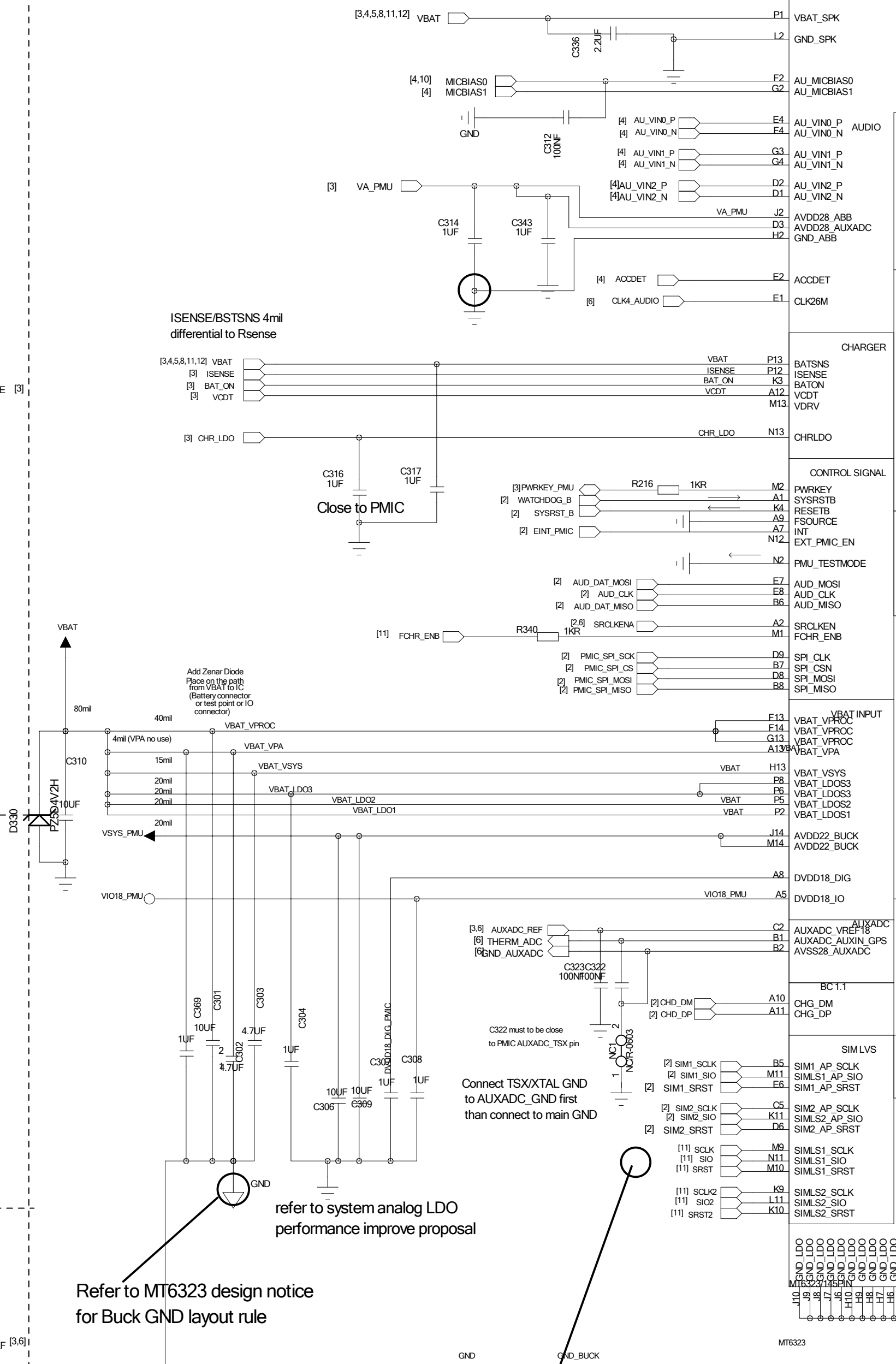
1. Close to Battery Connector.
(Rsense (R212) <10mm)
2. Main path should be 40mil.
(VBUS -> U201's E, -> U201's C -> R212 -> VBAT)
3. Star connection from R212 to BAT Connector



BATTERY CONNECTOR



Symbol	LPDDR2/1.2V	PCDDR3L/1.35V	PCDDR3/1.5V	LPDDR1/1.8V	Default
SPI_CSN	H	L (20K)	H	L (20K)	PU
AUD_MOSI	L	H (20K)	H (20K)	L	PD

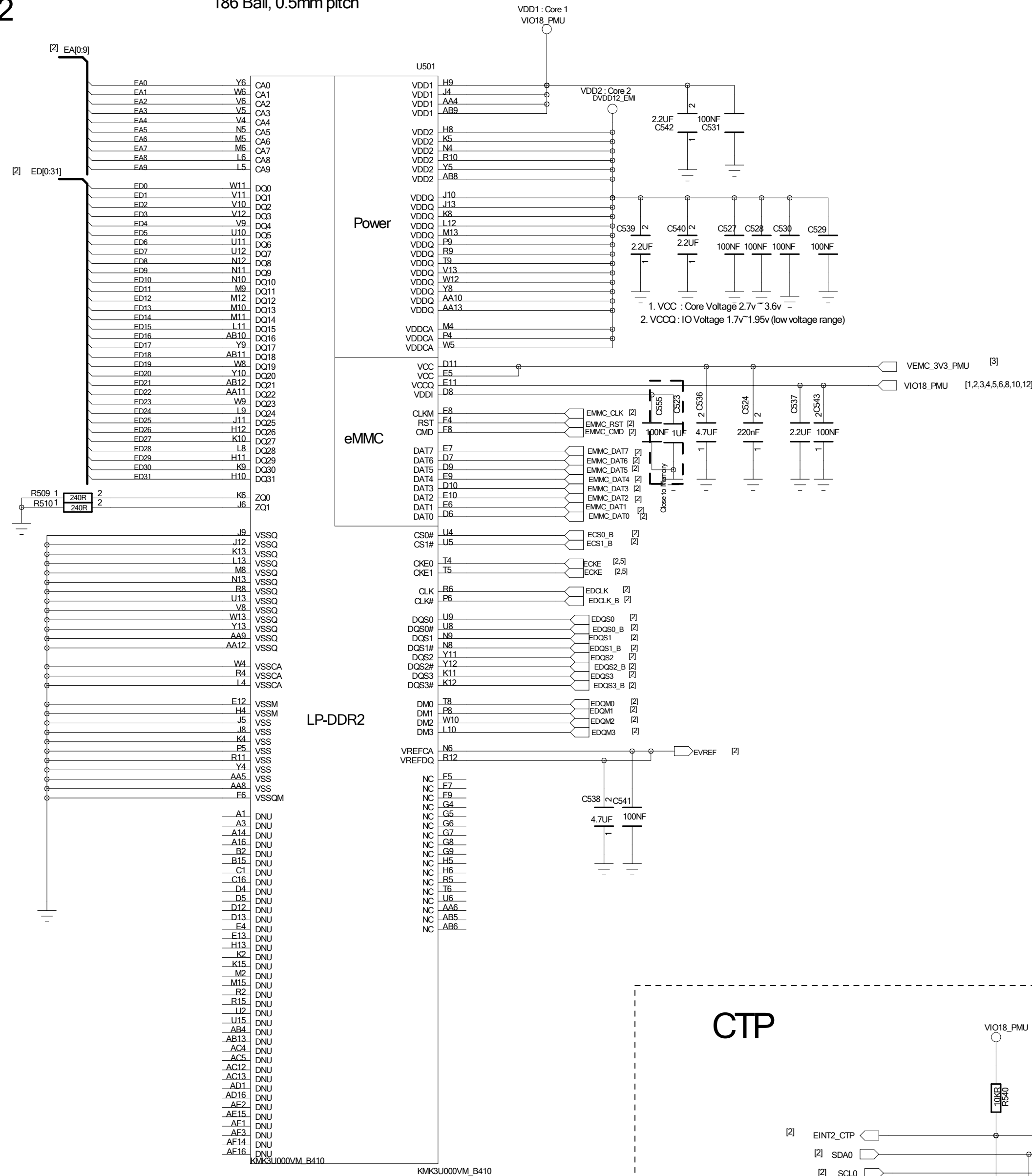


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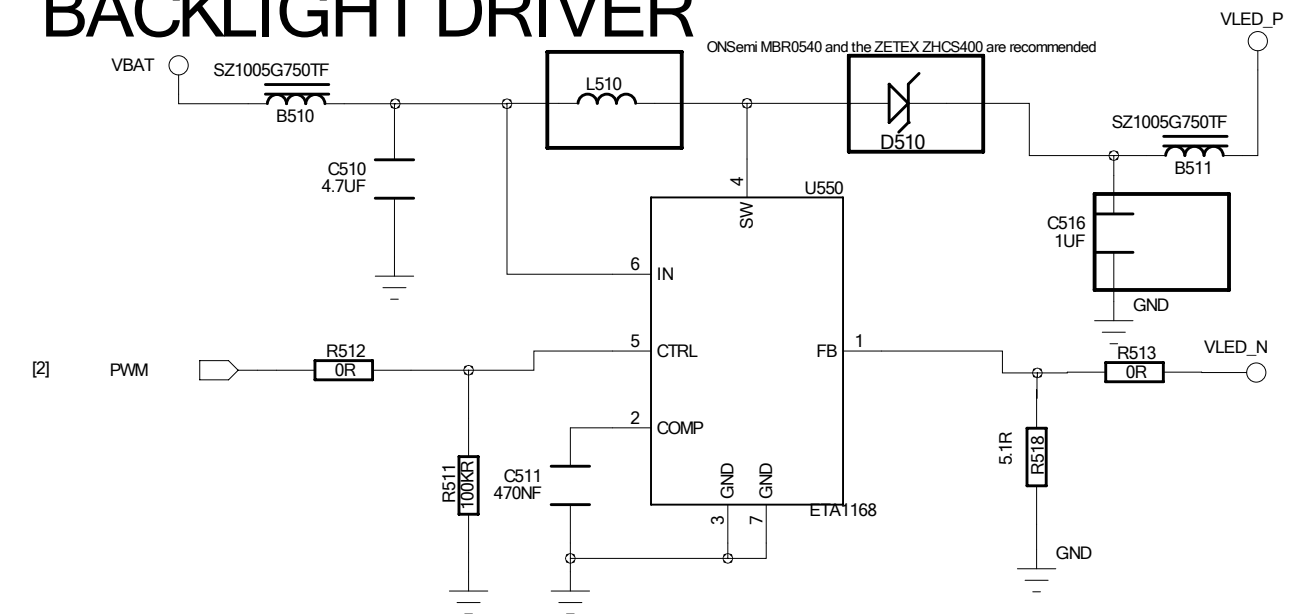
eMMC+LPDDR2

186 Ball, 0.5mm pitch

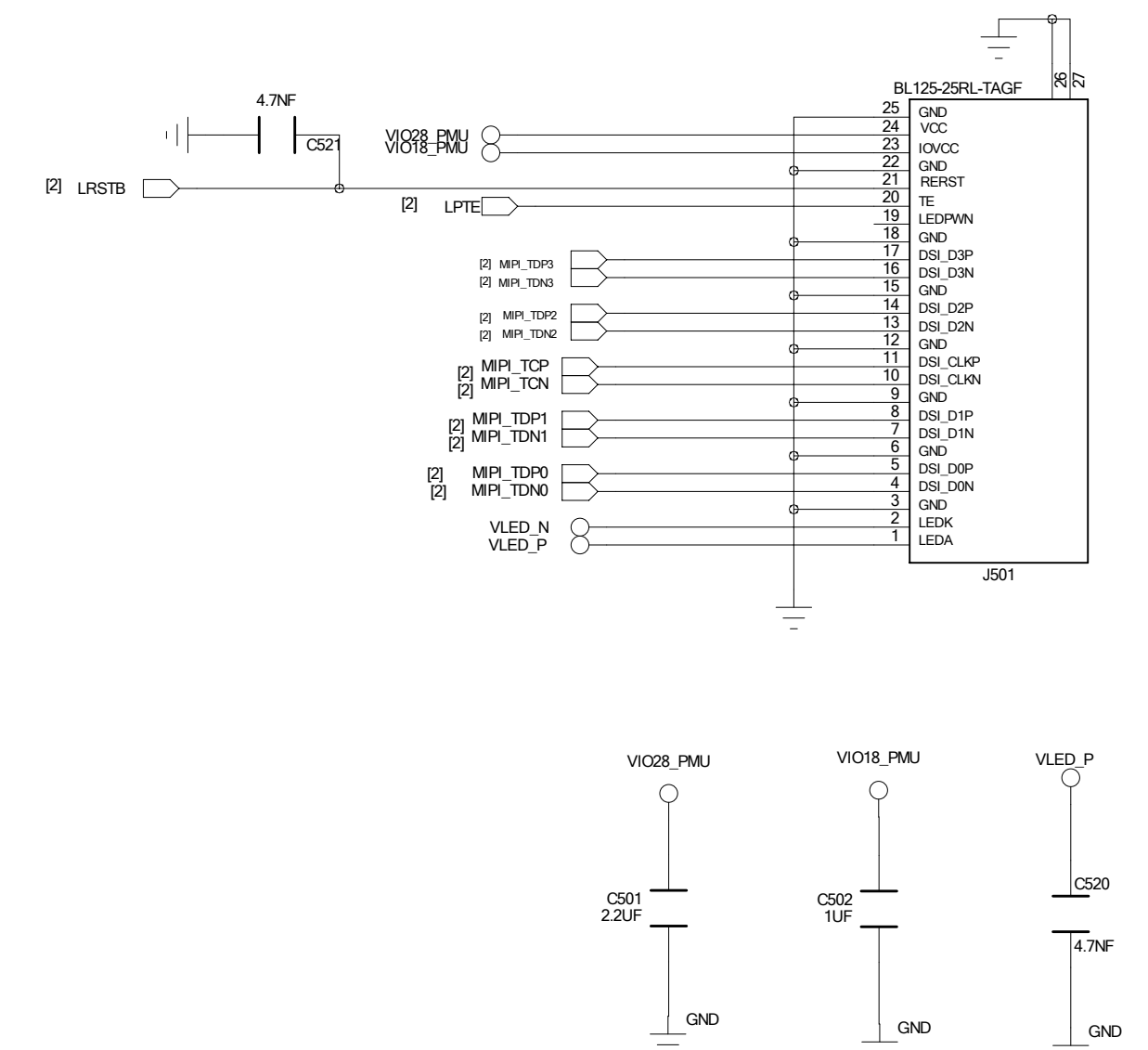
VDD1=1.8V
VDD2=1.20V
VDDCA=1.2V
VDDQ= 1.20V



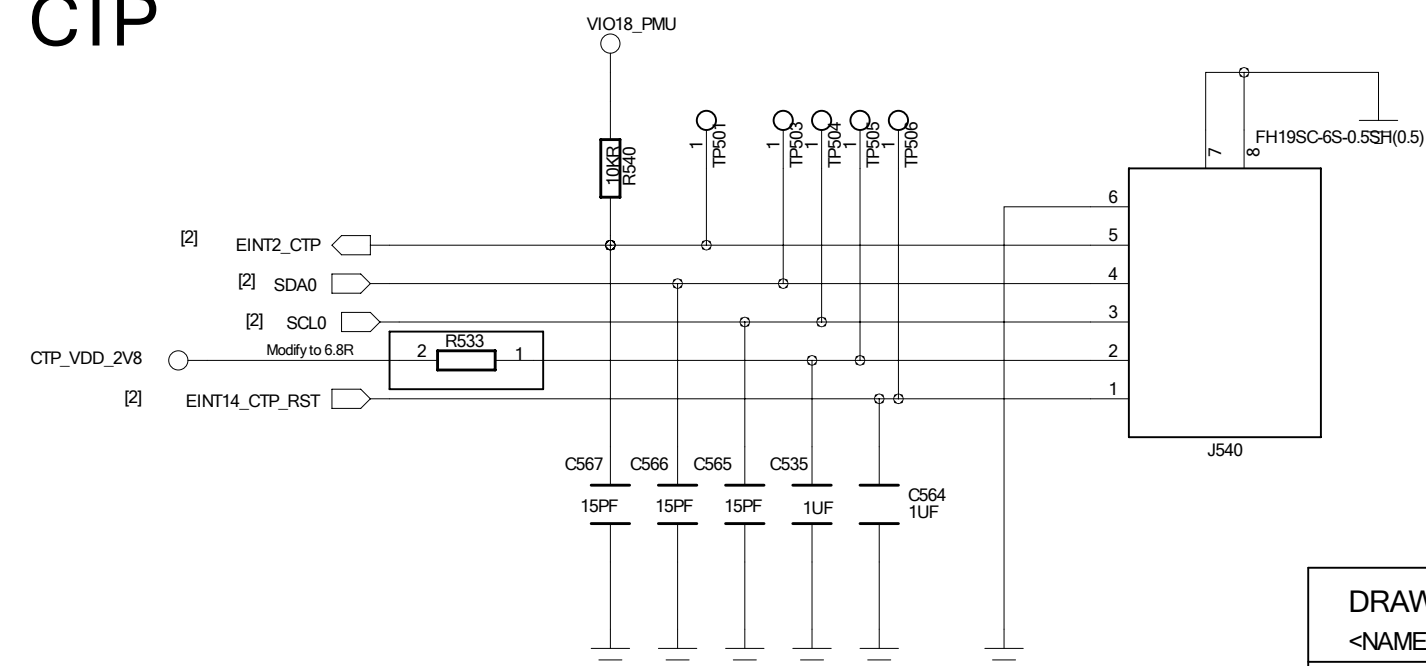
BACKLIGHT DRIVER



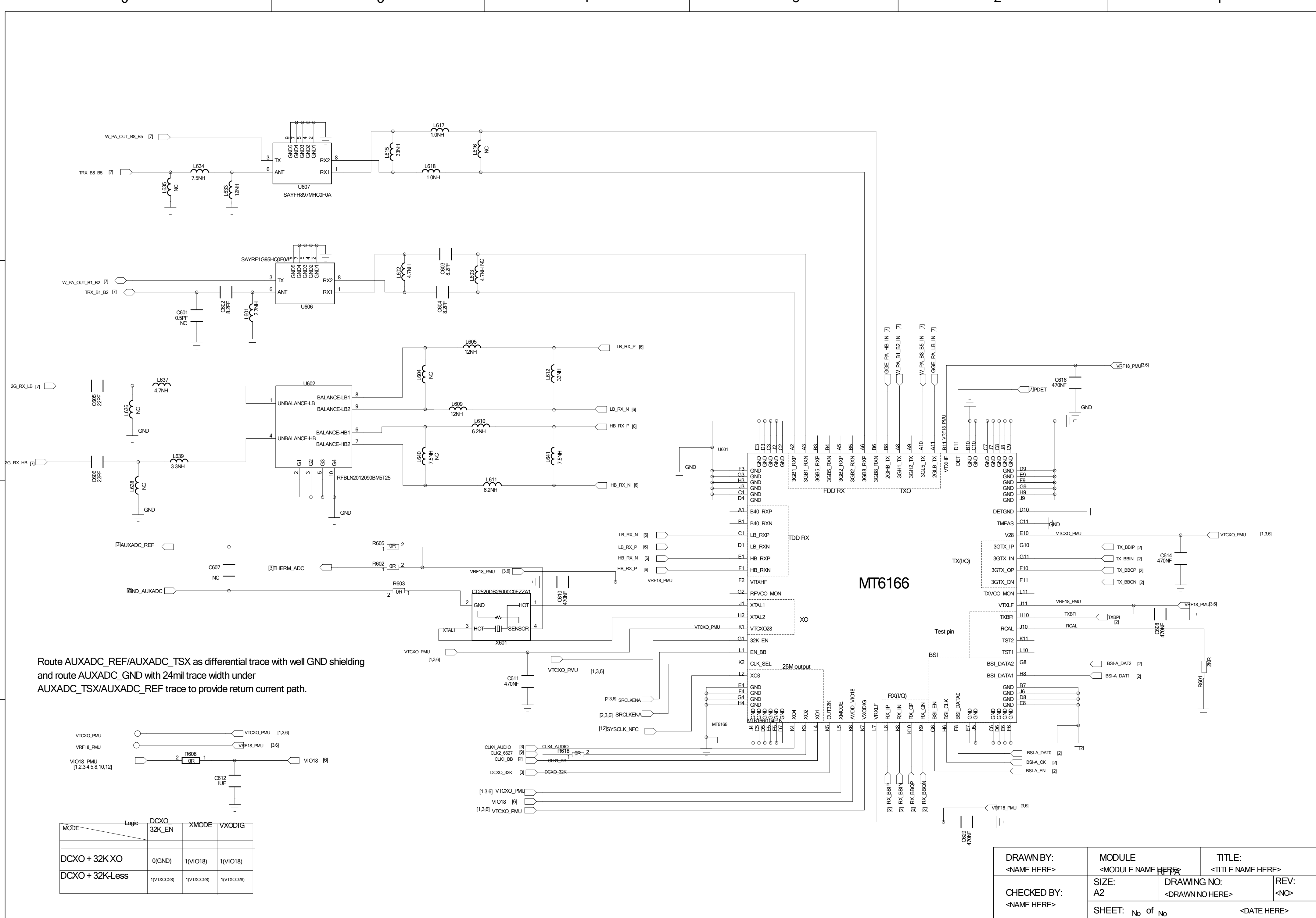
LCM



CTP



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	SHEET: No of No <DATE HERE>		



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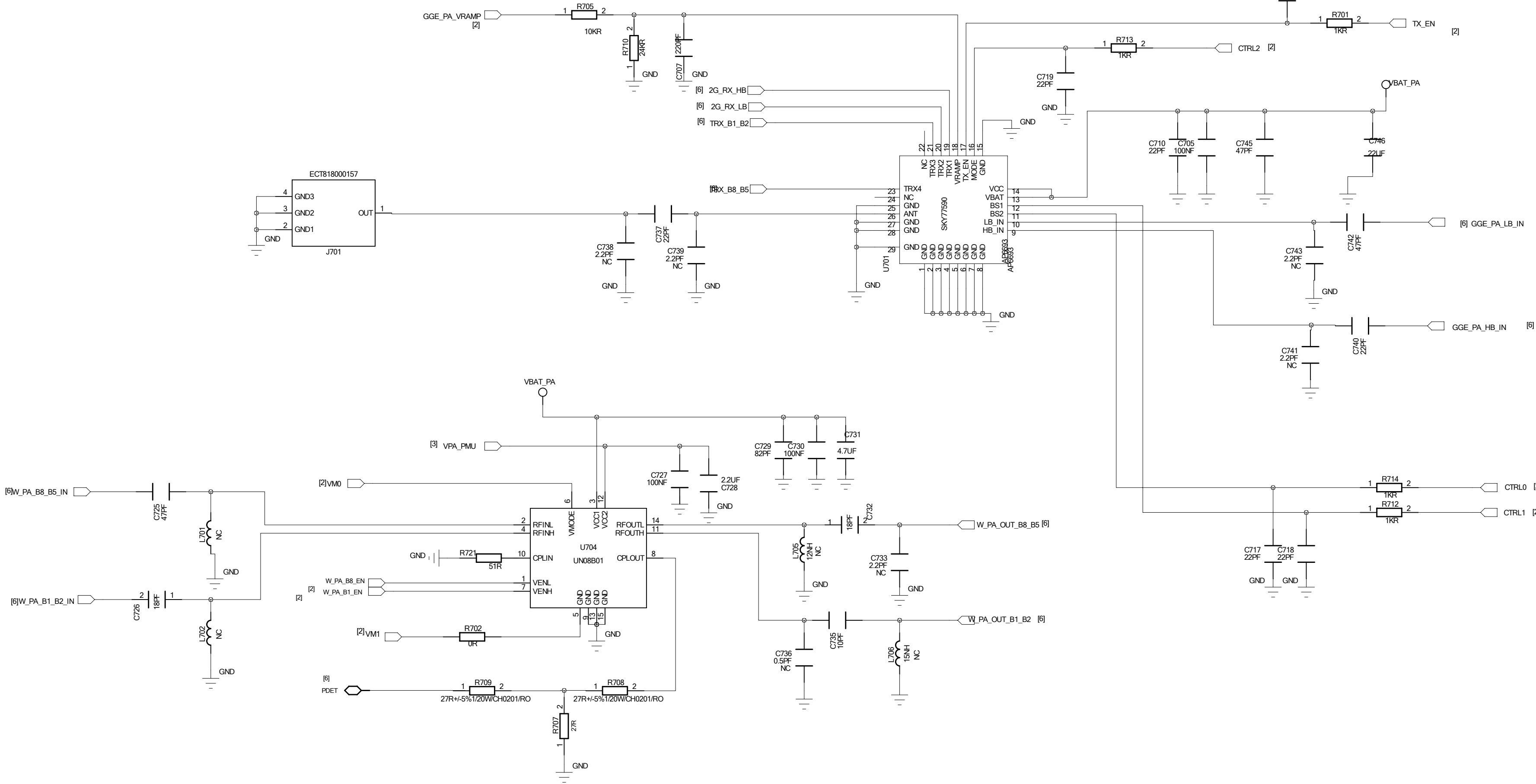
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SHEET: No of No		<DATE HERE>	

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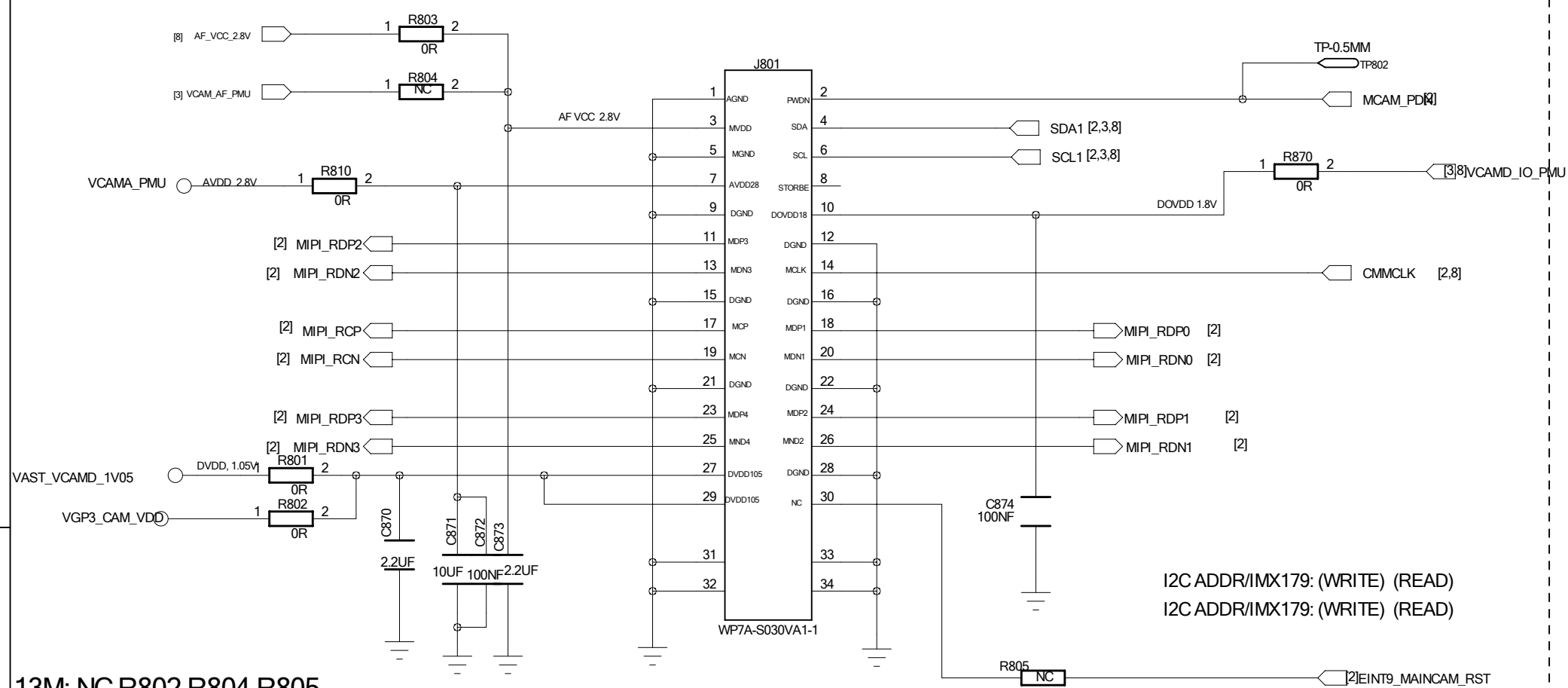
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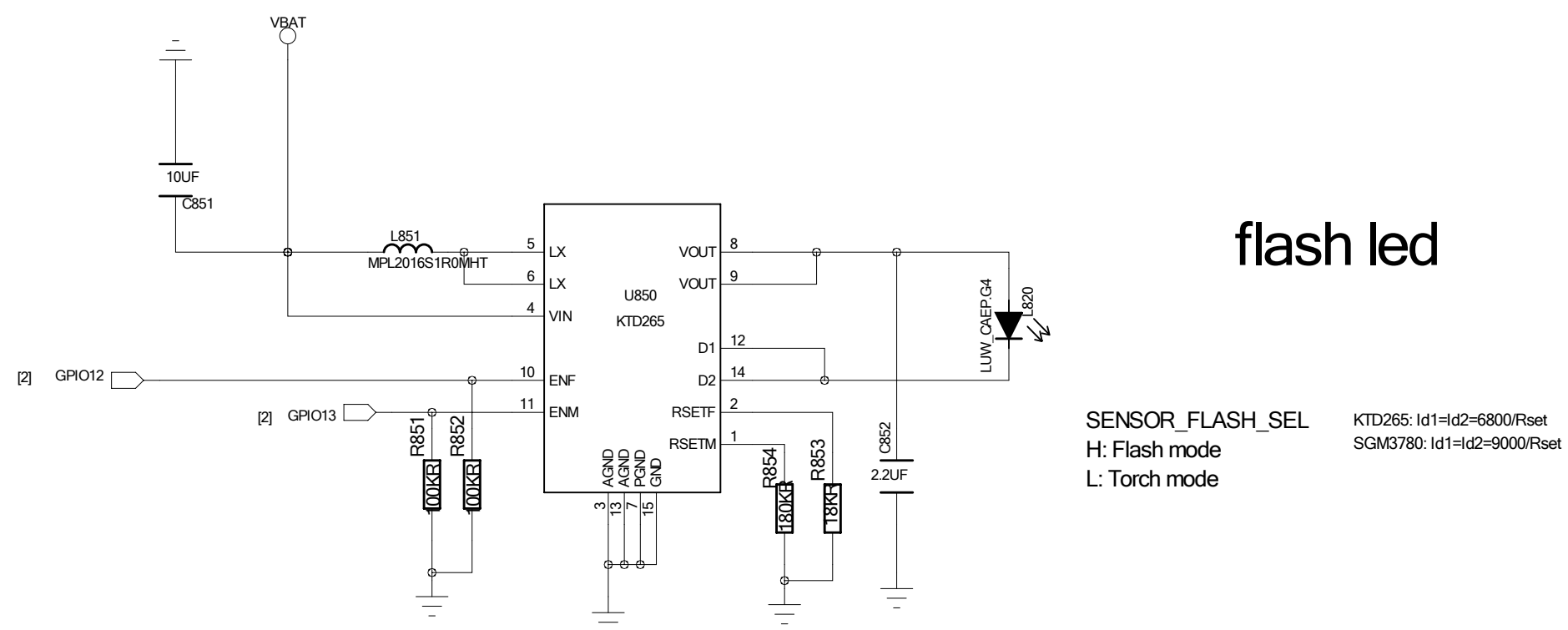
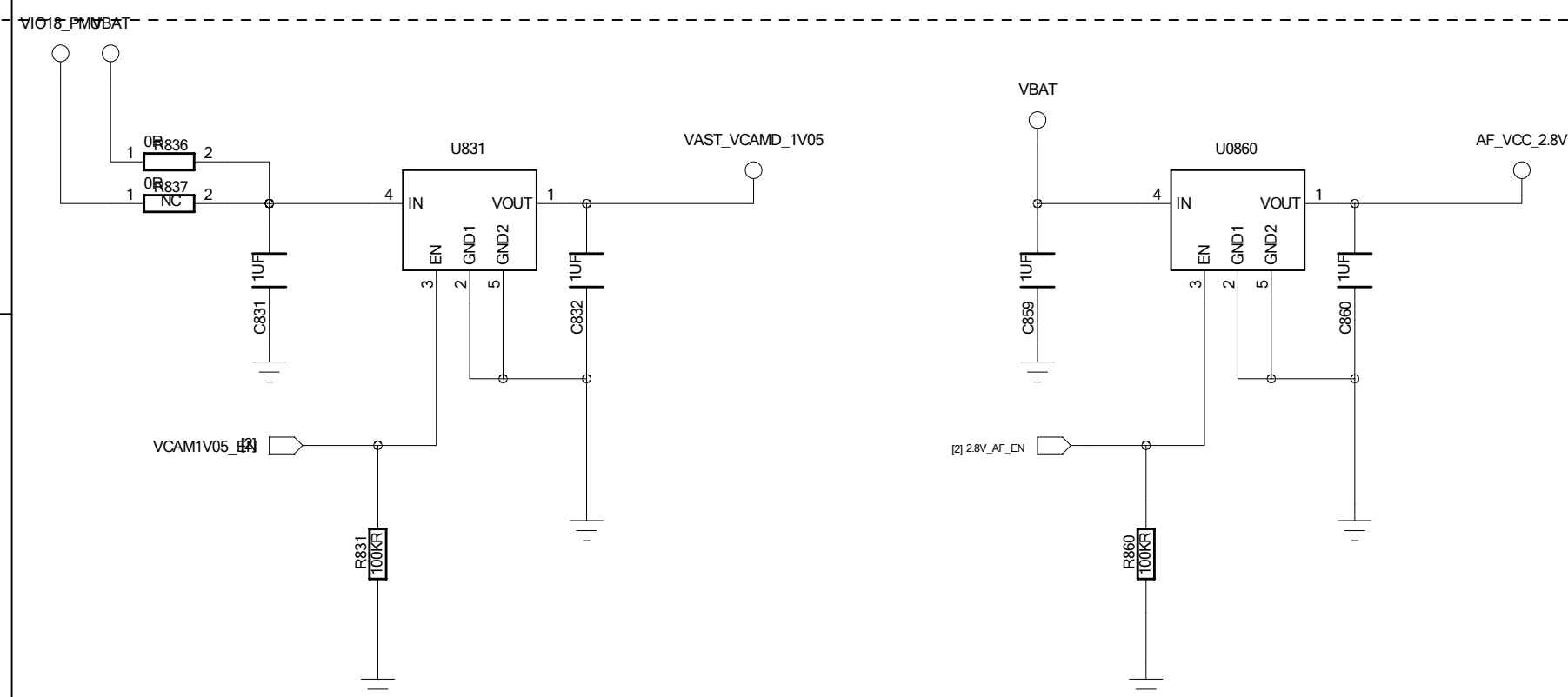
2

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Main Camera



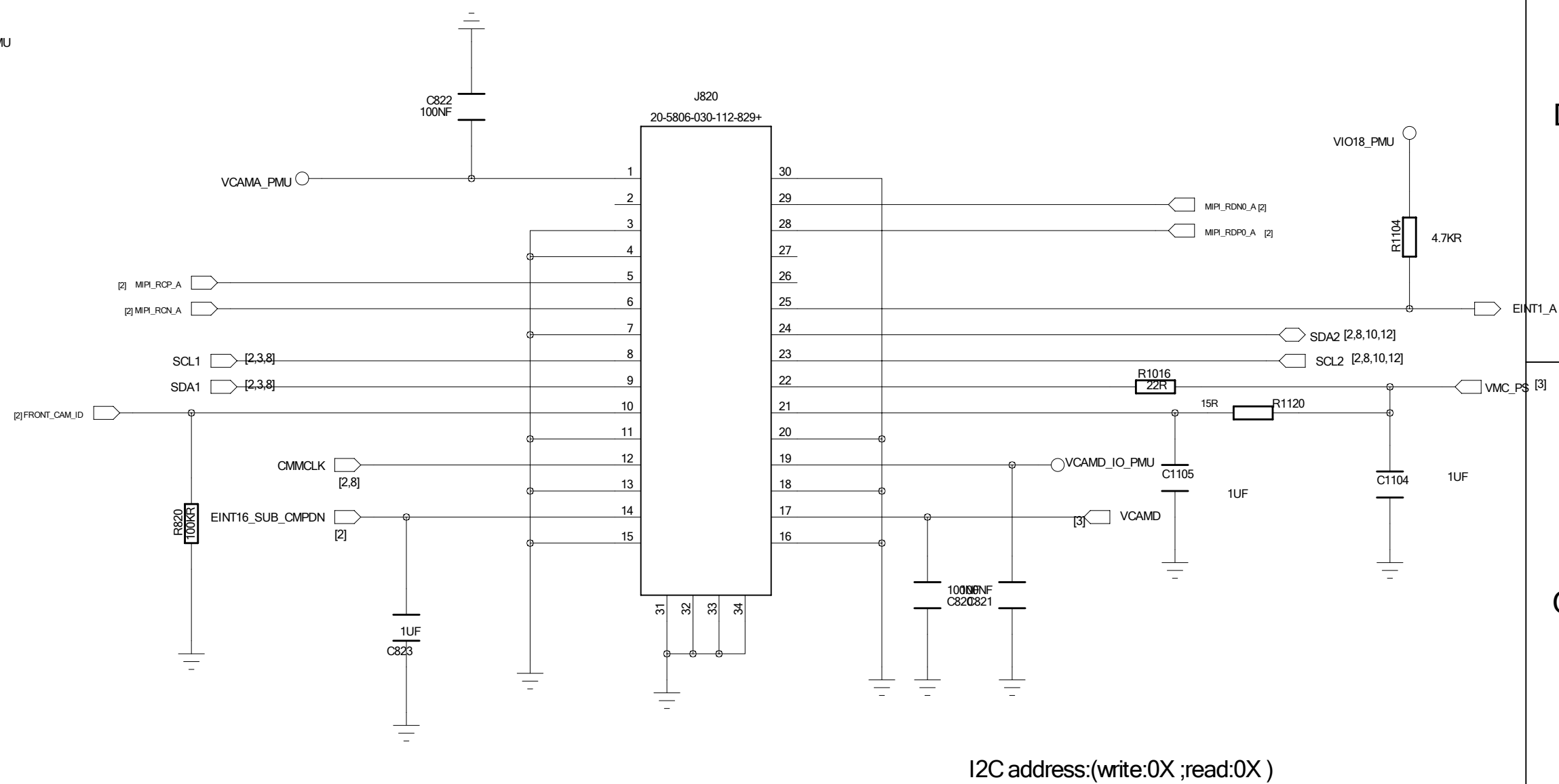
13M: NC R802,R804 R805
8M: NC R801,R803



flash led

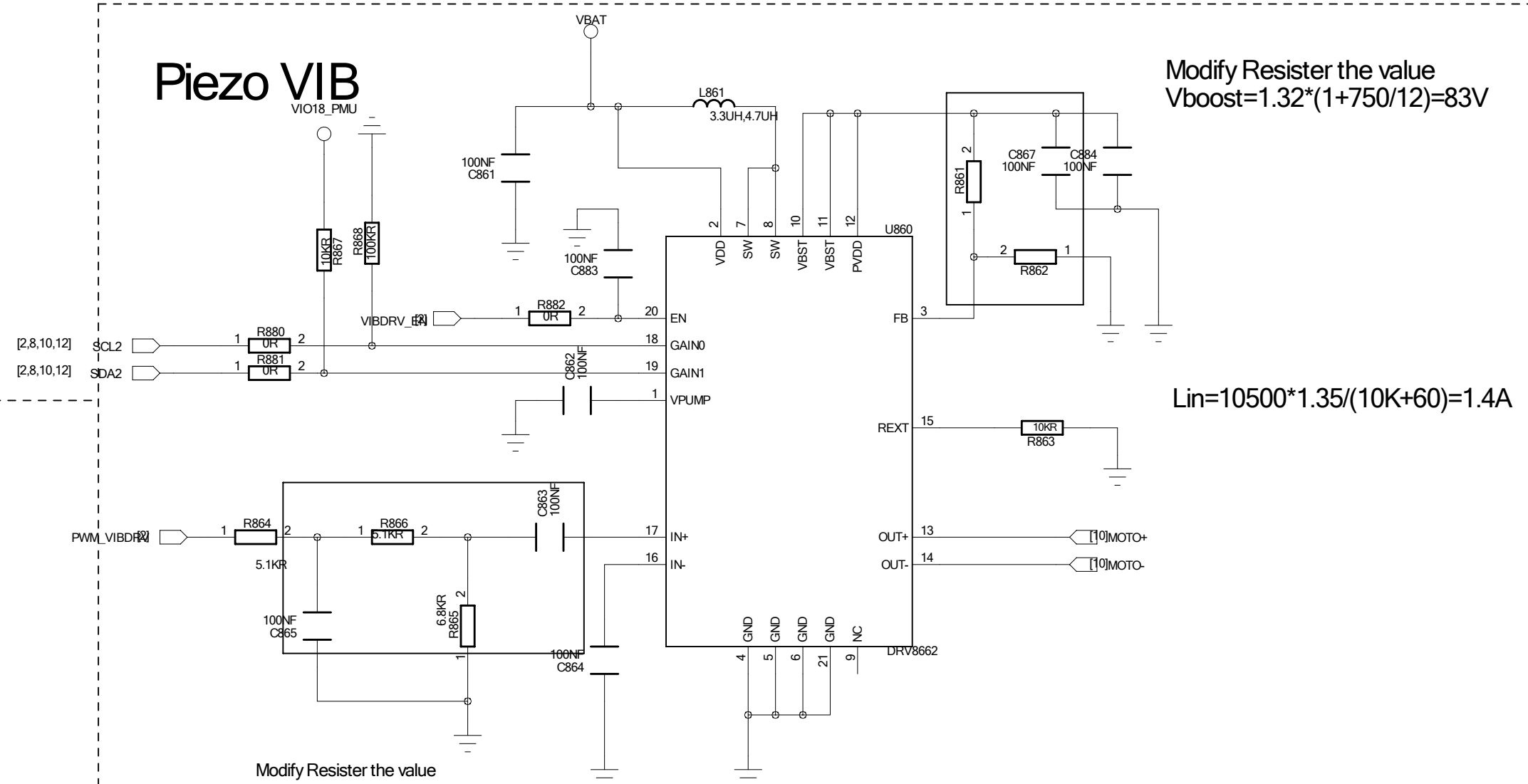
SENSOR_FLASH_SEL	KTD265: Id1=Id2=6800/Rset
H: Flash mode	SGM3780: Id1=Id2=9000/Rset
L: Torch mode	

SUB CAM/PS/ALS CON



I2C address:(write:0X ;read:0X)

Piezo VIB



Modify Resister the value
 $V_{boost} = 1.32 * (1 + 750/12) = 83V$

$$I_{in} = 10500 \times 1.35 / (10K + 60) = 1.4A$$

DRAWN BY: <NAME HERE>	MODULE <MODULE NAME HERE>	TITLE: <TITLE NAME HERE>	
CHECKED BY: <NAME HERE>	SIZE: A2	DRAWING NO: <DRAWN NO HERE>	REV: <NO>
	SHEET: No of No <DATE HERE>		

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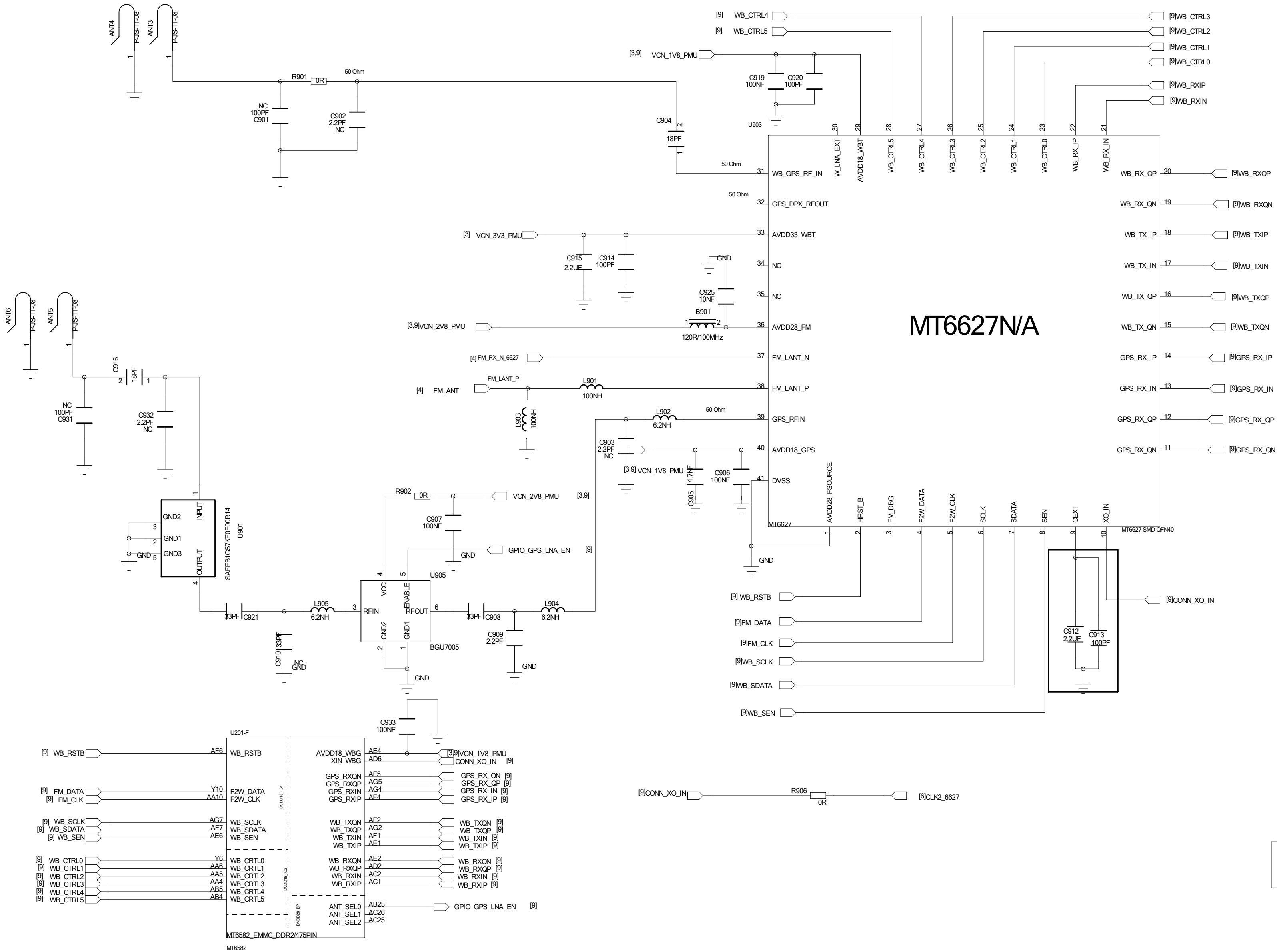
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DRAWN BY: <NAME HERE>	MODULE <MODULE NAME HERE>	TITLE: <TITLE NAME HERE>
CHECKED BY: <NAME HERE>	SIZE: A2	DRAWING NO: <DRAWN NO HERE>
	SHEET: No of No	REV: <NO>
		<DATE HERE>

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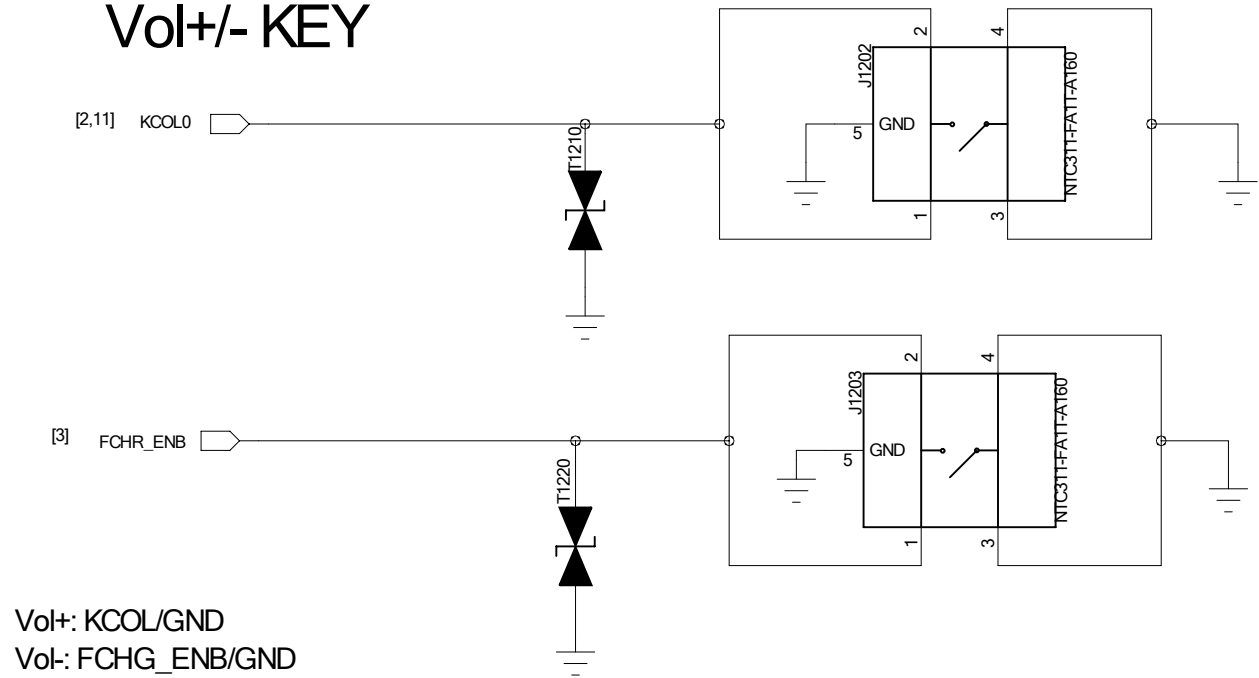
3

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1

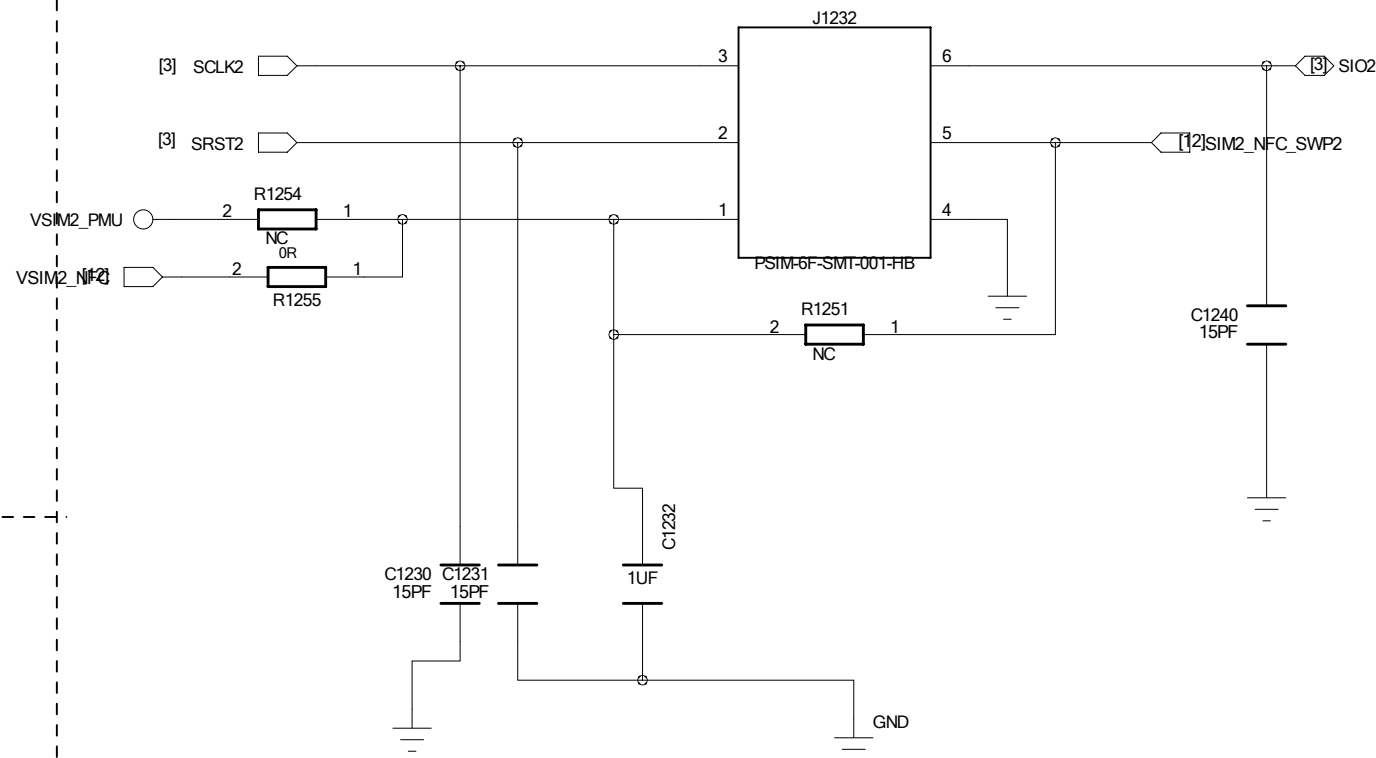
D

Vol+/- KEY



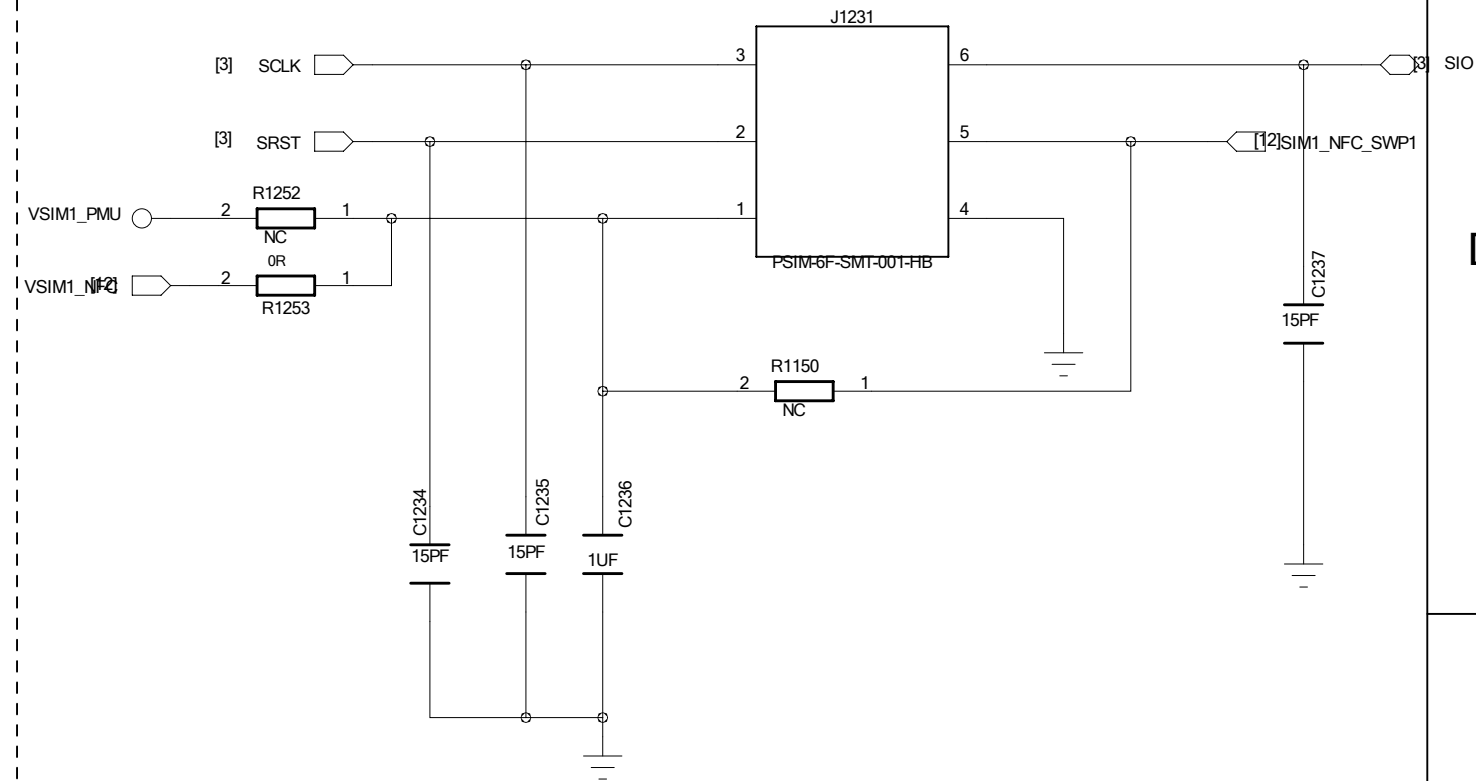
Vol+: KCOL/GND
Vol-: FCHR_ENB/GND

SIM2 Card



DET: L=NO SIM; H= INSERT CARD

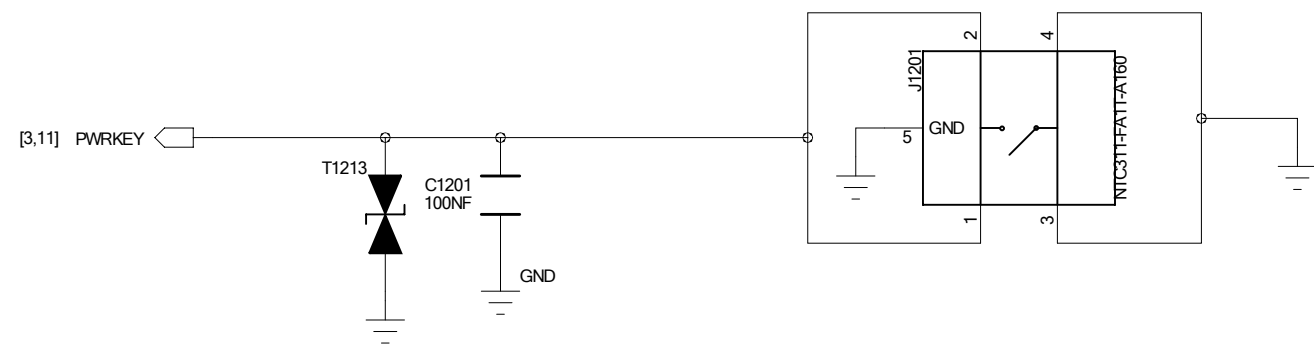
SIM1 Card



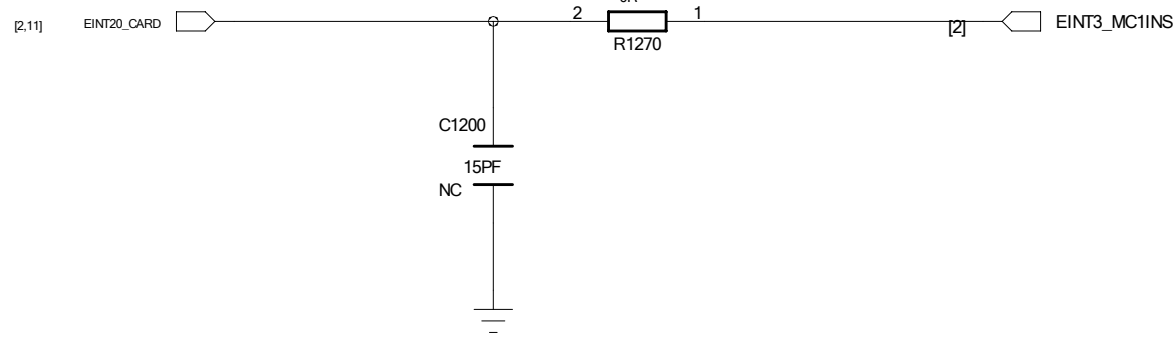
DET: H=NO SIM; L= INSERT CARD

C

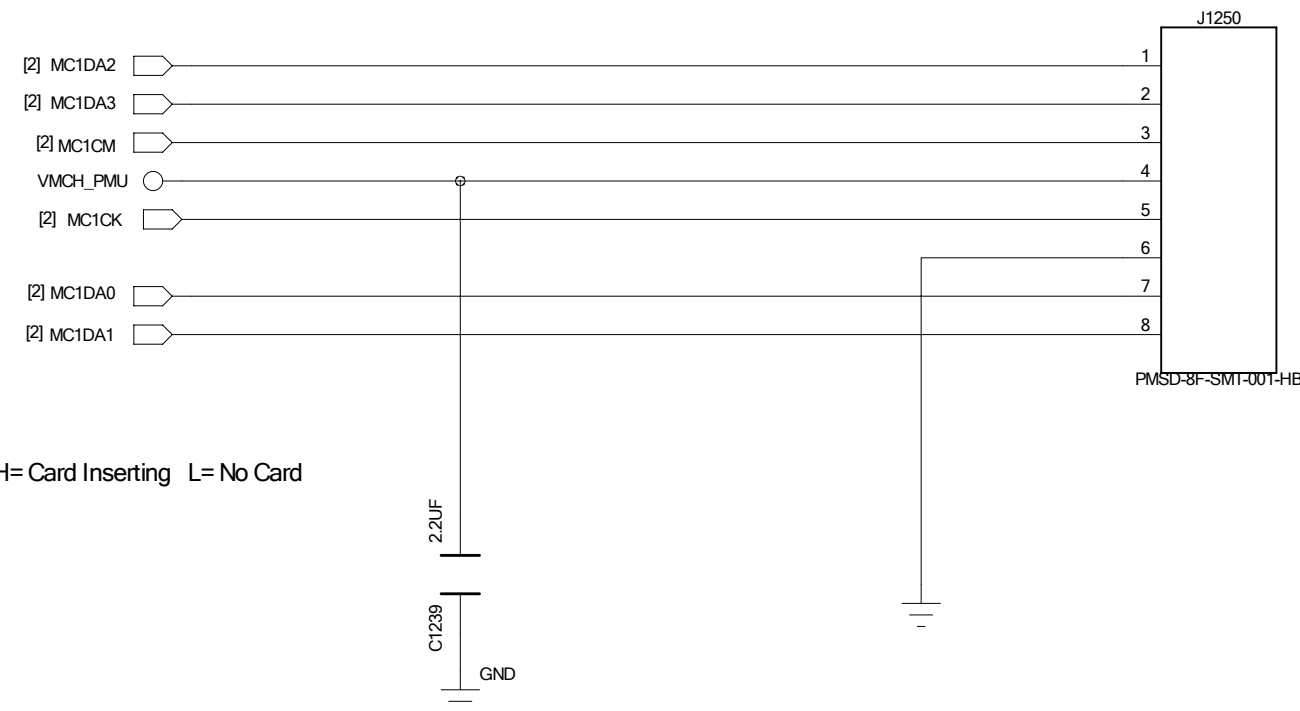
Power KEY



B



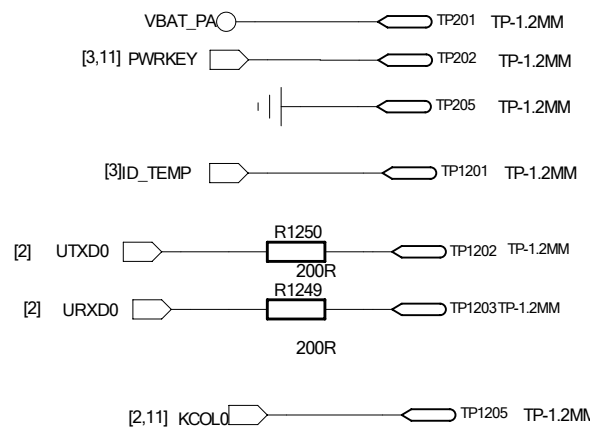
TFlash card



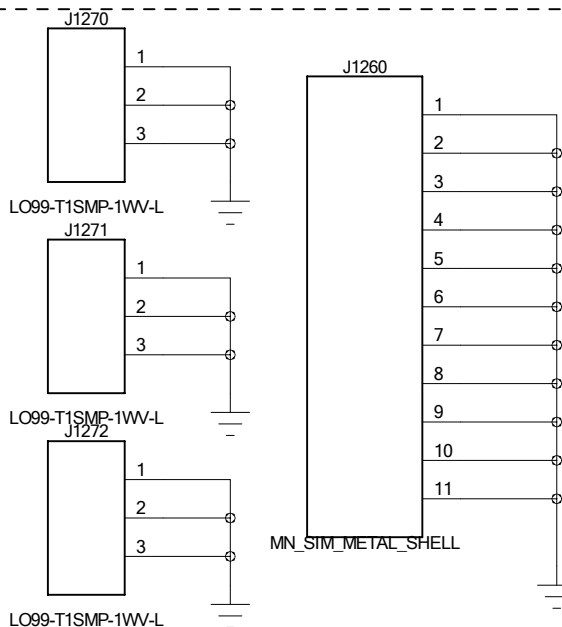
SW: H= Card Inserting L= No Card

A

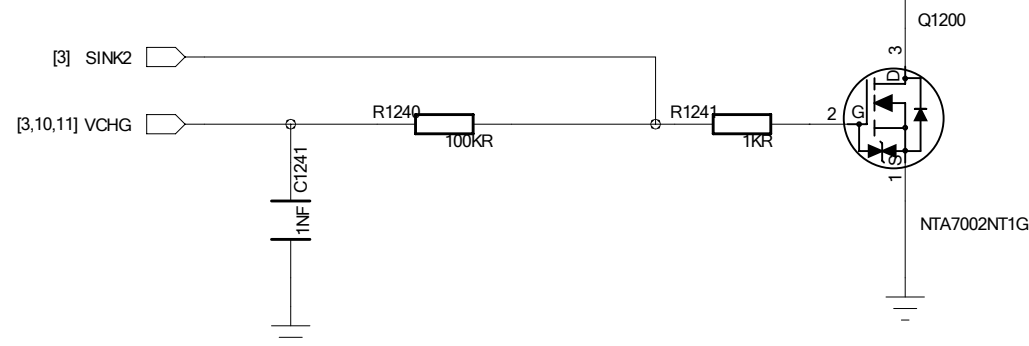
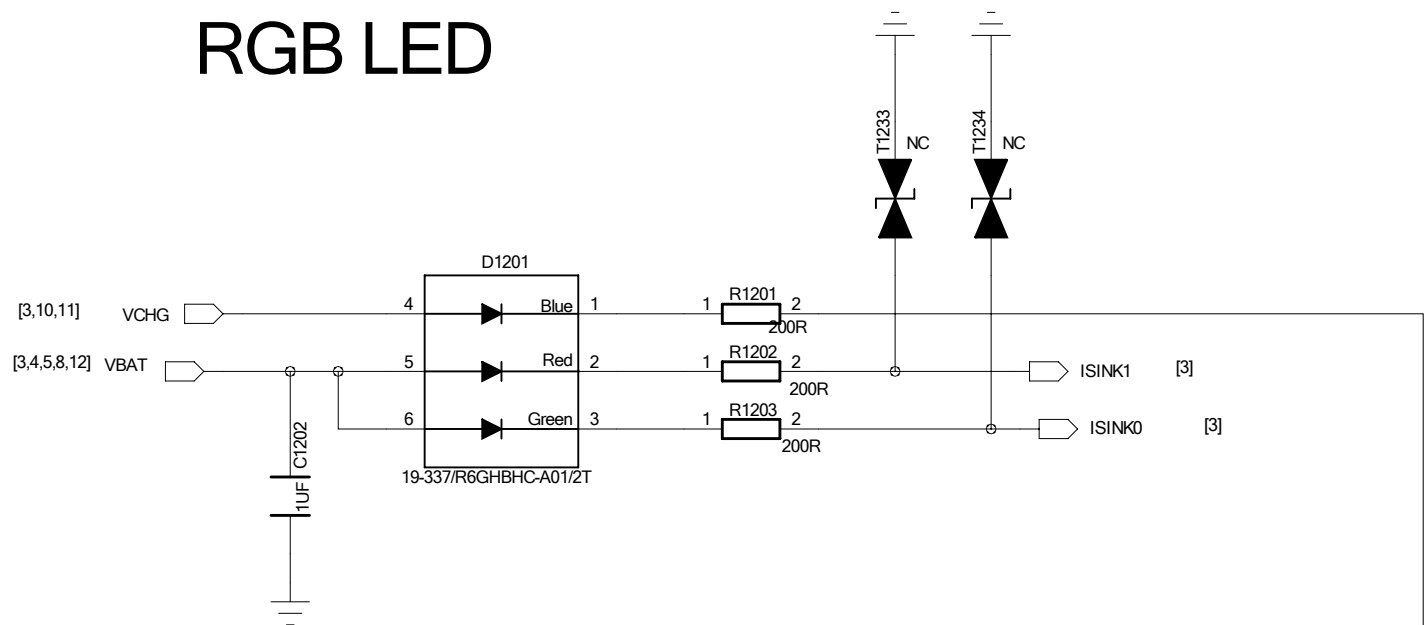
J1280
MANIFOLD_SOLID_BREP



SPRING1
8180002384



RGB LED



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CHECKED BY: <NAME HERE>	SIZEKEY & Sub A2	REVISION NO: <DRAWN NO HERE>
SHEET: No of No		<DATE HERE>

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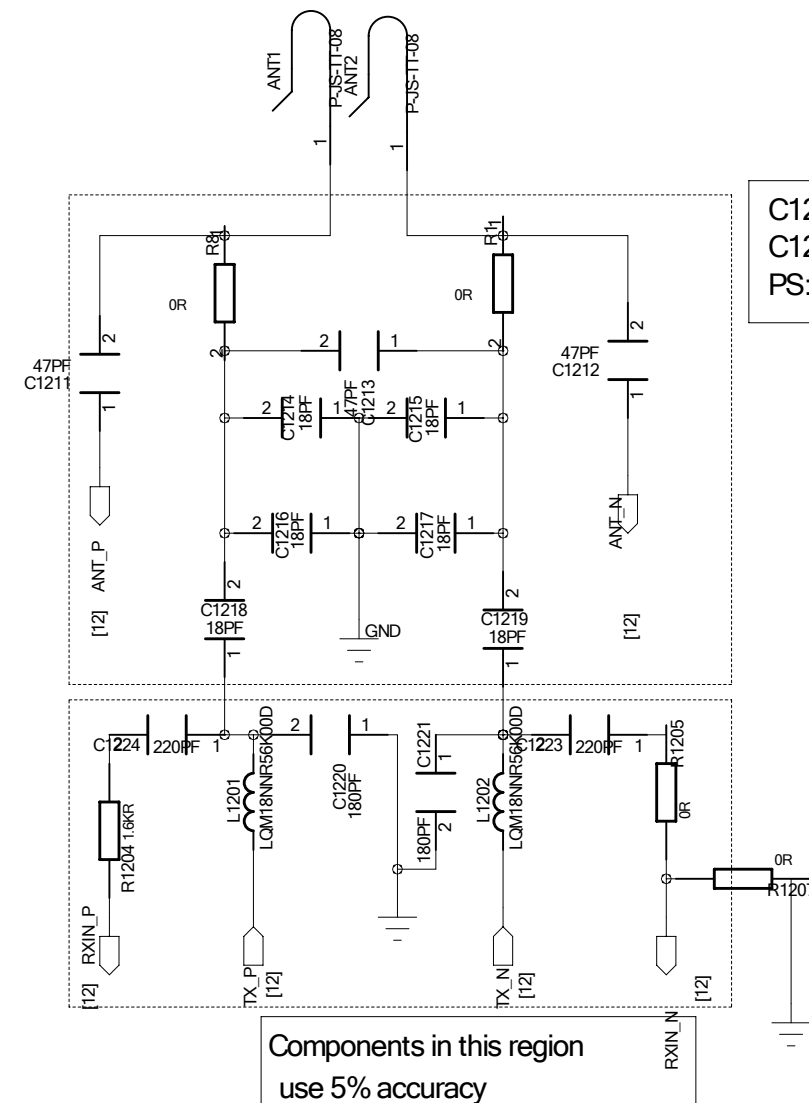
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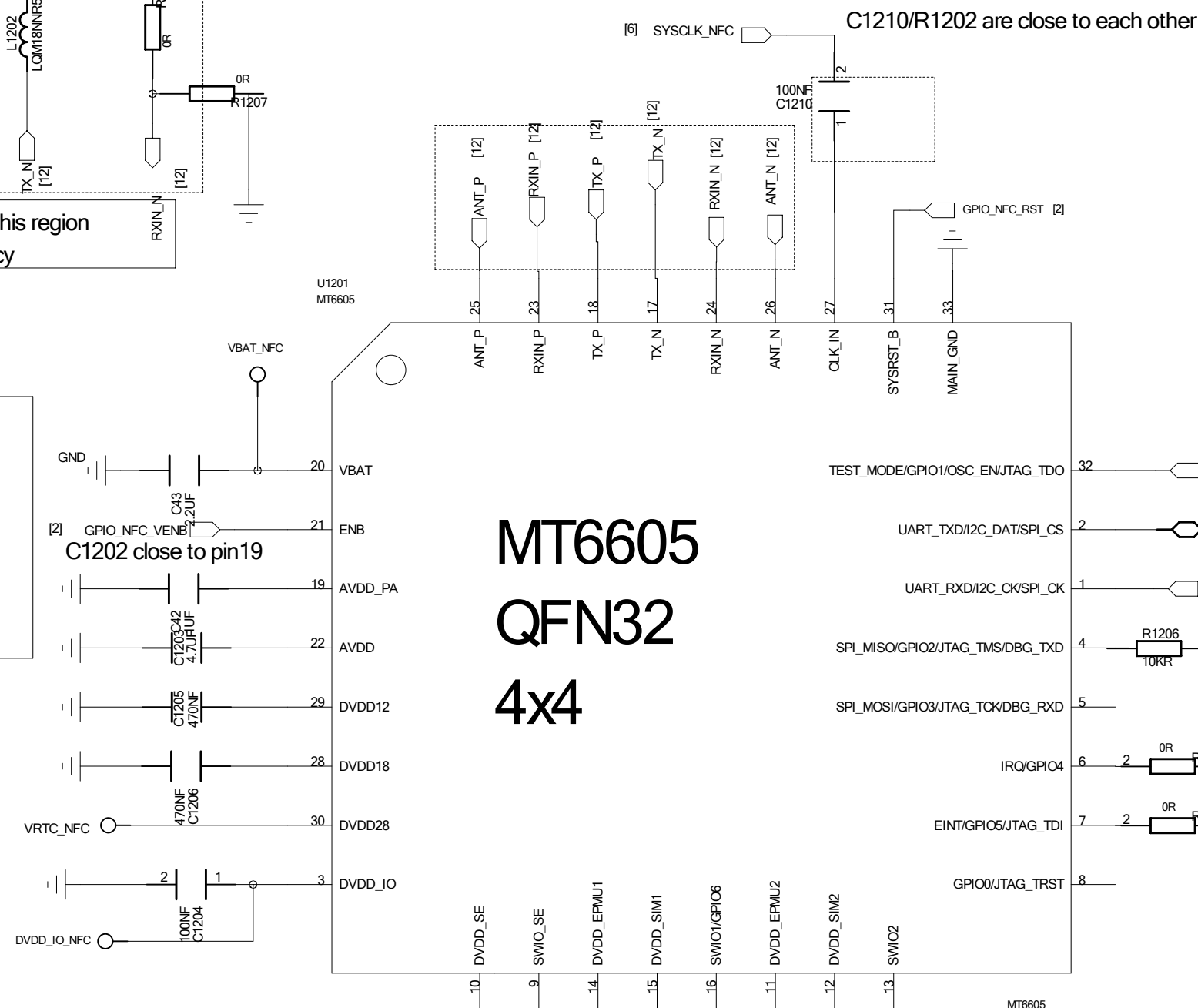
NFC MT6605



C1211,C1212,C1213,C1214,C1215,C1216,C1217, C1218,
C1219 need to use 2% accuracy and 50V tolerance capacitor,
PS: 0201 cap can't tolerance 50V

POWER MODE[1:0]=[NFC_RST:NFC_VENB]

Power Mode	NFC_RST	NFC_VENB
NFC enable (configure, R/W, card, polling loop, polling loop card listening)	1	0
NFC disable (HPD)	0	1
High battery card listening	1	1
Reset	0	0



ENB (NFC_VEN)
1.Input pin
2.Internal pull low
3.Low active
4.If default NFC
would like to disable,
please configure to high

SYSRST_B (NFC_RST)

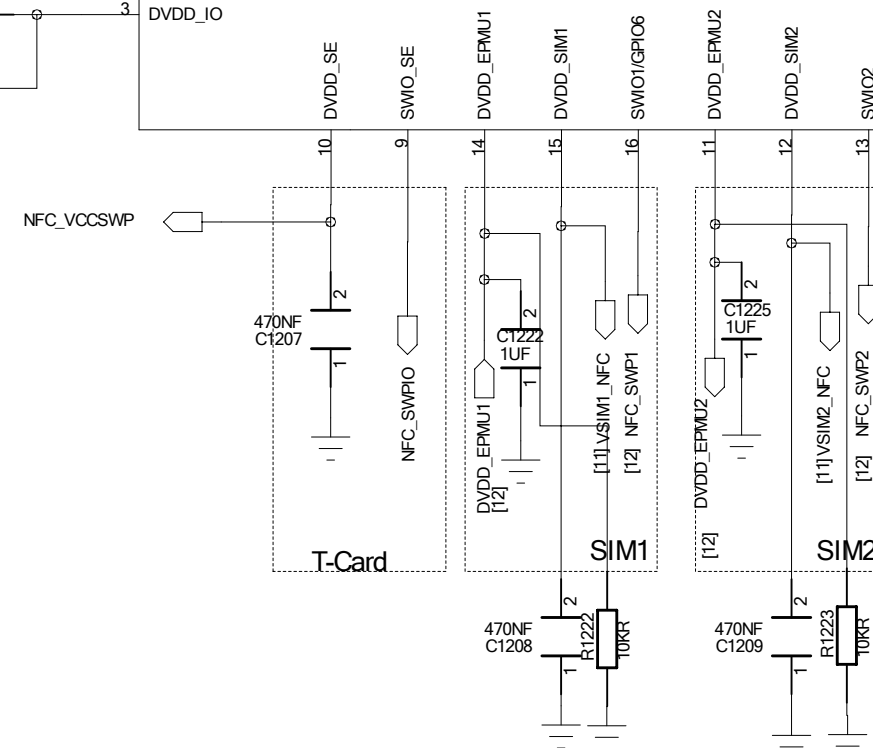
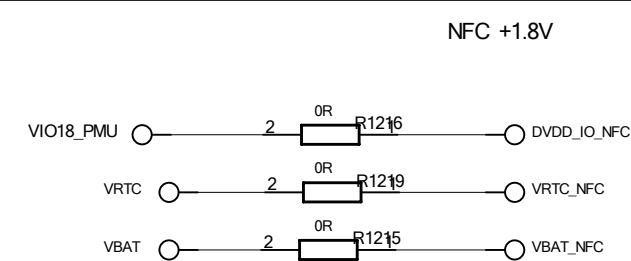
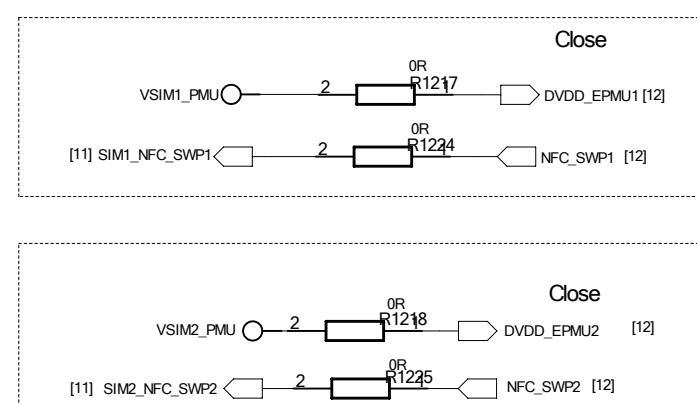
1. Input pin
2. Internal pull high
3. Low active

- 1.NFC_OSC_EN is output pin, and high active.
- 2.Need to connect to host SRCLKENAI and SRCLKENAI pin need to be default low.

Only can use HW I2C. SW I2C is not allowed.	Address:0x28
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R1206 NC : XTAL MODE
R1206 10K : Co-Clock

- 1.IRQ_NFC is output pin, and high active.
- 2.IRQ_NFC is also strap pin and host I/F connected with IRQ need to be default low.



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CHECKED BY: <NAME HERE>	SIZE: A2	DRAWING NO: <DRAWN NO HERE>		REV: <NO>
	SHEET: No of No			